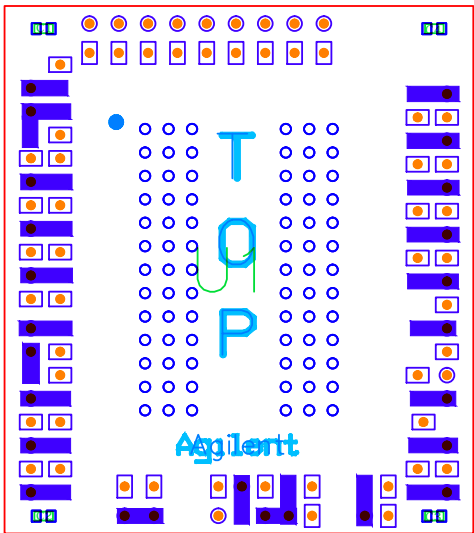


Notes:

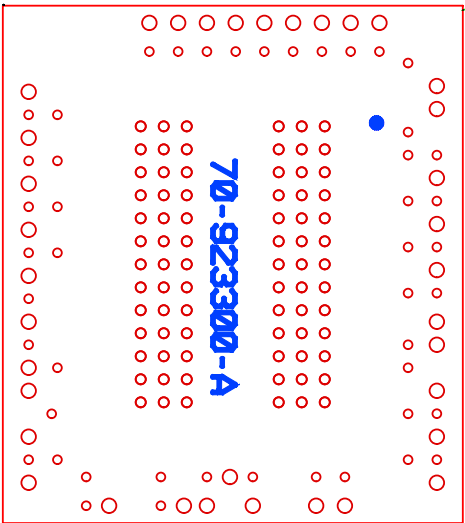
1. Use solder (tin/lead or lead free) as specified in Purchase Order.

2. Add Solder balls to "Bottom" Side

REVISIONS				
ECO #	REV	DESCRIPTION	DATE	APPROVED
C01038	A	Initial Release	12/28/12	S Wint



Top

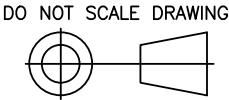


Bottom

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UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES
[MILLIMETERS BRACKETED FOR REF.]
TOLERANCES
DECIMALS .XX ± .010" ANGULAR FRACTIONAL .XXX ± .004" ± 1' ± 1/16"

MAT'L. SEE NOTES



DRAWN
T Bhattacharya

CHECKED
S Wint



5055 CORPORATE PLAZA DRIVE
Colorado Springs, CO 80919 U.S.A.
Ph: 719.260.1625 www.astekcorp.com

DATE 12/28/12 DWG NAME 70-923200 DDR4 V2 Interposer

DATE 12/28/12 SCALE Not to scale DWG NO. 70-923600 SHEET 1 OF 1 REV A