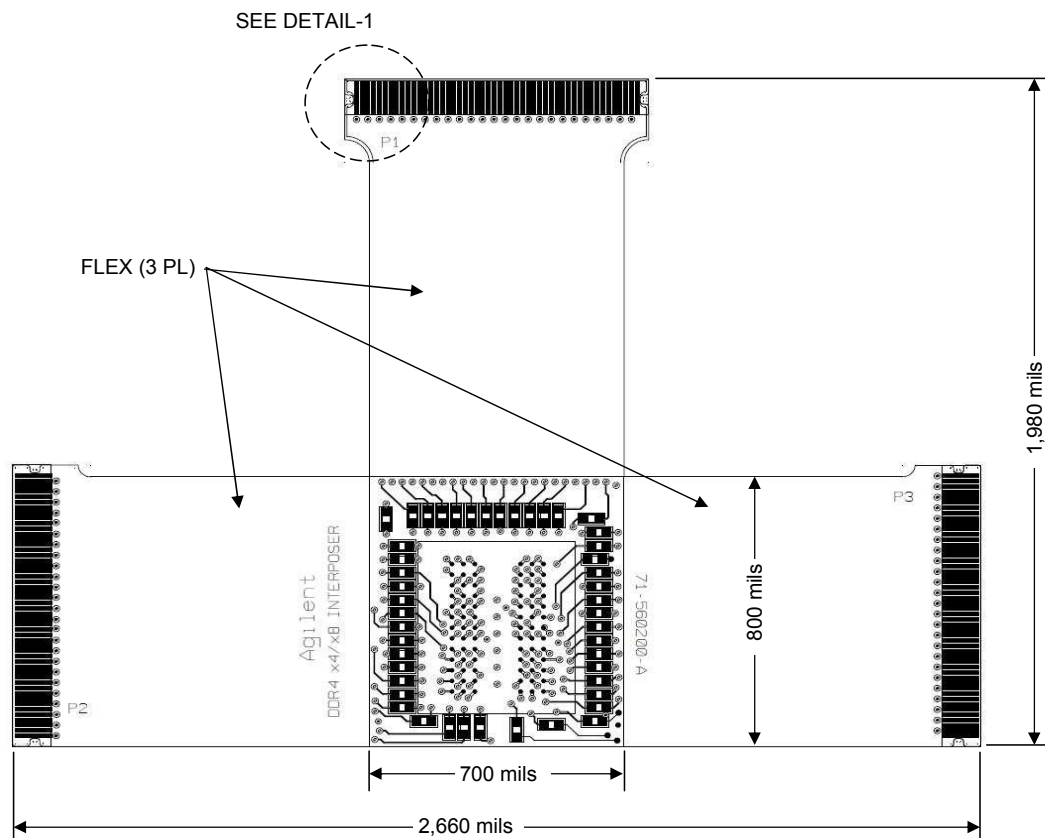




NOTES:

1. PCB SHALL BE FABRICATED TO IPC-6011 AND 6012 CLASS 2 OR BETTER
2. MATERIAL AS DEFINED IN STACK-UP
3. FINISHED THICKNESS: 0.062 +/- 0.006"
4. FINISH: SOFT GOLD (3-10 MICROINCHES) FOR SOLDER AREAS. GRADE C, CLASS 1 HARD GOLD PLATING (50 MICROINCHES MINIMUM) FOR SIGNAL FINGERS.
5. SOLDER MASK: GREEN LPI PER IPC-SM-840-C, CLASS T
SOLDER MASK CLEARANCE PADS IN GERBER DATA ARE THE SAME SIZE AS COPPER PADS. VENDOR MAY CREATE CLEARANCES TO FIT YOUR PROCESSES UP TO 0.004" CLEARANCE. SIDES: TOP AND BOTTOM LAYERS
6. SILKSCREEN: NON-CONDUCTIVE WHITE EPOXY BASED INK; NOT ALLOWED ON COMPONENT PADS OR IN MOUNTING HOLES.
7. HOLE SIZES AFTER PLATING AND FINAL CONDUCTOR FINISH SHALL BE PER DRILL TABLE ON DRILL DRAWING. CENTERLINE OF HOLES SHALL BE WITHIN 0.005" RADIAL OF EXACT CENTERLINE OF THE PAD ON THE ARTWORK.
8. 100% TESTING FOR OPENS AND SHORTS REQUIRED.
9. PERFORM GERBER FILE DATABASE DESIGN RULE CHECK.
10. IMPEDANCE REQUIREMENTS AS SHOWN IN LAYER STACK-UP DRAWING.
VENDOR MAY ADJUST TRACE SIZE OR LAYER THICKNESS AS NECESSARY TO ACHIEVE THE IMPEDANCE REQUIREMENTS.
11. VENDOR MAY ALTER PHOTO DATA (GERBER) TO COMPENSATE FOR ETCHING PROCESS
12. ALL IMPEDANCE COUPONS MUST BE SERIALIZED TO INDICATE WHICH BOARD THEY CORRESPOND TO. IMPEDANCE COUPONS TO BE SUPPLIED WITH BOARDS.
13. BURIED RESISTOR VALUE: 100 OHMS +/- 20%; TIGER 50 OHMS PER SQUARE
DELIVER BURIED RESISTOR MEASUREMENT DATA WITH DATA PACKAGE
14. SOLDERABILITY TEST, CATEGORY 2 OF J-STD-003 OR STRESSED MICROSECTION
15. SAFETY REQUIREMENT: MATERIAL TO BE UL94V-0 OR EQUIVALENT
MARK FLAMMABILITY RATING IN COPPER OR SILK (ON FLEX OK)
16. CRITICAL DIMENSION
16. SOLDERMASK OVER GOLD (SMOG) AND SOLDERMASK OVER BARE COPPER (SMOBC) ARE ACCEPTABLE



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PART: 71-560300

REV A

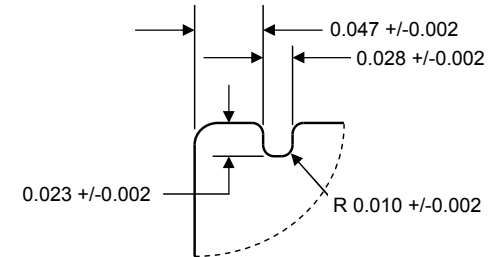
DATE: 2/6/2013

SHEET 1

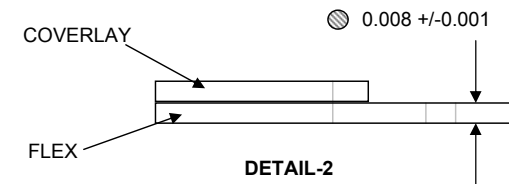
OF 3

Astek Corporation
P/N: 71-560300-A

			Before Press	After Press	
Top					
(rigid)	N7000-2		0.0006	0.0014	1/2 OZ CU + plating
			0.005	0.005	POLY
GND			0.0006	0.0006	1/2 OZ CU
	67N		0.0033	0.003	1080 PREPREG
(rigid)	67N		0.0033	0.003	1080 PREPREG
	67N		0.0033	0.0029	1080 PREPREG
signal			0.0006	0.0012	1/2 OZ CU / Impedance Controlled
(rigid)	N7000-2		0.0035	0.0035	POLY
	67N		0.0033	0.003	1080 PREPREG
	67N		0.0033	0.0029	1080 PREPREG
coverlay					
signal			0.0003	0.0006	1/4 OZ CU / Impedance Controlled
(flex)			0.005	0.005	PYRALUX
GND			0.0003	0.0006	1/4 OZ CU + plating
coverlay					
	67N		0.0033	0.003	1080 PREPREG
(rigid)	67N		0.0033	0.0029	1080 PREPREG
TICER 50 OHM / SQ.			0.0006	0.0006	1/2 OZ CU / TICER FOIL / R = 100 Ohms +/- 20 %
(rigid)	N7000-2		0.01	0.01	
GND			0.0006	0.0012	1/2 OZ CU / Impedance Controlled
	67N		0.0033	0.003	1080 PREPREG
GND			0.0006	0.0006	1/2 OZ CU
(rigid)	N7000-2		0.005	0.005	1080 PREPREG
Bottom			0.0006	0.0014	1/2 OZ CU + plating
			0.0597	Thickness Before Lamination	
				0.0604	Thickness Out Of Lamination



DETAIL-1
TYP 6 PL



DETAIL-2



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PART:	71-560300	REV	A
DATE:	2/6/2013	SHEET	2 OF 3

Board PN:	71-560300-A	Material:	POLY
		Finish:	Au

	Layer	Material	thickness mils	SE Zo			Drill	thickness µm	
				ohms	mils				
1	Soldermask		0.7				1-9	17.8	
	Topside		1.4	75	3.5	microstrip			35.6
2	GND plane	Poly	5.0					127.0	
		1080 Prepreg x3	0.6					15.2	
3	Signal		9.0				3-7	228.6	
			1.2	75	4.4	stripline			30.5
		Poly	3.5					88.9	
		1080 Prepreg x2	6.0					152.4	
		Coverlay						0.0	
			0.6	75	5.0	microstrip		4-5	15.2
4	Signal	Pypalux AP	5.0					127.0	
	GND plane		0.6					15.2	
		Coverlay						0.0	
		1080 Prepreg x2	6.0					152.4	
			0.6	75	3.5	stripline			15.2
6	Ticer 50 Ohm	Poly	10.0					254.0	
	GND plane		1.2					30.5	
8	GND plane	1080 Prepreg	3.0					76.2	
			0.6					15.2	
		Poly	5.0					127.0	
		Bottomside	1.4	75	3.5	microstrip			35.6
	Soldermask		0.7					17.8	

Tolerances for all Zo targets are +/-10%
Tolerance for Ticer embedded resistance is +/-20%

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PART:		71-560300	
DATE: 2/6/2013		SHEET 3 OF 3	