

Feature Differentiation: Analyzer



	N4851B	U4421A
Speed	1 Gbit	1.5 Gbit
Lanes	4	1, 2, 4
M-PHY Upgrade Path	no	yes
Base Memory	1MB	1 GB
Photo Extractor	standalone	integrated
Packet Statistics	yes	yes
Time Correlated Windows & Markers	yes	yes
Hierarchical Display w/ Packet Details	yes	yes
Triggering		
Pattern	yes	yes
Packet Data	yes	yes
Protocol Commands	yes, with >130 macros	yes, with >130 macros
Event Counter	yes	yes
Time Counter	yes	yes
Sequence Levels	16 protocol	8 protocol
Logical Branching	4-way	n-way
Error Triggering	clock, lane, ECC, CRC	clock, lane, ECC, CRC
LS/HS transition triggering	yes	yes
"Raw Data" view	no	yes
SW Applications needed to control	1	1
SW Applications needed for full stimulus/response	2	1
Probing options	Soft Touch, Flying Leads	Soft Touch, SMA breakout, Flying Leads



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Feature Differentiation: Exerciser



	N4861B	U4421A
Speed	1 Gbit	1.5 Gbit
Lanes	3	1, 2, 4
M-PHY Upgrade Path	no	yes
Base Memory	16 MV	1 GB
Max Memory	16 MV	16 GB
Image Inserter	standalone	integrated
Lane skew adjustment	no	yes
Output level adjustment	static	dynamic
Packet insertion	no	yes (new)
SW Applications needed to control	1	1
Probing options	SMA	SMA



Feature Differentiation: Analyzer



	N4851B	U4421A	Tek/MP Analy
Speed	1 Gbit	1.5 Gbit	1.5 Gbit
Lanes	4	1, 2, 4	4
M-PHY Upgrade Path	no	yes	no
Base Memory	1MB	1 GB	2 Mb
Photo Extractor	standalone	integrated	no
Packet Statistics	yes	yes	?
Time Correlated Windows & Markers	yes	yes	yes
Hierarchical Display w/ Packet Details	yes	yes	no?
Triggering			
Pattern	yes	yes	yes?
Packet Data	yes	yes	no?
Protocol Commands	yes, with >130 macros	yes, with >130 macros	no
Event Counter	yes	yes	patt. only?
Time Counter	yes	yes	patt. only?
Sequence Levels	16 protocol	8 protocol	16 states
Logical Branching	4-way	n-way	8-way
Error Triggering	clock, lane, ECC, CRC	clock, lane, ECC, CRC	no?
LS/HS transition triggering	yes	yes	no?
"Raw Data" view	no	yes	no
SW Applications needed to control	1	1	2
SW Applications needed for full stimulus/response	2	1	4
Probing options	Soft Touch, Flying Leads	Soft Touch, SMA breakout, Flying Leads	solder-in



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Feature Differentiation: Exerciser



	N4861B	U4421A	MP Stim
Speed	1 Gbit	1.5 Gbit	1.5 Gbit
Lanes	3	1, 2, 4	4
M-PHY Upgrade Path	no	yes	no
Base Memory	16 MV	1 GB	64 MV
Max Memory	16 MV	16 GB	64 MV
Image Inserter	standalone	integrated	yes
Lane skew adjustment	no	yes	yes
Output level adjustment	static	dynamic	dynamic
Packet insertion	no	yes (new)	yes
SW Applications needed to control	1	1	2
Probing options	SMA	SMA	SMA

