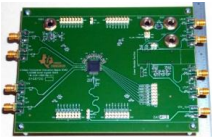
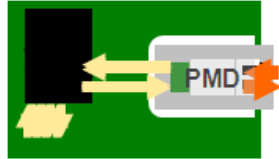
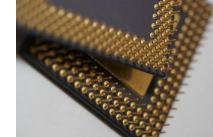
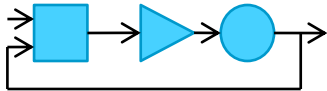


28G & 40G
ASICs/FPGAs(Embedding /
De-embedding)10 Gb Ethernet
8G/16G FC

(Electrical R&D)

Optical
Transceivers(High Volume
Manufacturing)Virtual Device
Demonstration(Demo chipsets
with offline scope)

Signal Integrity

(TDR,
S-Parameters)PLL And Clock
Analysis(Phase Noise &
Jitter Spectrum)

Research

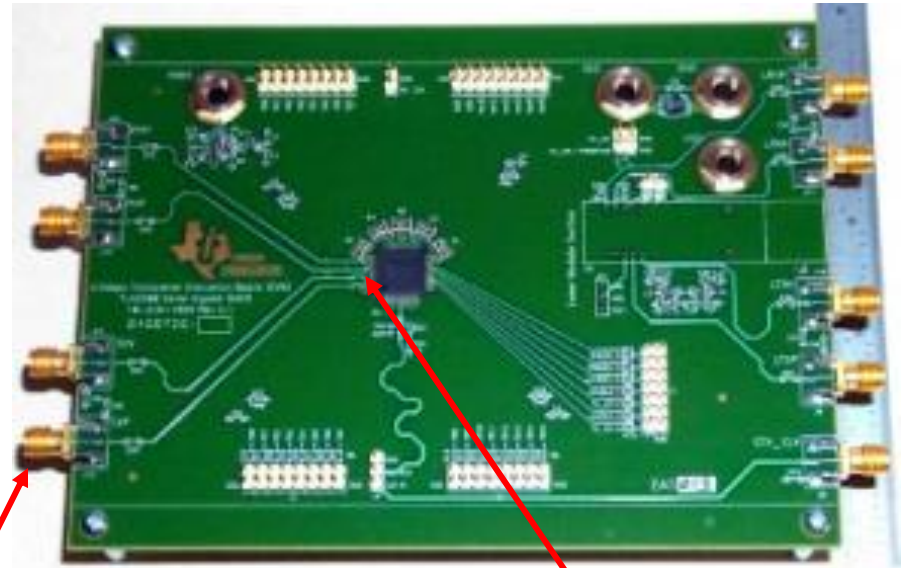


(Scope/Matlab)

Parallel Optics

(Verification &
Manufacturing)PRBS-31 /
Live Traffic(Advanced Eye
Analysis)86100D DCA-X
& Modules(Overview &
Images)*[click on application to start]*

- **IC Designers:**
See what the performance looks like inside your package, at the balls / pins of your IC
- **Serial Bus Designers:**
Add a 'virtual' cable (e.g. SAS, SATA), 'virtual' channel on a target PCB, or remove fixture effects



Measure performance here

Predict performance here

Screen capture
before/after

Base configuration (approx. \$100K):

- 86100D Mainframe (with #ETR)
- 86117A Dual 50 GHz Electrical Module
- 86107A Precision Timebase (with #040)



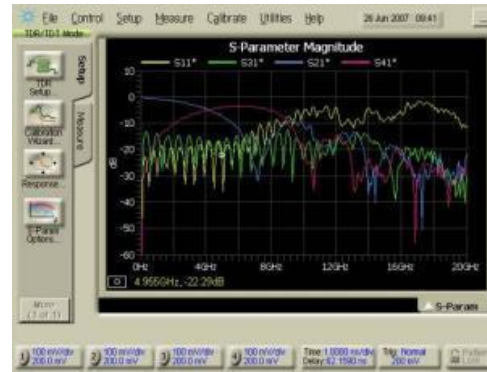
Best configuration (approx. \$150K)

- 86100D Mainframe (with #ETR)
- 86118A Dual 70 GHz Electrical Remote Heads (with #H01)
- 86107A Precision Timebase (with #040)
- 86100D-200/300 Jitter and Amplitude Analysis Software
- 86100D-201 Advanced Waveform Analysis
- 86100DU-400 PLL And Jitter Spectrum Analysis
- 86100DU-401 Advanced Eye Analysis



1. Measure S-Parameters using the DCA-X

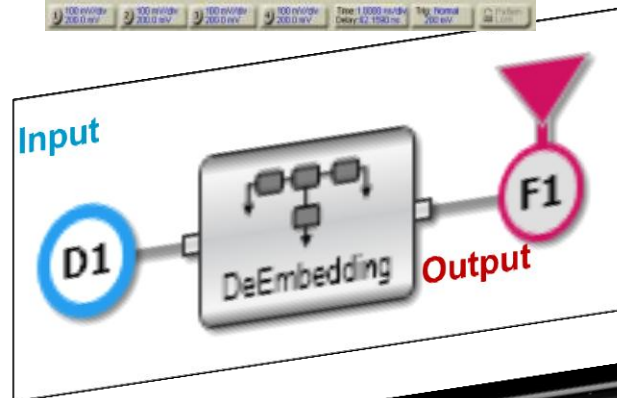
S-parameters may come from a TDR, VNA, ADS or any other source



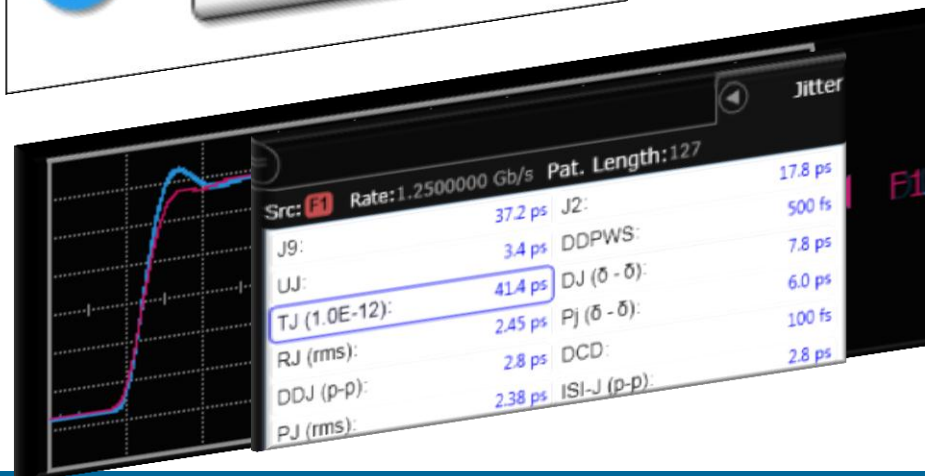
Watch the video!

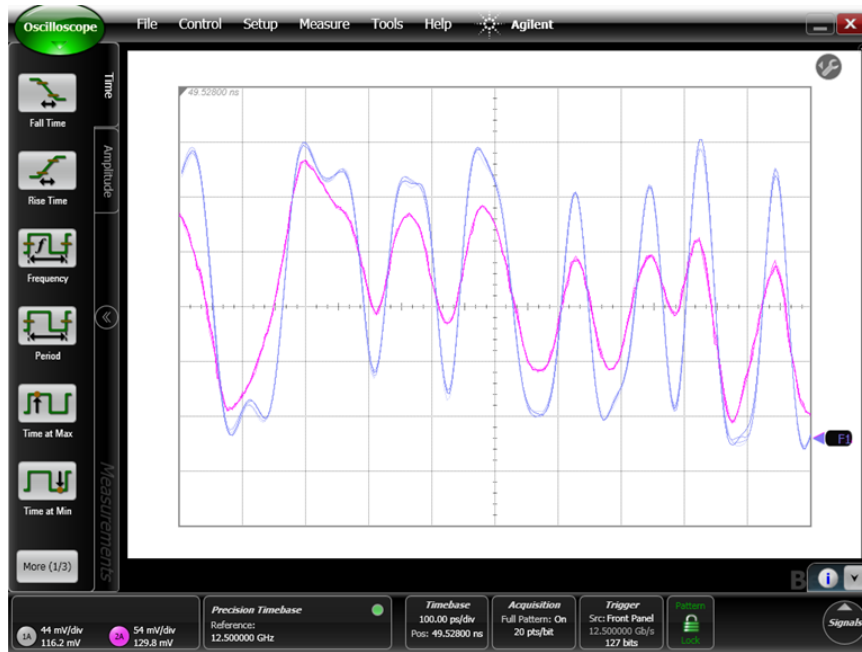
(File: N1010_DeEmbed_Overview.mp4 or <http://wireless.agilent.com/vcentral/viewvideo.aspx?vid=643>)

2. Configure your De-Embed / Embed Simulation

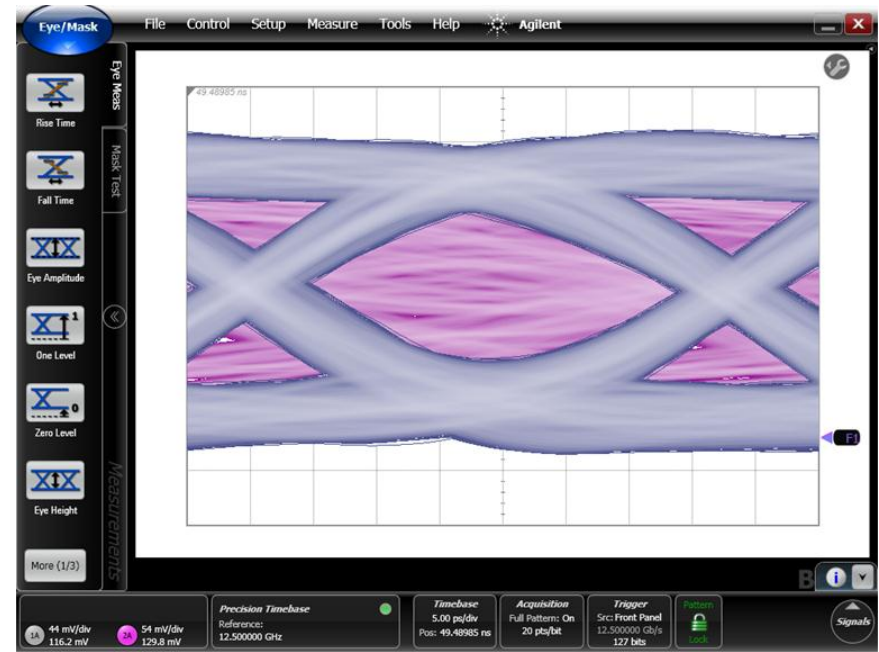


3. Analyze the results





25G waveform before (pink) and after (blue) de-embedding



25G eye before (pink) and after (blue) de-embedding

More accurate insights in shorter time help you to

- *Emulate the signal at the chip and in your customers' systems*
- *Optimize component designs for highest performance or best yield*
- *Accelerate time-to-market and get a competitive head-start*
- *Reduce time to troubleshoot issues and down times*

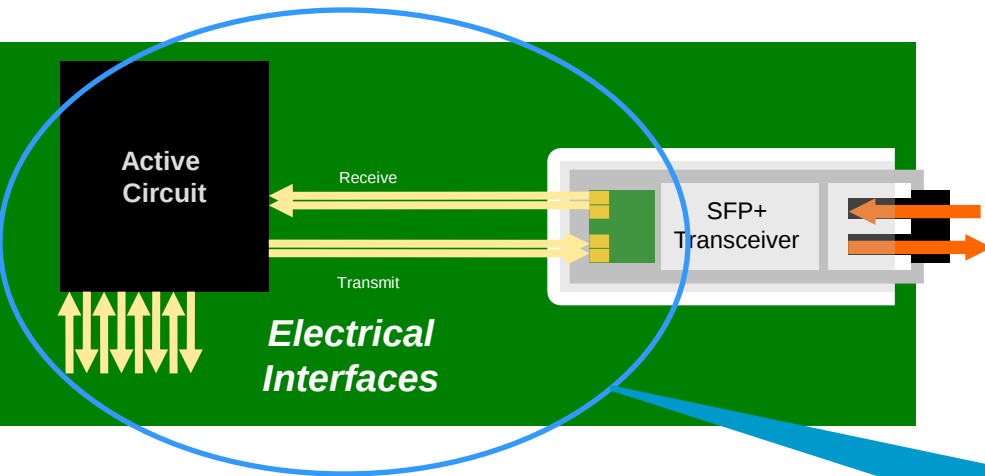


- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - 86107A Precision Timebase:
 - 86117A Dual 50 GHz Module
 - 86118A Dual 70 GHz Remote Heads
 - FlexDCA software: www.agilent.com/find/N1010A



DCA-X Family Overview

- SAN/LAN technology highly standardized:
 - IEEE 802.3ae 10 Gb Ethernet over multimode and single-mode fibers
 - IEEE 802.3aq 10 GbE over installed multimode fiber
 - IEEE 803.3ba 40/100 GbE (4x 10G or 10x 10G or 4x 25G)
 - INCITS FC-PI-4 8G Fibre Channel (8.5 Gbps)
 - INCITS FC-PI-5 16G Fibre Channel (14.025 Gbps)
 - INCITS FC-BB-5 Fibre Channel over Ethernet
 - SFF-8431 SFP+ Electrical Interface
 - SFF-8436 Quad SFP+ Copper And Optical Transceivers
 - SFF-8461 SFP+ Active Cable Specifications
- Standard compliance essential for components to succeed in marketplace
 - Test methodologies often spelled out in standard



Most accurate scope on the market for
1.0 to 14.2 Gbps differential signals

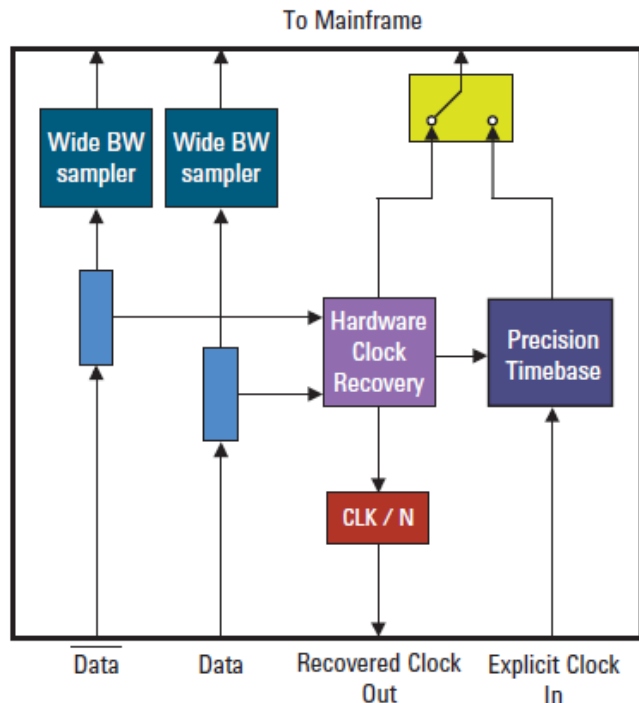
Base configuration (\$35K to \$60K):

- 86100D Mainframe
- 86112A Dual Electrical Module – OR -
- 86108B Precision Waveform Analyzer (PWA)

Optional components (\$2K to \$27K)

- 86108B-PTx Pair of phase trimmers for skew adjustment
- 86100D-200/300 Jitter and Amplitude Analysis Software
- 86100D-201 Advanced Waveform Analysis
- 86100DU-400 PLL and Jitter Spectrum Analysis
- 86100DU-401 Advanced Eye Analysis





PWA allows simple connection to differential signal yet makes very complex analyses

PWA: three modules in one:

- Dual 35 or 50 GHz electrical
- Clock Recovery to 16 or 32 Gb/s
- Precision Timebase

Integration: ultra-low jitter



TJ(1E-12): 800 fs	DJ(δ-δ): 230 fs	RJ(rms): 41 fs
PJ(δ-δ): 230 fs	DDJ(p-p): 0.0 s	DCD: -----
PJ(rms): 103 fs		ISIJ(p-p) 0.0 s



Most accurate waveform measurements:

- High signal integrity: PWA has lowest jitter, minimized cabling effects, wide bandwidth, low noise

•Benefits:

- Confidence in compliance testing
- Higher yield (not obscured by test equipment uncertainties)
- More competitive specifications on your products (win more deals and/or command higher prices)

- Questions/Discussion

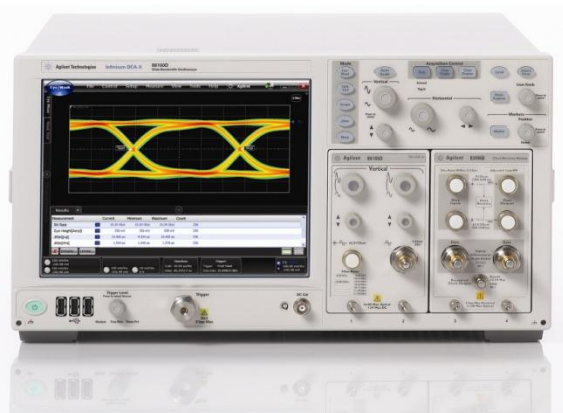


- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - PWA overview: www.agilent.com/find/86108A
 - Application note:
http://www.home.agilent.com/agilent/redirector.jsp?action=ref&cname=AGILENT_EDITORIAL&ckey=1415294&lc=eng&cc=US&nfr=-35270.749290.00

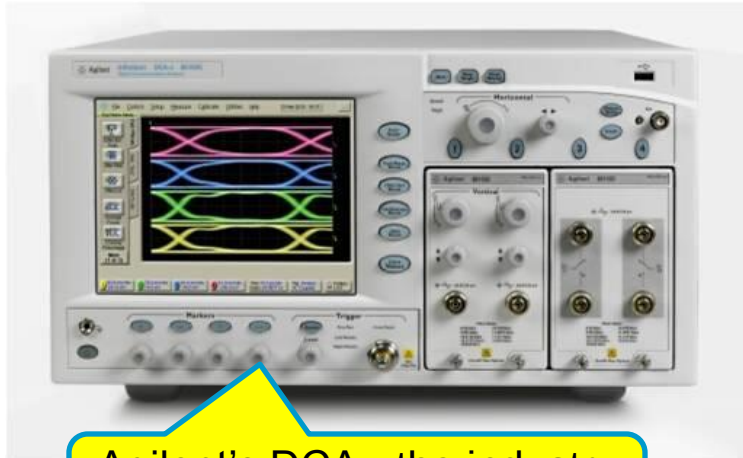
DCA-X Family Overview



- Optical transceivers are now high-volume products
 - Popular designs: SFP/SFP+, XFP, PON, XENPAK, GBIC, & more
- Standard compliance essential to succeed in the marketplace
- Transceiver manufacturers under pressure to reduce cost of test
- Solution: test multiple devices in parallel
 - Less CAPEX required to test same number of devices/year



The best is getting even better



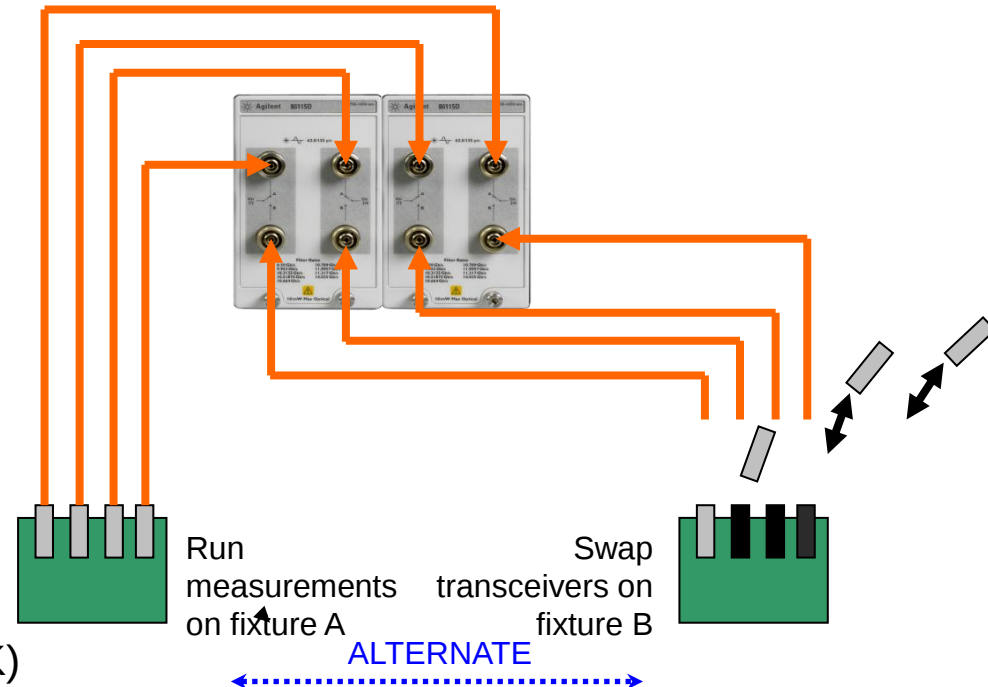
Agilent's DCA - the industry standard for over 15 years!

Best flexibility:

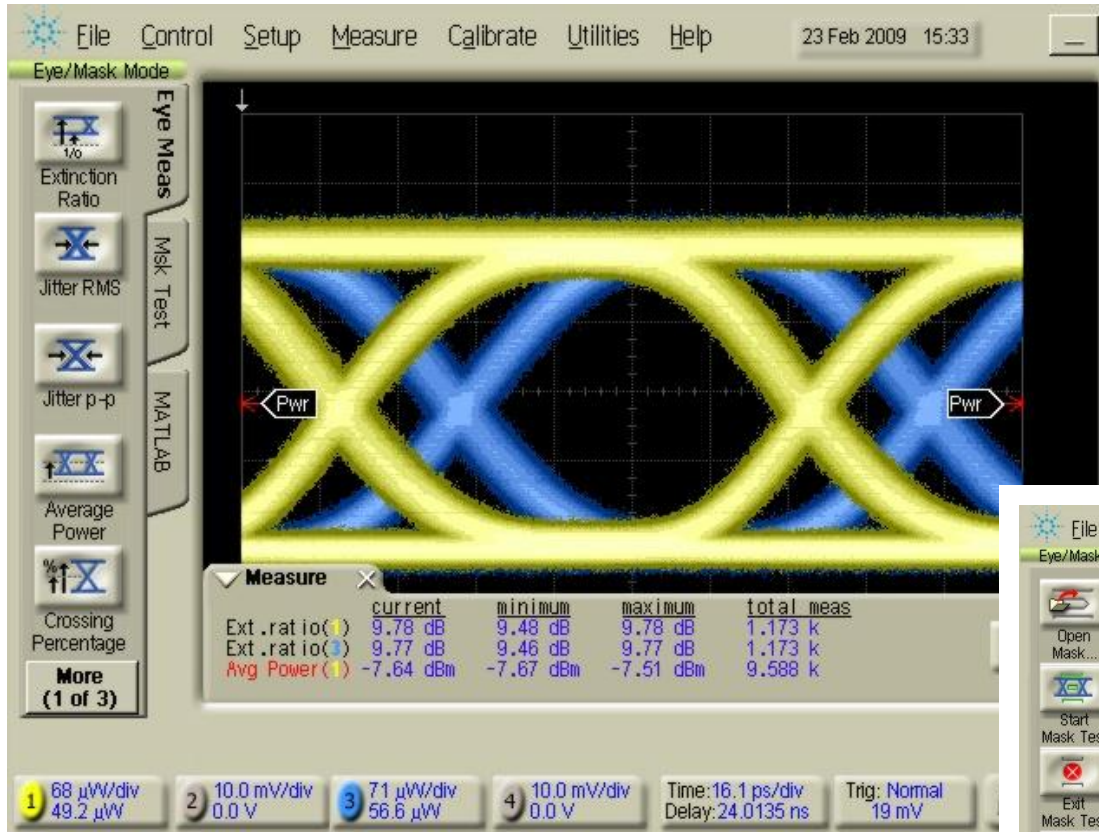
- 86100C/D with 86105C (\$50K to \$90K)
- 1 optical & 1 electrical port
- 155 Mb/s to 11.3 Gb/s

Lowest cost of test (& best accuracy):

- 86100C/D with 86115D (\$85K to \$195K)
- 2 to 8 optical ports
- 8.5 Gb/s to 14 Gb/s

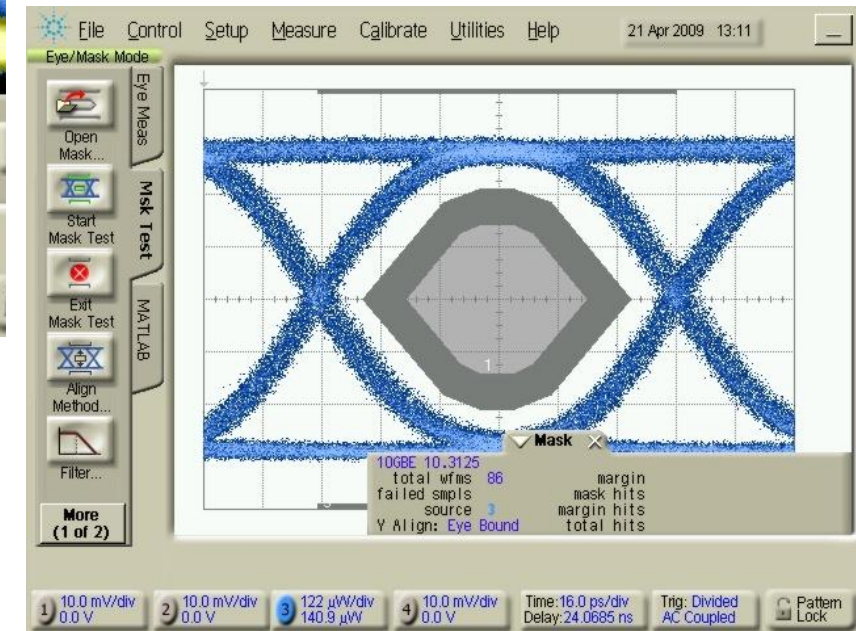


High volume manufacturing:
8 optical ports/mainframe



*ER Tuning (and Autoscale?)
video to be added later*

Mask margin measurement



Fast ER tuning on multiple channels



86105C: Highest Flexibility

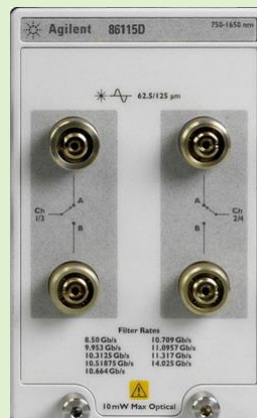
- any rate from 155 Mb/s to 11.3 Gb/s
- manufacture & test wide range of transceivers on the same line.
- up to 11 reference receivers in just one module (highest number in the industry)

**86105D: Highest Accuracy**

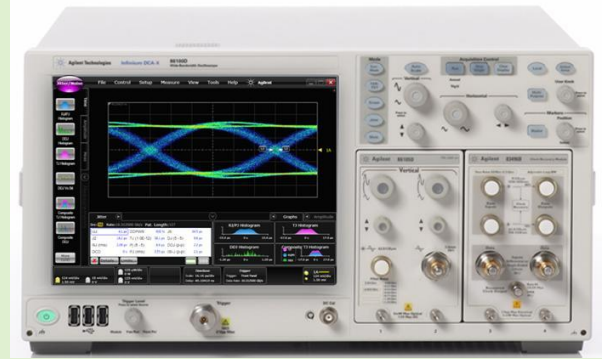
- any rate from 8.5 to 14.025 Gb/s
- Standard compliance testing
- 8G/16G FC & 10 GbE manufacturing

**86115D: Lowest Cost Per Test**

- Lowest price/optical port
- Least CAPEX required (high volume SFP/SFP+ manufacturing)

**DCA Family: De-Facto Standard**

- Contributes only **\$0.01 to \$0.10** to manufacturing cost of optical TRX

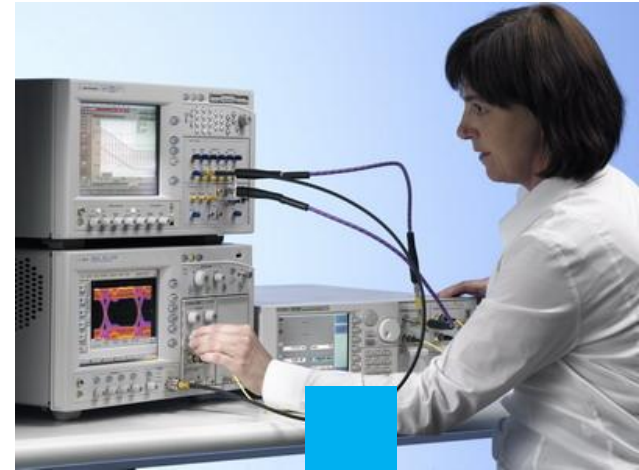
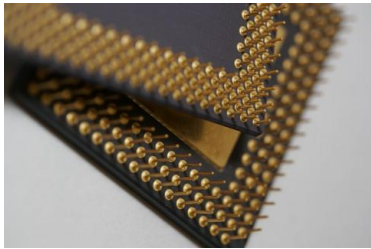


- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - Most flexible module: www.agilent.com/find/86105C
 - Most accurate module: www.agilent.com/find/86105D
 - Lowest cost of test module: www.agilent.com/find/86115D
 - Fastest modules (25/40G): www.agilent.com/find/86116C

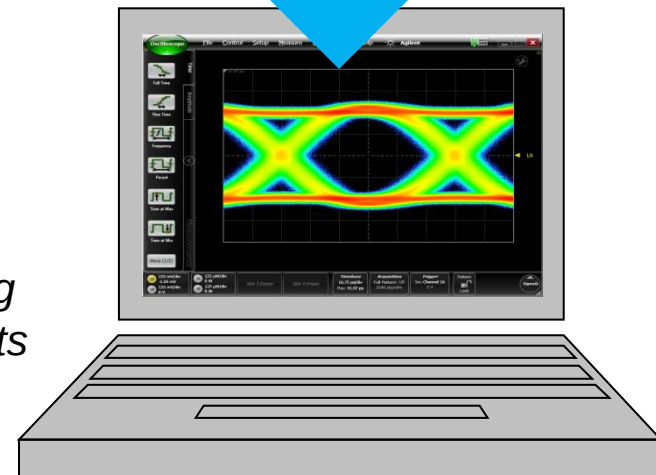


DCA-X Family Overview

“How can a sales person take a lab bench of equipment to his/her customers in order to demo a new chipset?”



Agilent's FlexDCA software uses saved measurement data in offline mode, allowing anyone with a laptop to make measurements and answer customer questions.

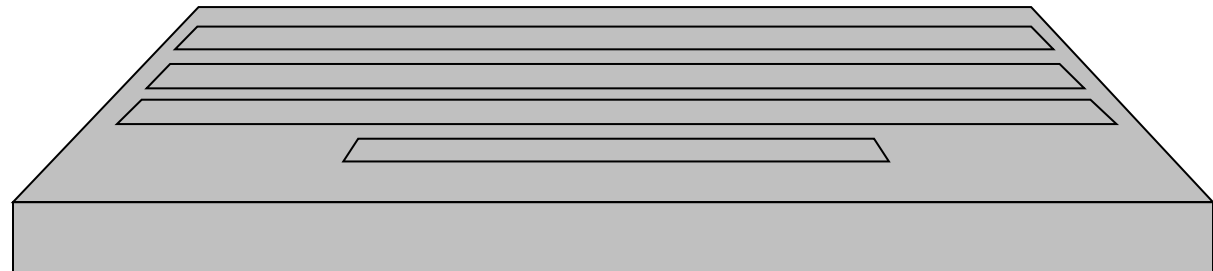


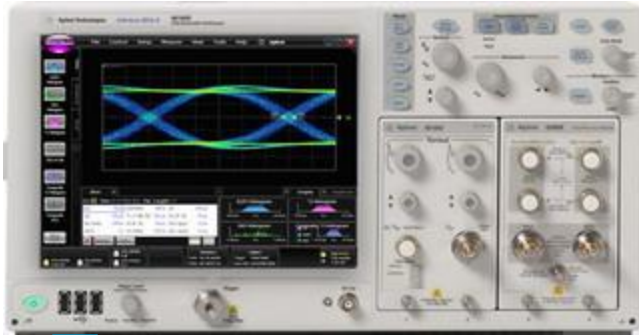
Base configuration (\$500):

- N1010A FlexDCA Software

Optional components
(\$5K to \$25K)

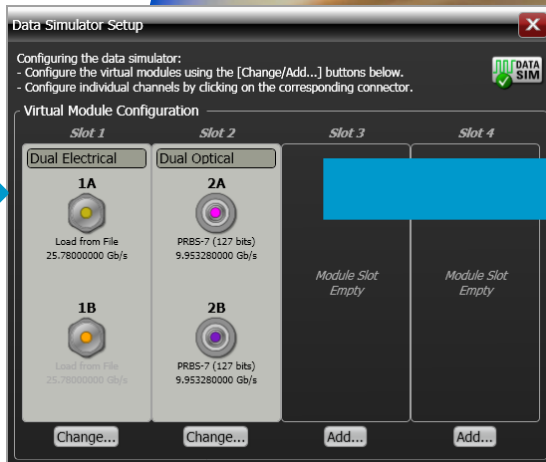
- N1010A-200/300
Jitter and Amplitude Analysis
Software
- N1010A-201
Advanced Waveform Analysis
- N1010A-SIM
InfiniiSim DCA Software



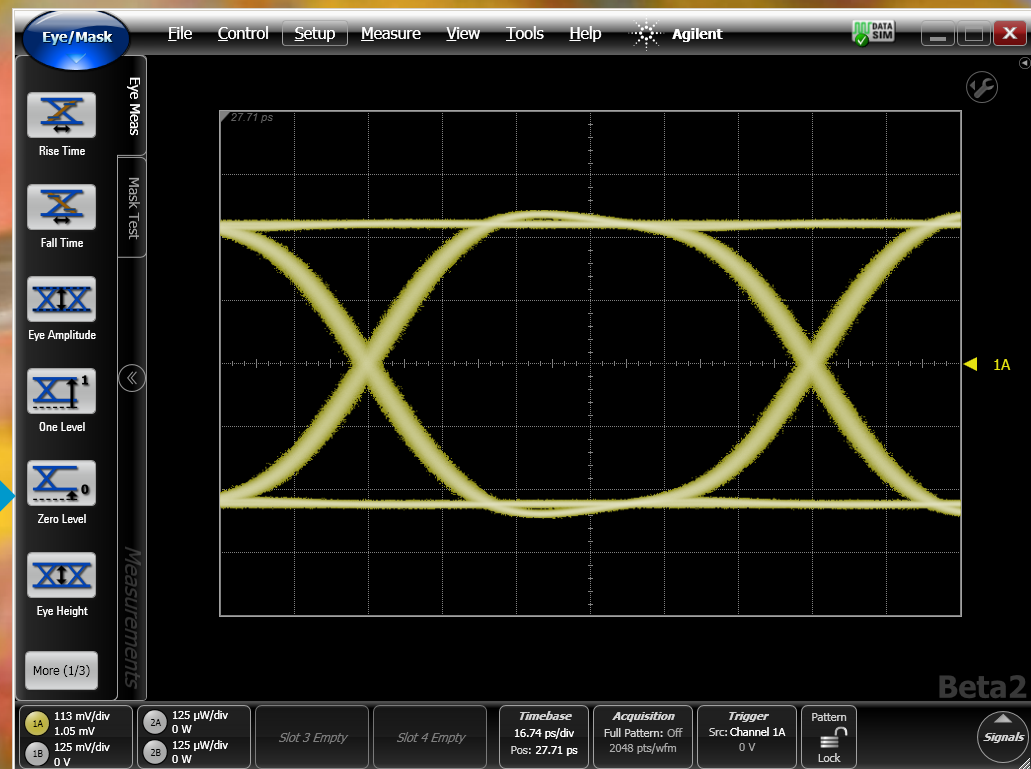


Video to be added later

DCA-X (factory/lab)



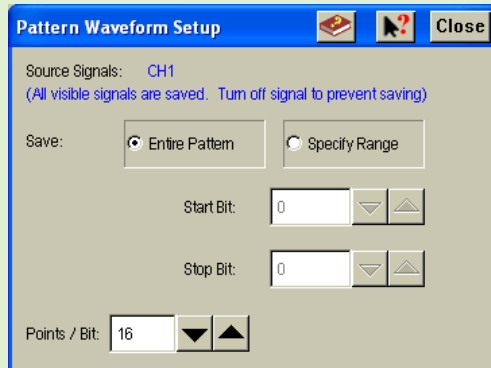
FlexDCA (field/laptop)
(with or without LAN connection to DCA-X)



Beta2



Factory/Lab: capture data



Field/Laptop: Import signals

- From file (receive captured data by email)
- Via LAN (remote connection to DCA-X)
- Integrate IBIS-AMI models (SystemVue)

- “Live” signal including jitter and noise
- Make almost ANY scope measurement



Oscilloscope Measurements



Jitter/Noise Measurements



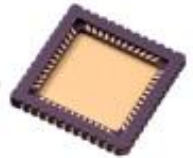
- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - FlexDCA software: www.agilent.com/find/flexdca
(*download a free 14-day trial license!*)



DCA-X Family Overview

Devices:

- Printed circuit boards (PCBs)
- Backplanes, motherboards
- Connectors / cable assemblies
- IC Sockets and packages



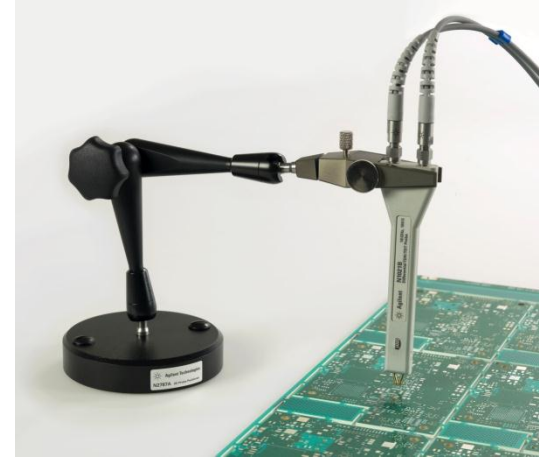
Channel characterization:

- Time-domain (TDR/TDT)
- Frequency-domain (S-parameters)

Troubleshooting:

- Fault location (short/open/mismatch)





Base configuration (\$35K to \$60K):

- 86100C/D Mainframe
- 54754A TDR/TDT Module (qty 2 for differential TDT measurements)
- N1024A Calibration Kit

Optional components (\$6K to \$14K)

- 86100C/D-202 Enhanced Impedance and S-Parameter Software
- N1021B TDR Probe
- N2787A Probe Holder



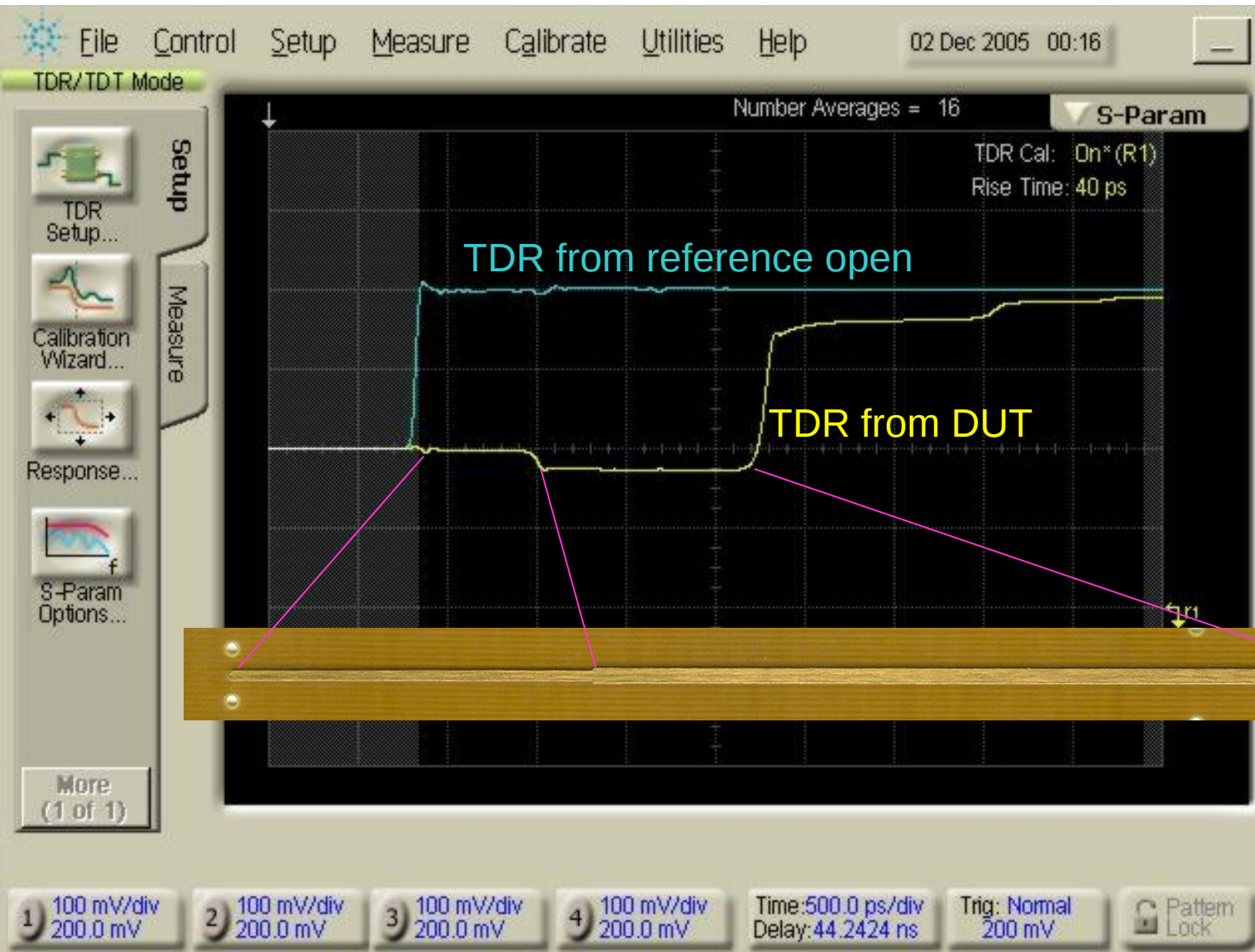
*Video to be
added later*

*TDR measures
impedance as a
function of time or
distance*

*Top trace is the
reflection from the
end of the cable*

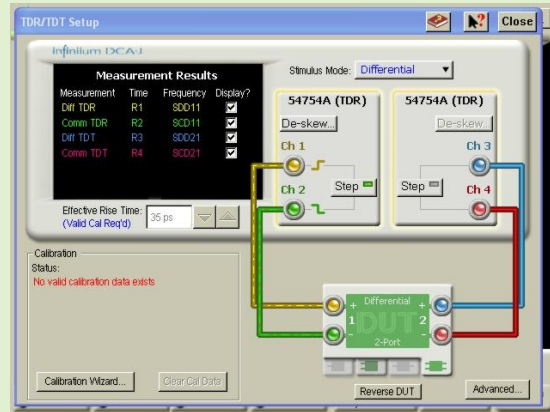
*Bottom trace is the
reflected signal
from the DUT*

*Add a slide that
has the frequency
domain view of the
impedance shown
here*



Ease Of Use

- Setup wizard eliminates user errors



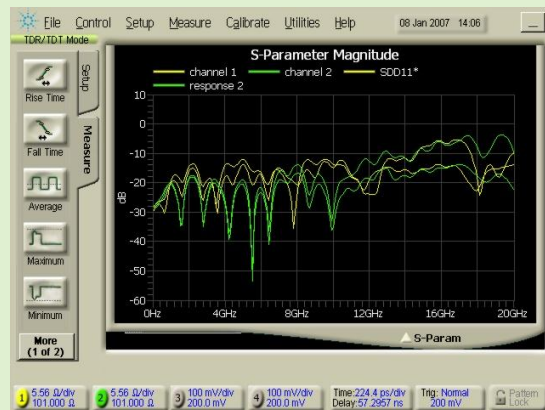
TDR calibration

- Most accurate results
- Variable edge speed – 15 to 500 ps
- Markers in time (sec) or distance (mm)



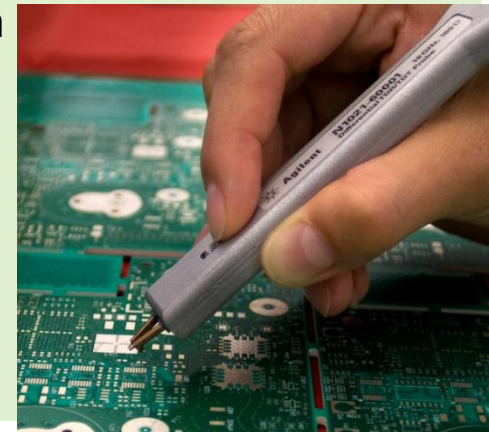
Live S-Parameters

- Magnitude, Phase & group delay
- Save data in Touchstone format



Differential TDR Probe

- Hardened tips - > 10,000 touchdown
- Ergonomic design



- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - TDR module: www.agilent.com/find/TDR
 - Differential TDR probe:
<http://www.home.agilent.com/agilent/product.jsp?id=1642413&pageMode=OV&pid=1642413&lc=eng&ct=PRODUCT&cc=US&pselect=SR.PM-Search%20Results.Overview>

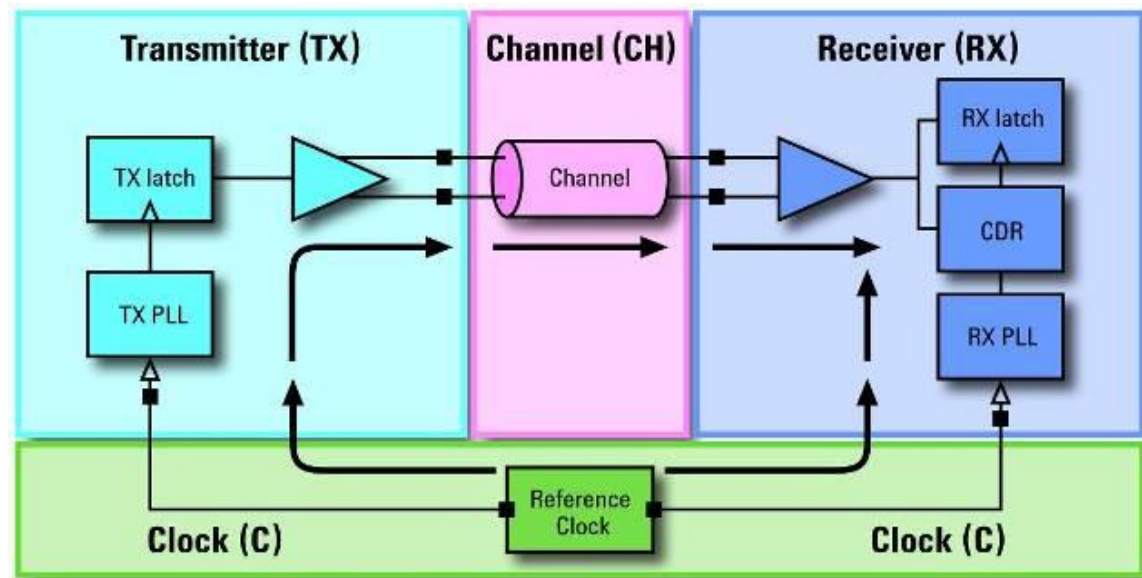


DCA-X Family Overview

- *PLLs are used throughout high-speed communications!*
- Clocks and PLLs affect jitter performance, BER of system

➤ Standards specifying PLL performance:

- PCI Express 1.1 / 2.0 / 3.0
- SONET/SDH
- Gigabit Ethernet
- Fibre Channel
- & more

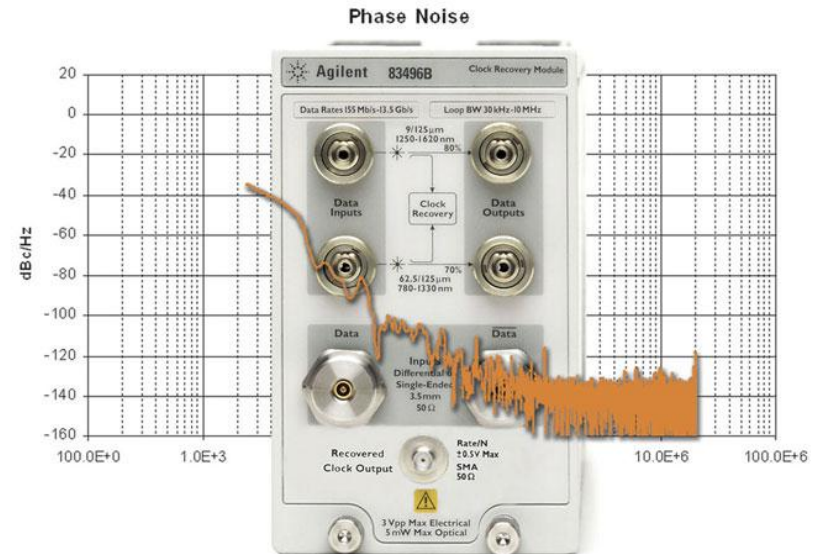


Base configuration (\$42K to \$90K):

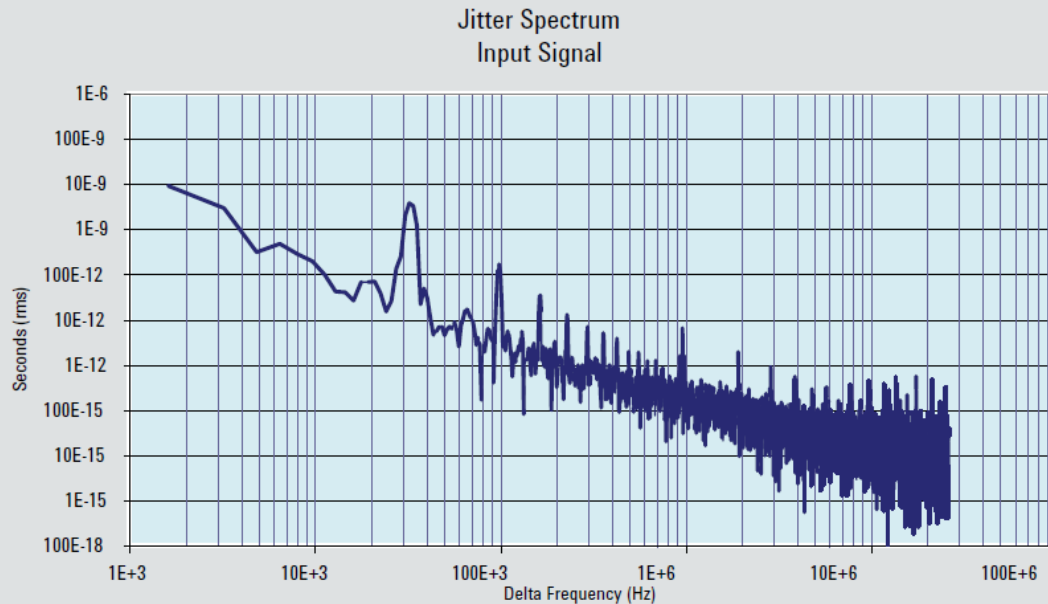
- 86100D Mainframe
- 83496B Clock Recovery Module (up to 14.2 Gb/s)
- 86100DU-400 PLL and Jitter Spectrum Measurement SW

Optimal for 1 to 32 Gb/s (\$80K to \$150K)

- 86100D Mainframe
- 86100D-200/300 Jitter and Amplitude Analysis Software
- 86108B Precision Waveform Analyzer with option JSA
- 86108B-PTx Pair of phase trimmers for skew adjustment
- 86100D-200/300 Jitter and Amplitude Analysis Software
- 86100D-201 Advanced Waveform Analysis
- 86100DU-400 PLL and Jitter Spectrum Measurement SW



Video to be
added later



Highest Peak Frequency	32.562E+3	Integrated Jitter		TJ (rms)	DJ (rms)	RJ (rms)
Jitter (rms)	5.70E+3	Integral f1 to fmax		5.71E-9	5.70E-9	269.89E-12
Jitter (p-p)	16.11E-9	Integral f2 to fmax		6.95E-12	3.74E-12	5.86E-12
		Difference f1 to f2		5.71E9	5.70E-9	269.82E-12

Jitter Spectrum / Phase Noise

The phase noise plot provides valuable insights into the nature of random jitter. Different noise mechanisms have different profiles.

Standard compliance test report



Agilent Technologies

PCI Express 2.0 Add-In Card
PLL Jitter Transfer Results

Test Summary: **PASS**

Vendor Details

Company Name	Agilent Test	Data Rate	5GT/s
Vendor ID	234234	De-Emphasis	3.5dB
Vendor Device ID	NX800GT Series		

Test Procedure Agilent 86100C DCA-J Method

Software Revision 1.01.13

Test Date 6/11/2008 11:22

Test Equipment

86100C Serial # MY46520627

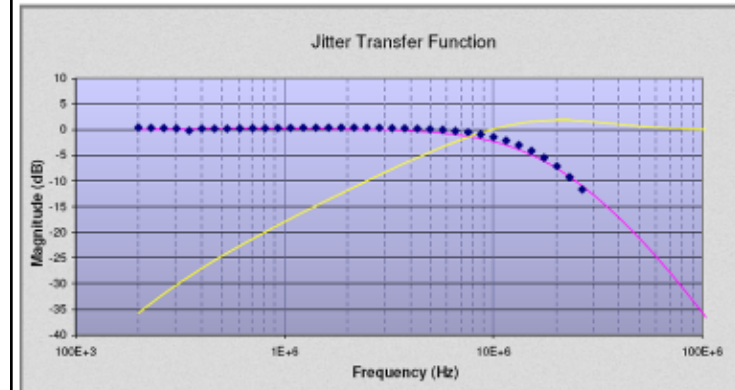
Module Serial # P1-3

N4903A Serial # DE46B00332

Test Results:	Measured Values	Status	Specifications
			<u>min</u> <u>max</u>
Data Rate	5.000E+9	N/A	N/A N/A
Bandwidth	13.43E+6	PASS	5.0E+6 16.0E+6
Peaking (dB)	0.44	PASS	- 3

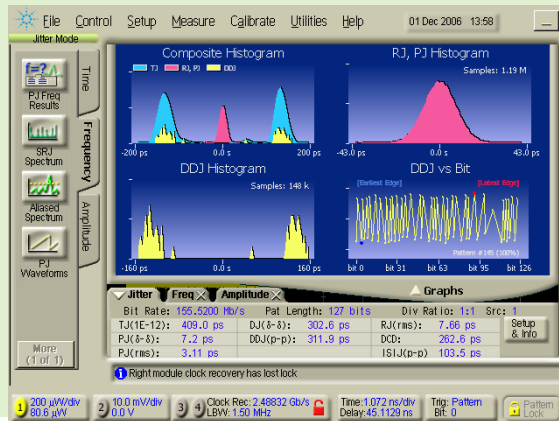
Jitter Transfer Function Model (FYI Only)

f_zero	f_gain	f_pole
000.0E+0	13.7E+39	7.6E+18



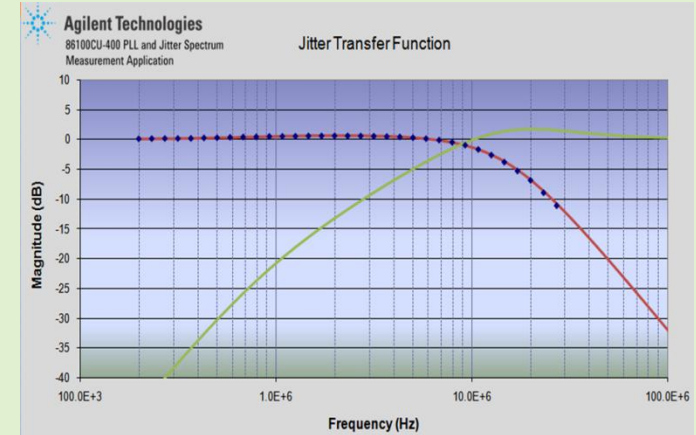
Jitter Separation

Automated jitter analysis helps identifying root causes



PLL Bandwidth & Peaking (Jitter Transfer)

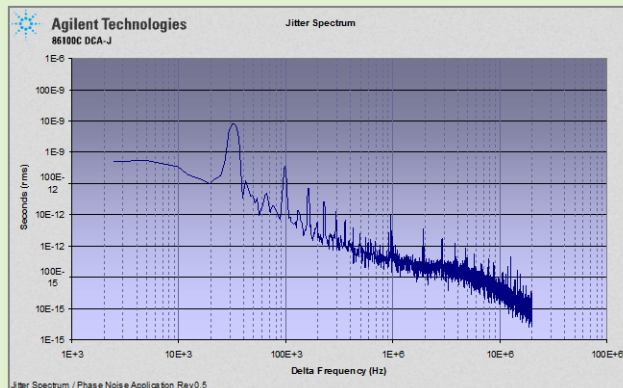
Automated PLL analysis can save hours of test time



Unique to the DCA: jitter spectrum/phase noise on both data and clock signals

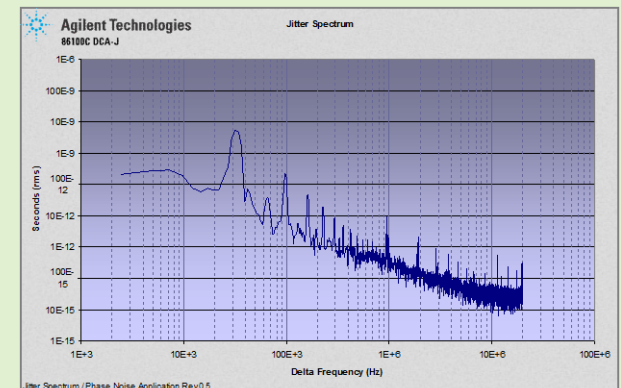
Jitter Spectrum of DATA

Automated characterization of signals with spread-spectrum clocks (SSC)



Phase Noise of CLOCK

Automated characterization of clocks with spread-spectrum clocks (SSC)



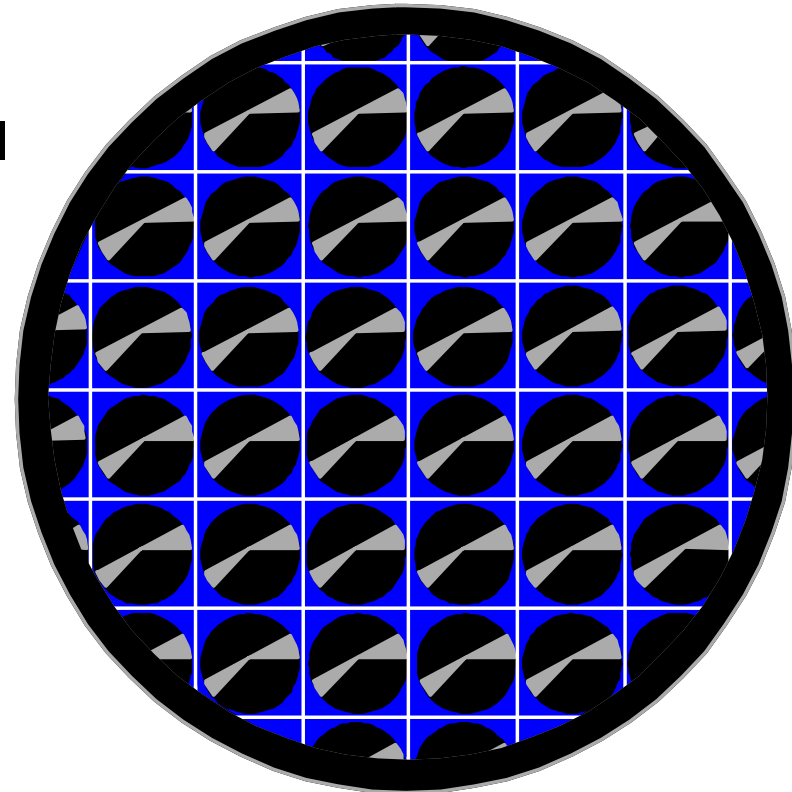
- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - Precision Waveform Analyzer: www.agilent.com/find/86108A
 - PLL & Jitter Spectrum: www.agilent.com/find/jtf
 - Webinar recording (ask Rob for URL)



DCA-X Family Overview

- Research often pushes the edge of technology where commercial measurement solutions and automation tools have yet to be developed.
- Flexibility of measurement tools is essential to make progress.
- Desirable: modularity and custom measurement algorithms.

$$r = \frac{\sum_{i=1}^n (x_i - x_m)(y_i - y_m)}{(n-1)x_s y_s}$$



Base configuration:

- 86100D Mainframe
- 86100D-061/062 MATLAB® software
- 86100D-201 Advanced Waveform Analysis
- At least one measurement module

Optional:

- 83496B Clock Recovery
- 86107A Precision Timebase

FFT plot of measured waveform



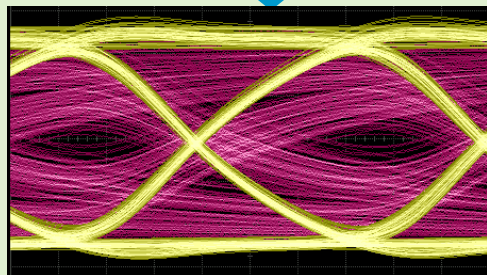
Raw Waveform



MATLAB®



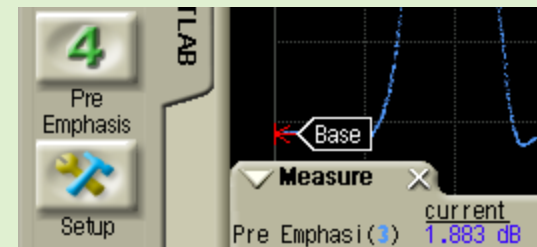
Processed Waveform



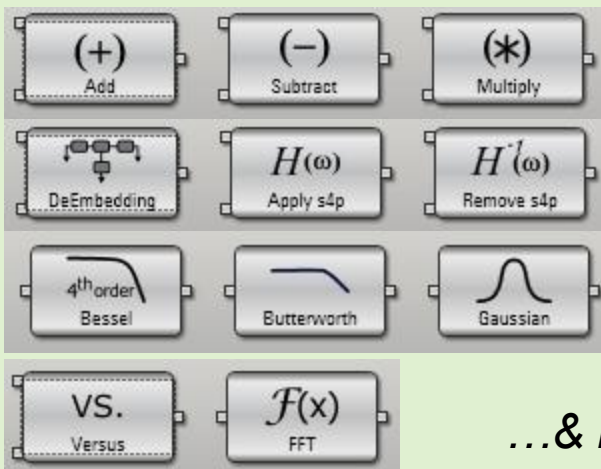
Raw Data



MATLAB®

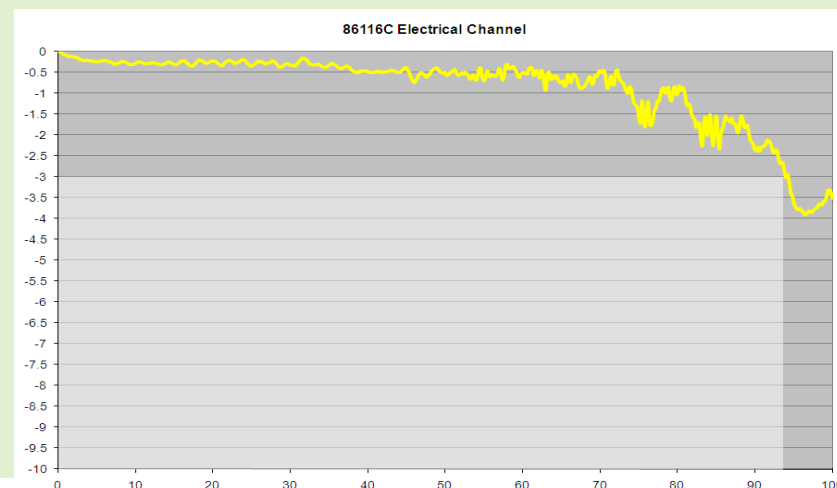
User-defined Measurement
(Pre-Emphasis)

Common signal processing tasks



...& more!

Ultra-wide bandwidth – DC to >93 GHz



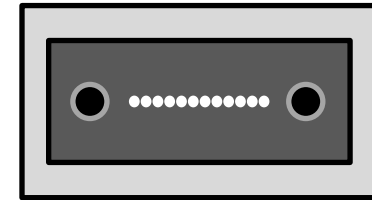
- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - Advanced Waveform Analysis:
http://www.home.agilent.com/agilent/redirector.jsp?action=ref&lc=eng&cc=US&nfr=-35270.912215&ckey=1376620&cname=AGILENT_EDITORIAL
 - MATLAB: www.mathworks.com



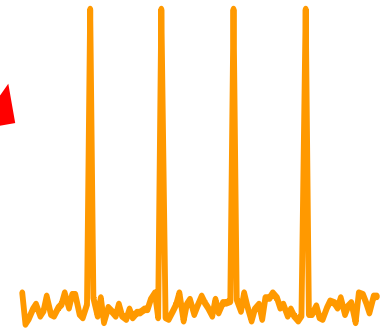
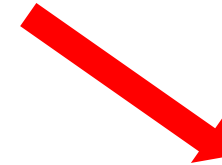
DCA-X Family Overview

- Fundamental architectures:

- N optical signals, each in its own optical fiber
- Coarse Wavelength-Domain Multiplexing (CWDM)
(different “colors” in one fiber)



12-fiber Connector



CWDM Spectrum

- Typical configurations:

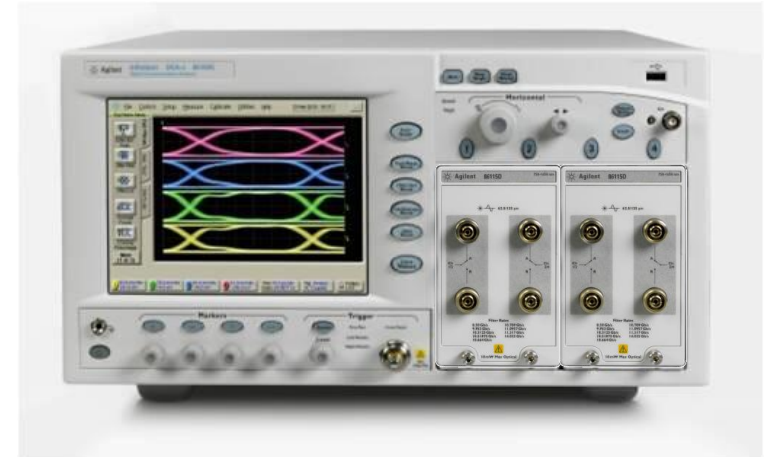
- 4 x 10 Gbps
- 12 x 10 Gbps
- 4 x 25 Gbps
- Proprietary standards (≥ 240 Gbps aggregate)

Base configuration (\$90K – 110K)

- 86100D Mainframe
- 86115D-002 Dual Optical Module
- [Optional] 86112A Dual Electrical Module

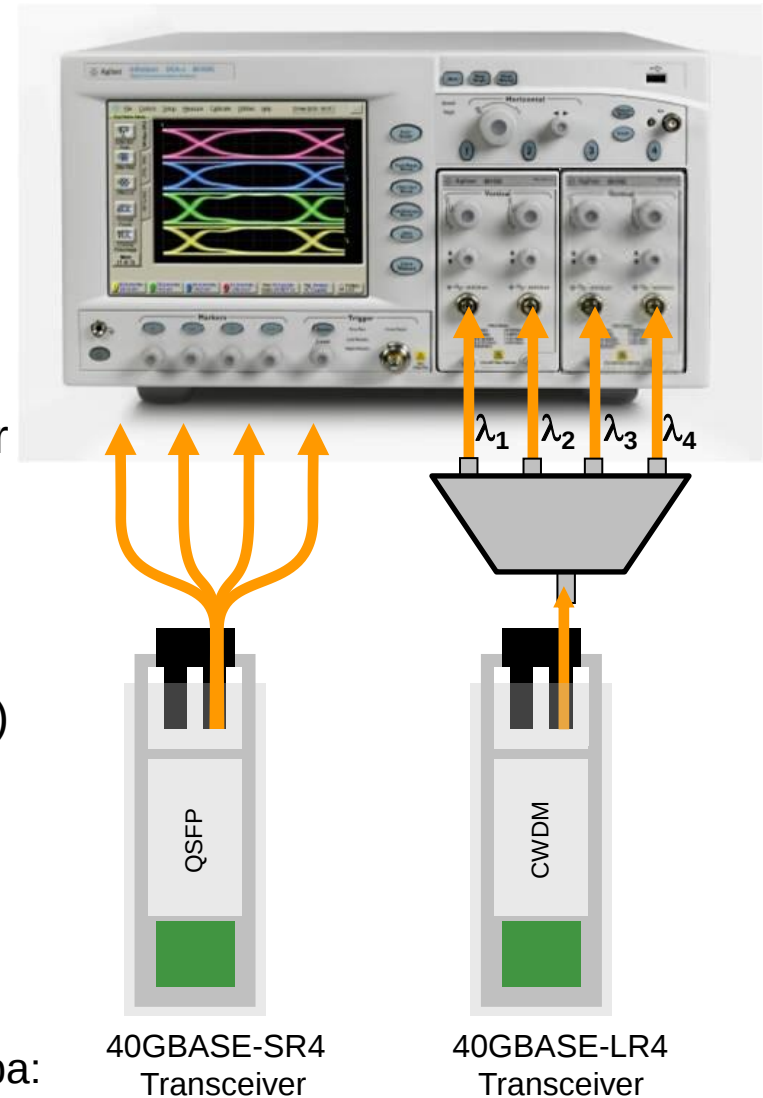
Advanced configurations (\$110K – 200K)

- 86100D Mainframe
- 2x 86115D-002 Dual Optical Module
- 1x or 2x 86115D-004 Quad Optical Module



*Video to be
added later*

- Connectivity:
 - Breakout patchcords for QSFP devices
 - Demultiplexer for CWDM devices
 - Multiple DCAs or optical switches required for higher channel counts
- Measurement:
 - Acquire four databases in parallel (10-60 sec)
 - Analyze eyes sequentially (~1 sec each)



Reduced total CAPEX:

- *Modules share common mainframes*
- *2 or 4 optical ports/module (up to 8 per mainframe)*
- *Effective cost/port as low as \$24K (lowest cost test solution in the industry!)*

Mainframe plus...	Opt. Ports	Ele. Ports	\$/Opt.Port
86105D (Qty 1)	1	1	\$ 64,278
86105C/200 (Qty 1)	1	1	\$ 56,009
86105D (Qty 2)	2	2	\$ 53,719
86105C/200 (Qty 2)	2	2	\$ 45,450
86115D/002 (Qty 1)	2	0	\$ 42,559
86115D/002 (Qty 2)	4	0	\$ 37,280
86115D/004 (Qty 1)	4	0	\$ 26,780
86115D/004 (Qty 2)	8	0	\$ 24,140

Reduced total test time:

- *Test times dominated by data acquisition*
- *DCA allows you to acquire data on 4 channels simultaneously with no reduction in throughput*
- *3X to 4X throughput improvement****
*** over single port configuration

Expandable (12-channel example):

- *2 Mainframes*
- *3 Quad Optical Modules*
- *(optional***) 86112A Dual Electrical or 83496B Clock Recovery*

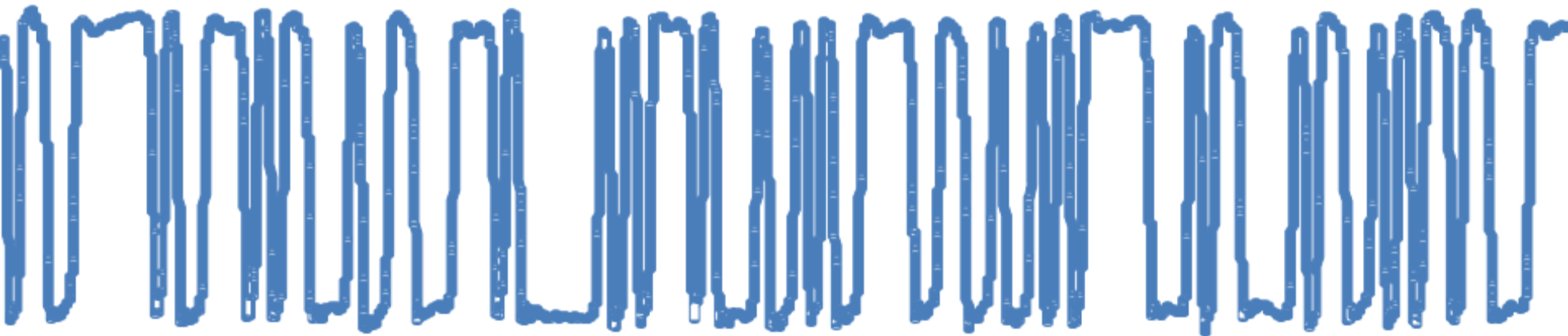
*** Desirable for troubleshooting

- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - Multi-channel optical: www.agilent.com/find/86115D
 -



DCA-X Family Overview

- Some standards require jitter analysis using test patterns such as PRBS-31 or scrambled/non-repeating data.
- Long patterns or non-repeating signals exceed the capabilities of conventional jitter analysis on sampling oscilloscopes (limit: PRBS-16).
- Long pattern capability often requested from current DCA owners.

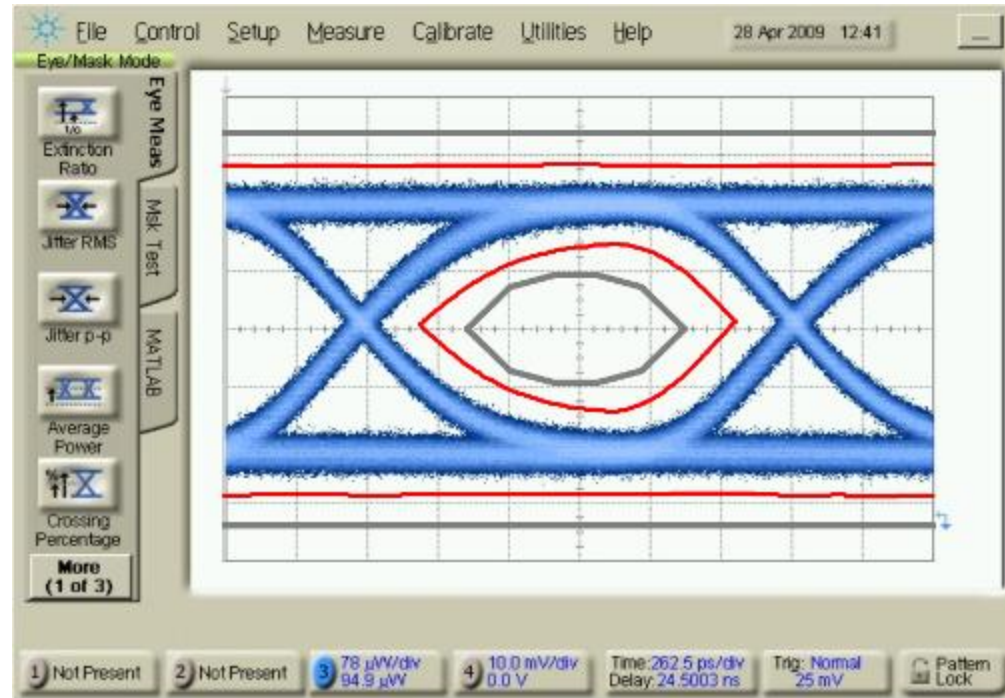


Base configuration:

- 86100D Mainframe
- Any module with optical or electrical channels
- 86100D-401 Advanced Eye Analysis Software
- LAN or GP-IB connection to 86100D running FW revision 8.1 or higher

Optional configuration:

- 86100D-200 Jitter Analysis
- 86100D-300 Amplitude Analysis

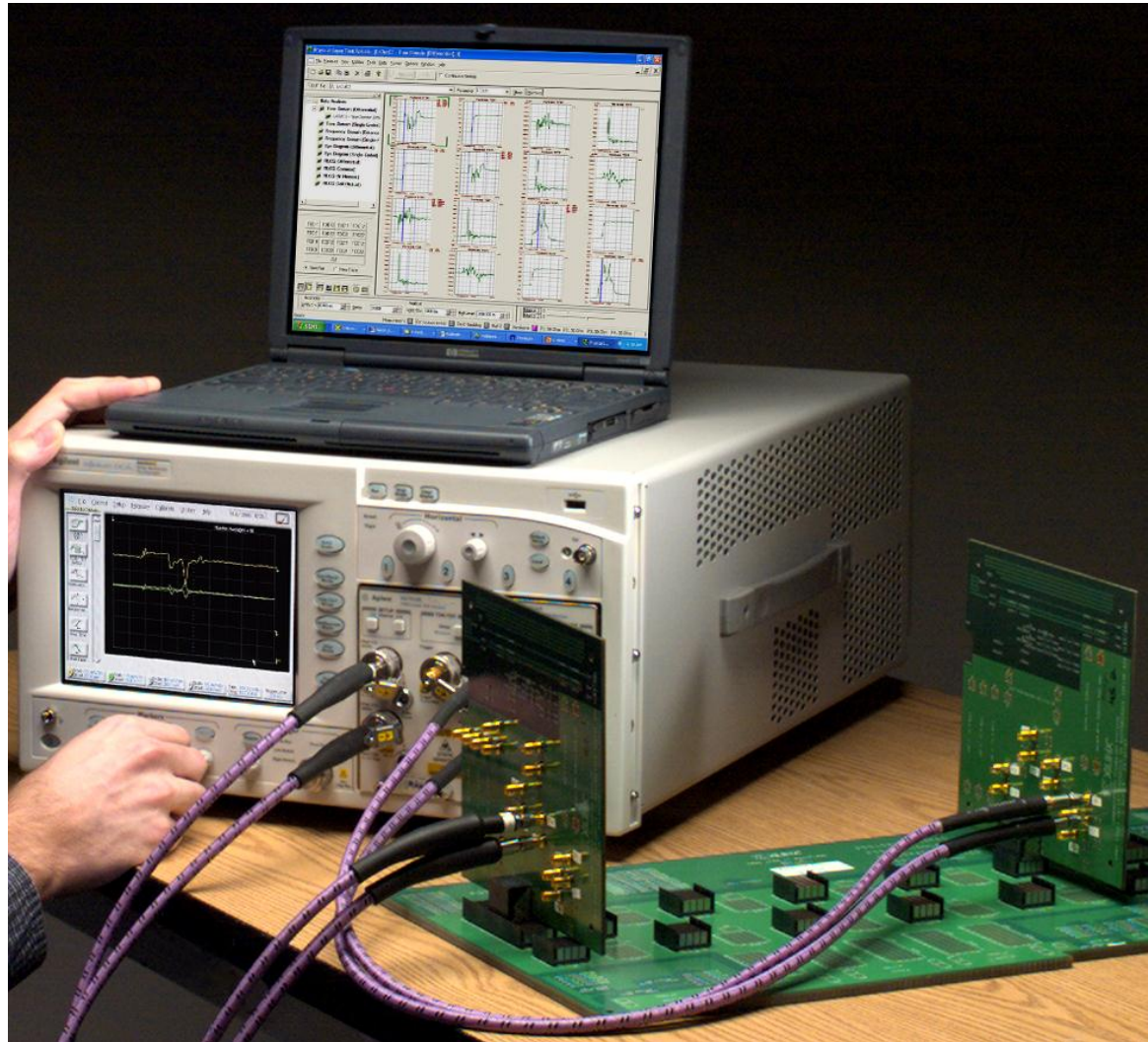


			Seconds	UI
Data Rate	622.08E+6	RJ	18.13E-12	0.0113
Mark Density	0.501	DJ (d-d)	82.59E-12	0.0514
Transition Density		TJ at P1	300.27E-12	0.1868
Eye Opening at Probability	284.74E-6	TJ at P2	337.61E-12	0.2100
Database Level	6.15E-03	P1 Value	1.00E-9	
Samples	0.33%	P2 Value	1.00E-12	
Revision	1.6E+6	Min Pulse	1.52E-9	0.9484
	P.01.15	DDPWS		0.0516



Advanced Eye Analysis is an external software application that combines the measurement capabilities of the DCA with the analysis power of Microsoft Excel®

- Data collection via LAN or GP-IB
- Advanced analysis (extending the capabilities of the scope)
- Numerical and graphical reports



Advanced Eye Analysis:

- Allows jitter analysis of any data signal – including **live traffic** (non-repeating patterns)
- Enhanced accuracy using 86100C/D-200:
 - Run DCA Jitter Mode on a short pattern to extract highly accurate RJ information
 - Switch to long pattern or live traffic and extract DJ
 - Complete analysis and report TJ

Key Features:

- Jitter includes RJ, DJ, TJ, J2, J9
- Amplitude Interference includes RN, DI, and TI
- Data Dependent Pulse Width Shrinkage (DDPWS)
- BER contour mask testing
 - Standards compliant or user-defined masks

Major Standards:

- 10/40/100 Gigabit Ethernet (GbE)
- 8G/16G Fibre Channel
- OIF-CEI
- SFP+

- Questions/Discussion
- References (*hyperlinks to Agilent.com*)
 - DCA-X overview: www.agilent.com/find/dcax
 - Advanced Eye Analysis: www.agilent.com/find/eye



DCA-X Family Overview

Larger 10.4"
display

Mappable
Vertical
Controls

Multi-
Purpose
Button

User
Defined
Knob

Dual user
interface

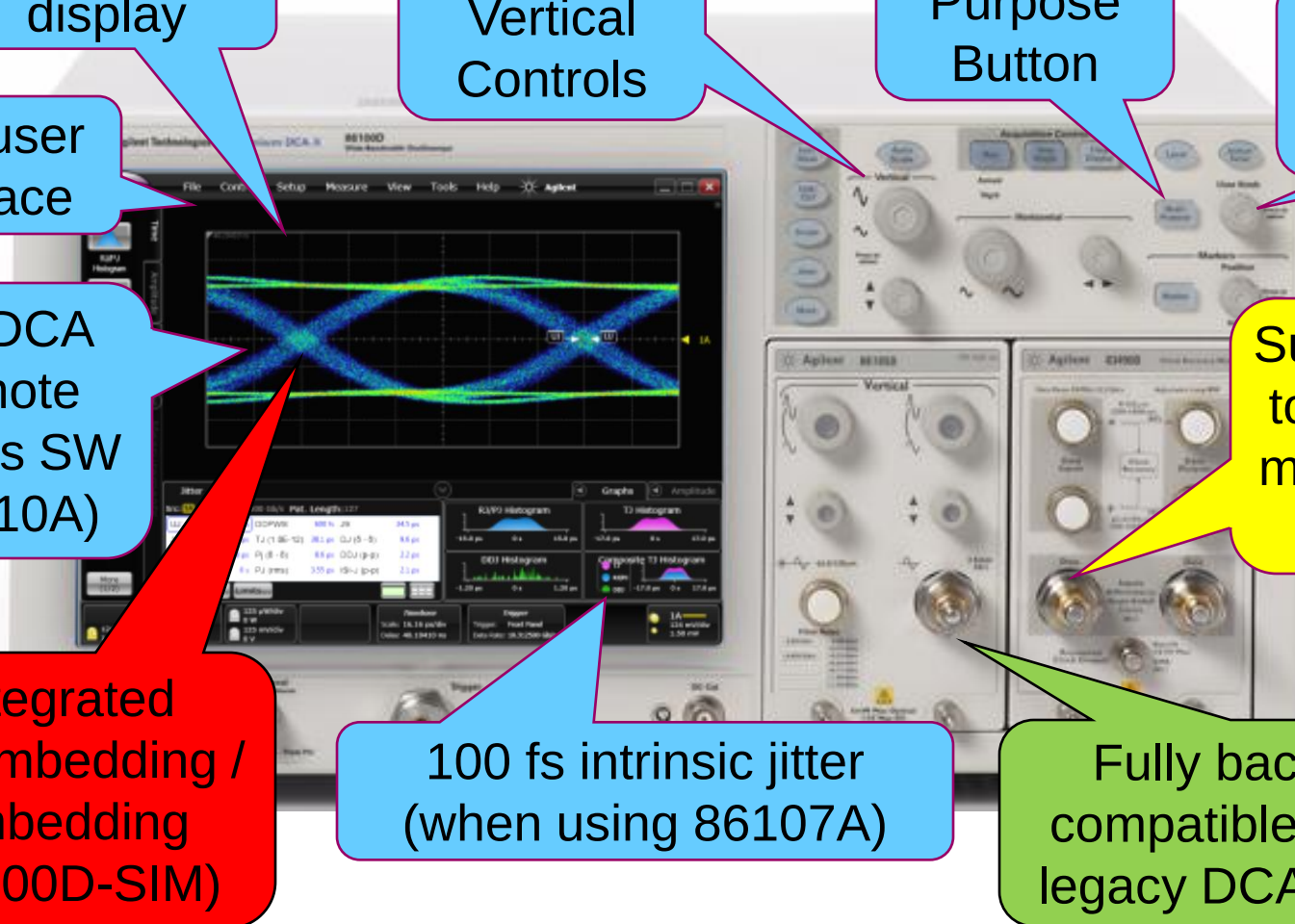
FlexDCA
Remote
Access SW
(N1010A)

Integrated
De-Embedding /
Embedding
(86100D-SIM)

100 fs intrinsic jitter
(when using 86107A)

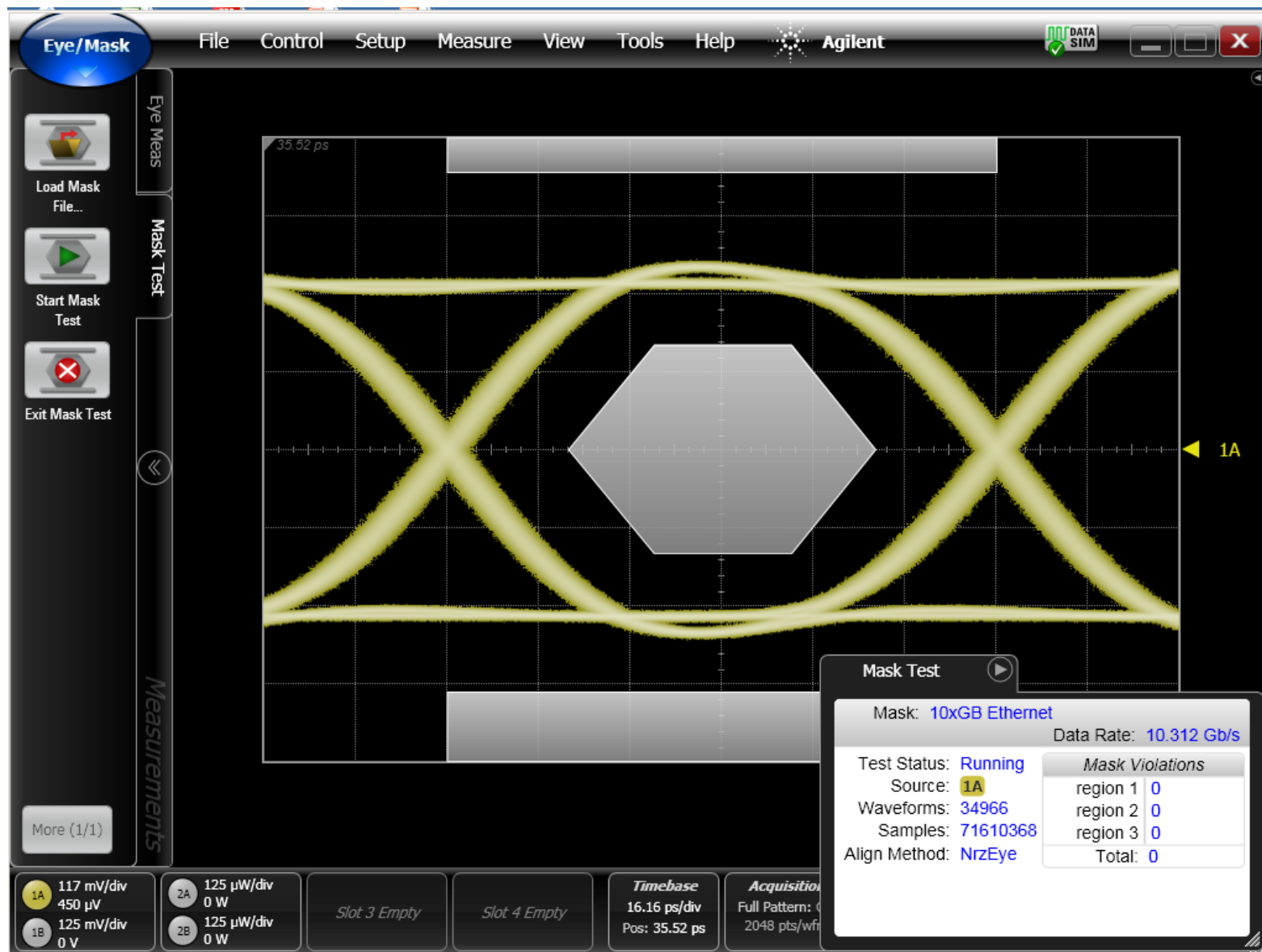
Fully backwards
compatible with ALL
legacy DCA Modules

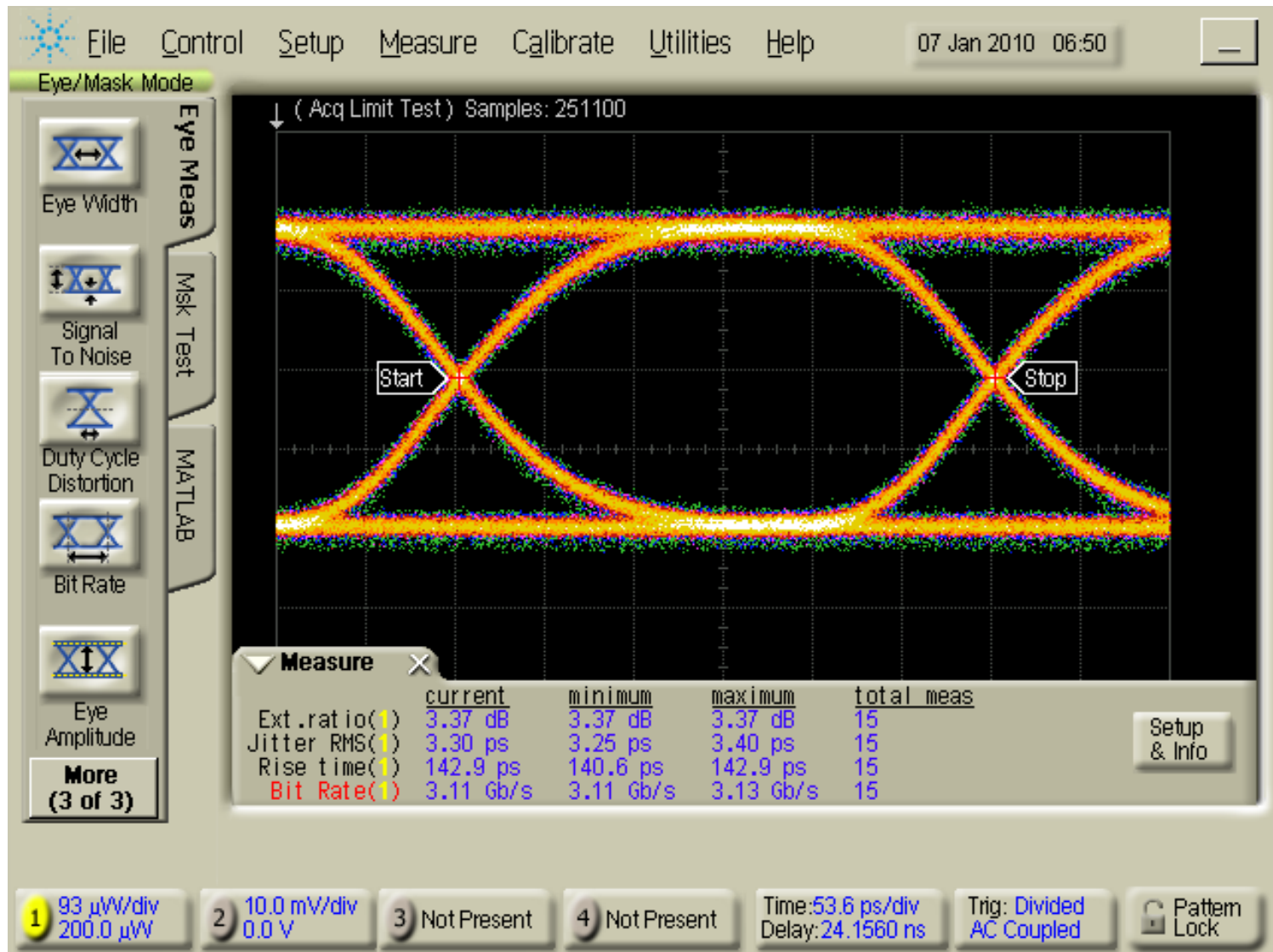
Support for up
to 16 parallel
measurement
channels

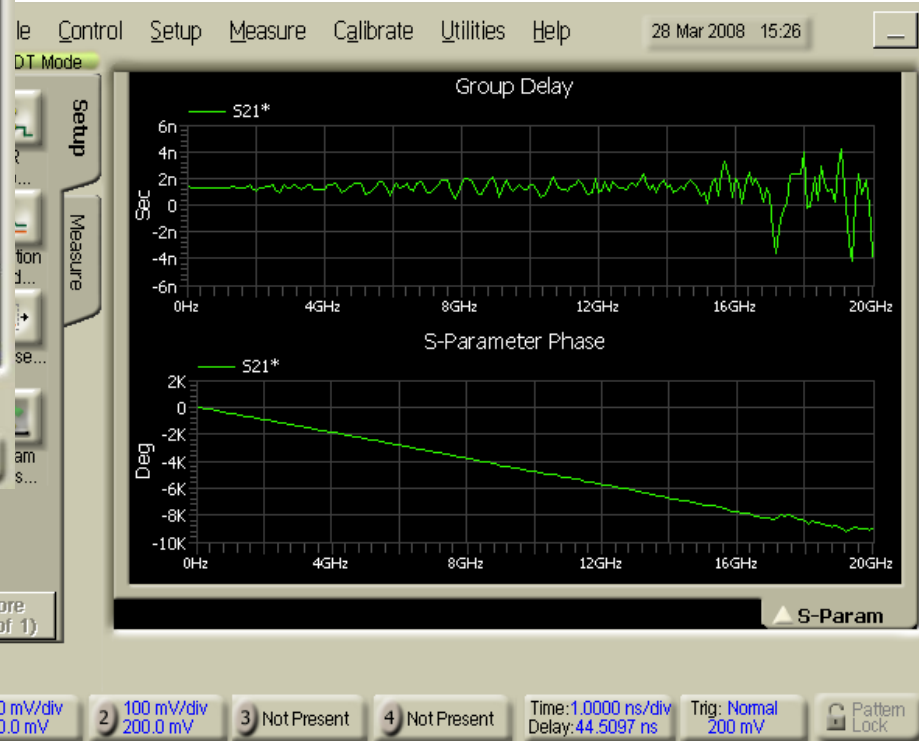
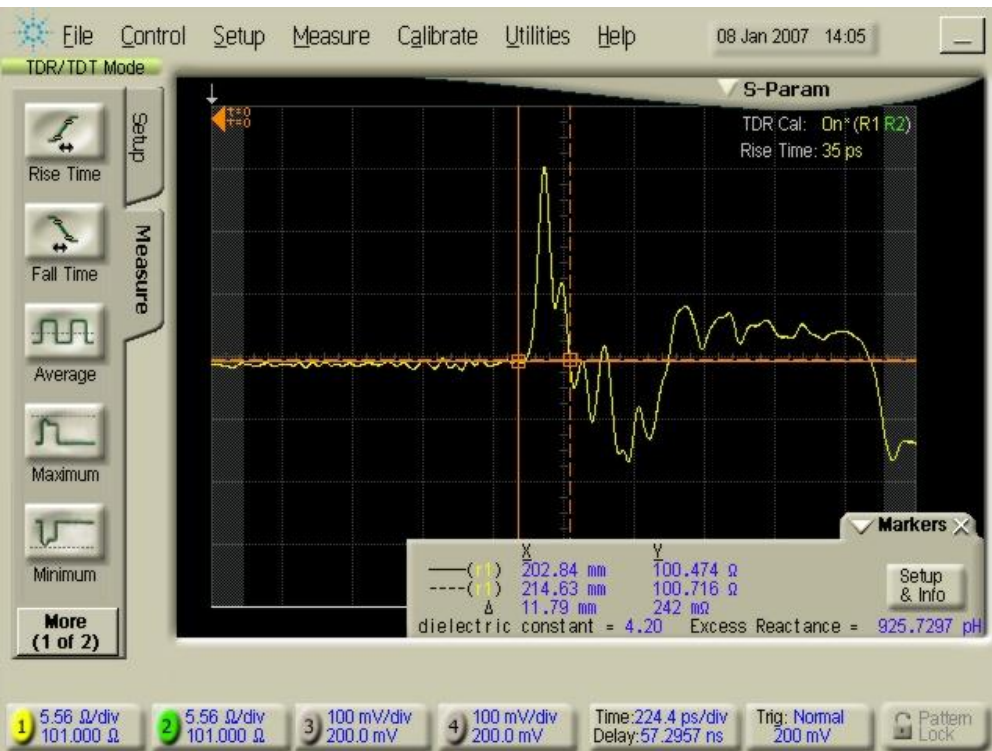












86105C Optical/Electrical Module

- 9 GHz amplified optical channel
 - 750-1600 nm
 - 9/125 to 62.5/125 μm fibers
 - 155 Mbps to 11.3 Gbps options
- 20 GHz electrical channel

*Optimized for greatest **flexibility***



86105C

86105D

86115D

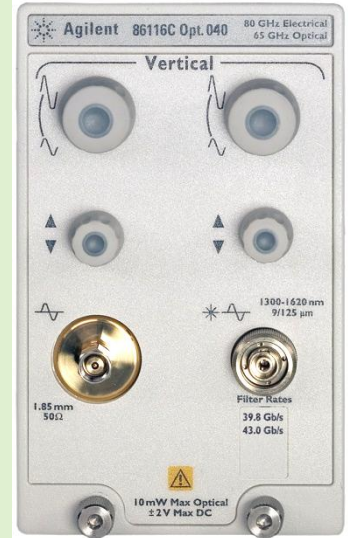
116C

86105D Optical/Electrical Module

- 20 GHz optical channel
 - 750-1650 nm
 - 9/125 to 62.5/125 μm fibers
 - 8.5 Gbps to 14.025 Gbps

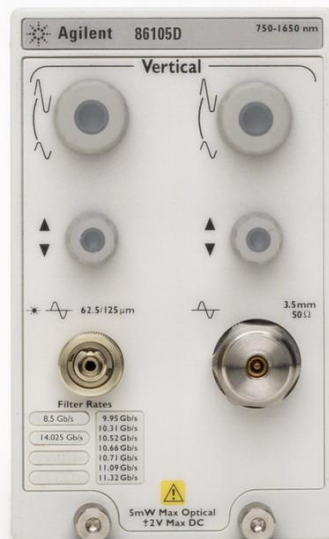
- 35 GHz electrical channel

*Optimized for best **accuracy***

**86116C Optical/Electrical Module**

- Option 25: 45 GHz (typical) optical
- Option 40: 70 GHz (typical) optical
 - 750-1600 nm
 - 9/125 fiber (single-mode)
- 93 GHz (typical) electrical channel

*Optimized for highest **bandwidth***

**86115D Dual/Quad Optical Module**

- 20 GHz optical channels
 - 750-1650 nm
 - 9/125 to 62.5/125 μm fibers
 - 8.5 Gbps to 14.025 Gbps

*Optimized for lowest **cost of test***



86112A Dual Electrical Module

- 20 GHz electrical channel
- Characteristic rms noise:
 - 0.25 mV (12 GHz BW)
 - 0.5 mV (20 GHz BW)

**86117A Dual Electrical Module**

- 50 GHz electrical channel
- Characteristic rms noise:
 - 0.4 mV (30 GHz BW)
 - 0.6 mV (50 GHz BW)

**86118A Dual Remote Heads**

- 70 GHz electrical channel
- Characteristic rms noise:
 - 0.4 mV (30 GHz BW)
 - 1.3 mV (70 GHz BW)
- Cable length:
 - 2 m (each head)

**86108B Precision Waveform Analyzer**

- Combines 2 electrical channels, clock recovery (CR) and precision timebase into one module
- <50 fs intrinsic jitter
- 35 – 50 GHz bandwidth
- 16 – 32 Gb/s CR
- Easy differential setup for PCI-EXPRESS®, SATA



83496B Clock Recovery Module

- Option 100: Electrical Only
- Option 101: Optical & Electrical
- Continuous 0.05 to 14.2 Gbps
- Adjustable loop bandwidth
- PLL and Jitter Spectrum Analysis

**86107A Precision Timebase**

- <100 fs jitter (86100D mainframe)

**54754A Differential TDR Module**

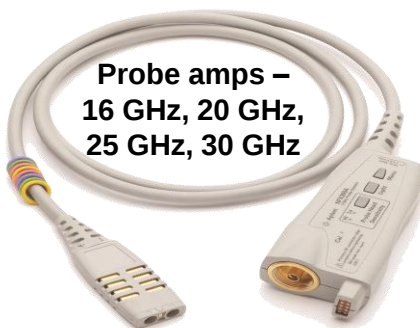
- 18 GHz electrical channel
- 35 ps step generators
(effective rise time software adjustable from 15 to 500 ps)
- Single-ended or differential
- TDR or TDT operation
- Can be used as a 2-channel receiver module

**Application Software**

- 200: Jitter Analysis
- 201: Advanced Waveform Analysis
- 202: Enhanced Impedance and S-Parameters
- 300: Amplitude Analysis
- 400: PLL and Jitter Spectrum
- 401: Advanced Eye Analysis
- 500: Productivity Package (for optical TRX manufacturing)
- SIM: InfiniiSim-DCA Waveform Transformation

The Industry's First 30 GHz Probing System
InfiniiMax III probe amp/probe head architecture

Probe Amplifier



Performance
verification and
de-skew fixture

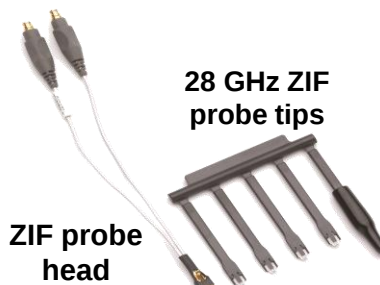


Probe Heads

30 GHz Browser
with LED Headlight



28 GHz ZIF
probe tips



16 GHz solder-in
probe head



28 GHz
2.92mm/3.5mm/SMA
probe head



Probe Adapters

Sampling scope
adapter



High impedance
probe adapter



Precision BNC 50
ohm adapter
adapter



Hyperlink to file “**86100D Family Specifications 5990-5824EN.pdf**”

Optional hyperlink to file “86100D.chm” (preferably link to chapter SPECIFICATIONS)

*Requires *.chm reader– available by default on all Windows XP/VISTA/7 computers, free application (for the iPad (search for “CHMate Lite”).*

