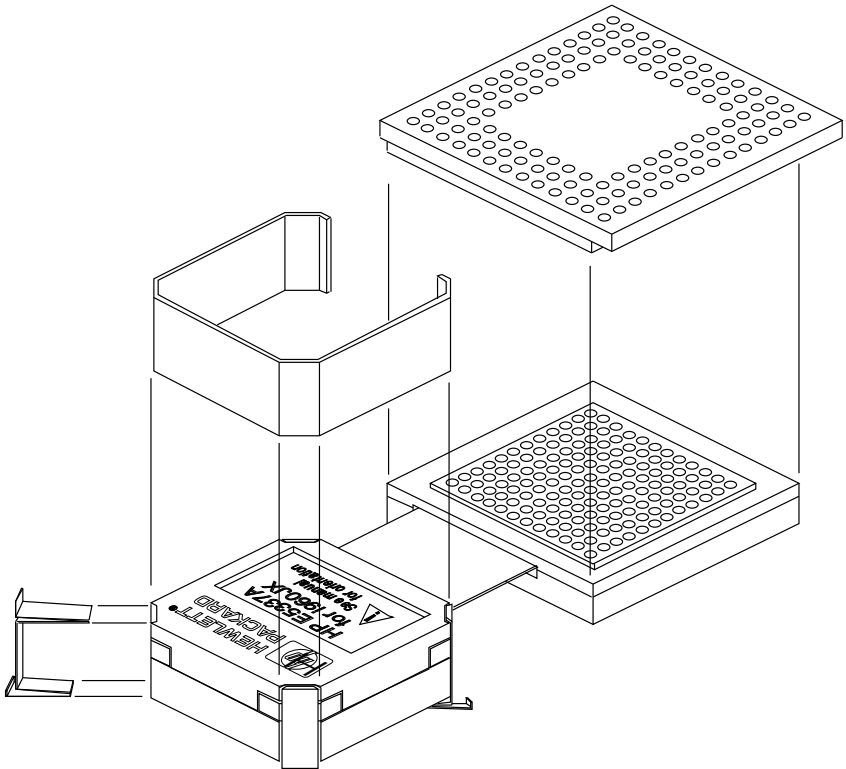

HP E5337A

Intel 80960J-series PQFP Adapter

**For use with
HP logic analyzers**

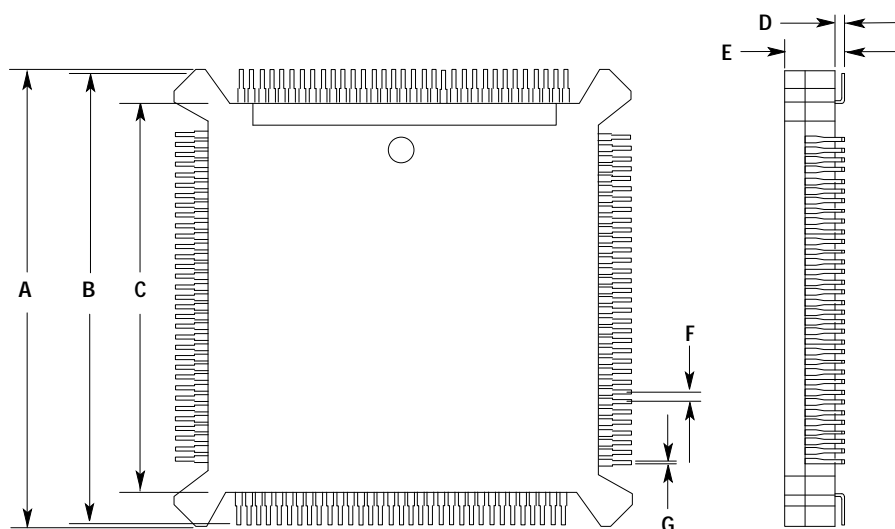
The Hewlett-Packard HP E5337A Intel 80960J-series 132-pin PQFP adapter provides an easy way to connect a Hewlett-Packard logic analyzer preprocessor to an Intel 80960J-series microprocessor in a 132-pin 0.65 mm pitch Plastic Quad Flat Pack (PQFP) or Ceramic Quad Flat Pack (CQFP) surface-mounted device. The HP E5337A probe head aligns with the corner “bumpers” on the PQFP package and slips down over the device to be probed, and extendable clips then retract under the device to provide mechanical stability. When a CQFP device is to be probed, a small adapter must be glued onto the surface of the CQFP device to provide macro-alignment. One of these adapters is supplied with each E5337A; additional adapters are available by ordering part number 5081-7709 (package of five). The preprocessor plugs into a PGA socket at the other end of the HP E5337A.



**Figure 1. HP E5337A
Intel 80960J-series
132-pin PQFP probe
adapter**

Key Specifications

H



A		B		C		D		E		F	G	
Bumper Distance		Terminal Distance		Package Body		Standoff		Package Height		Pitch	Lead Dimension	
Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Typical	Min	Max
1.097 in	1.103 in	1.070 in	1.090 in	0.947 in	0.953 in	0.020 in	0.040 in	0.160 in	0.180 in	0.025 in	0.008 in	0.012 in
27.86 mm	28.01 mm	27.18 mm	27.69 mm	24.05 mm	24.21 mm	0.51 mm	1.02 mm	4.06 mm	4.57 mm	0.635 mm	0.20 mm	0.31 mm

Figure 2. Chip dimension requirements

Chip Dimension Requirements

The HP E5337A probe adapter will successfully probe 132-pin CQFP or PQFP devices that conform to the dimensions in Figure 2.

Electrical Characteristics

The HP E5337A adds approximately 13 pF maximum loading to the device being probed.

Included Accessories

The HP E5337A includes one PGA interface board, one probe adapter, and one CQFP package adapter.

Supported HP Products

The HP E5337A is designed to work with the HP E2464A 80960J-series logic analyzer preprocessor.

Clearance Area Requirement

The user must provide a clearance area of 2.54 mm (0.1 in) around the perimeter of the chip regardless of which locator base is used, as shown in Figure 3. No components can be mounted in this area.

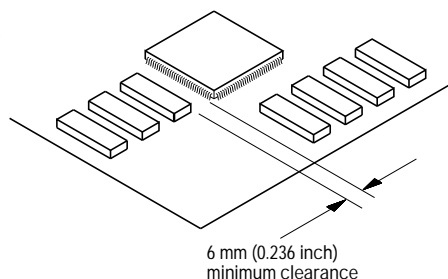


Figure 3. Clearance area requirements

For more information, call your local HP sales office listed in your telephone directory, or an HP regional office listed below:

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