

HP E5346A High Density Termination Adapter

Technical Specification

For use with HP logic analyzers

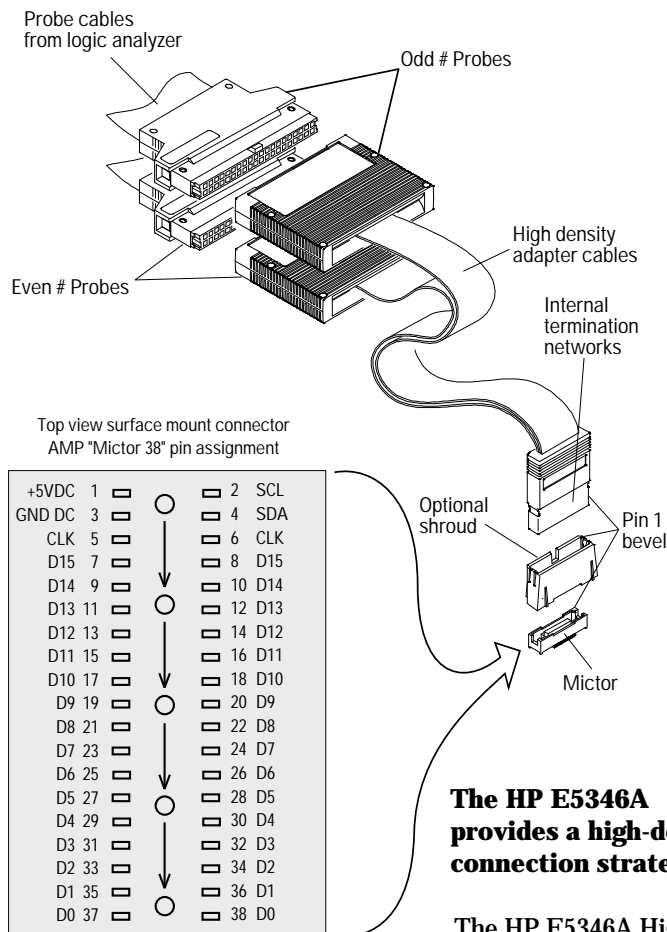


Figure 1:
High Density Termination Adapter
AMP P/N 2-767004-2

The HP E5346A provides a high-density connection strategy

The HP E5346A High Density Termination Adapter provides a convenient and easy way to connect an HP logic analyzer to your target system. With packages difficult to probe such as BGA and TQFP, the HP E5346A provides an alternative to directly probing the chip.

The HP E5346A provides a high-density connection strategy to

route your important signals to the HP logic analyzer. Simply design the AMP Mictor connectors onto the board for the critical signals you need. This process consumes a minimal amount of board space.

The adapter provides 32 channels of logic analysis per connector with 2 clocks. The termination for the logic analyzer is located right at the probe tip for the most convenient and best measurement results.

Signal Line Loading

Any probed signal line must be able to supply a minimum of 600 mV to the probe tip and handle a minimum loading of 90KOhms shunted by 10 pF. The maximum input voltage for the logic analyzer is +/- 40 volts peak.

Optional Accessories

An optional support shroud (HP E5346-44701) is available to provide additional strain relief between the HP E5346A and the AMP Mictor connector. Five shrouds are included with five AMP Mictor connectors in the HP E5346-68701. The shroud is mounted directly to the board and fits around the AMP Mictor connector.

Two holes are required to be designed into the PC board as shown in figure 5 to provide a rugged mechanical connection for the shroud.

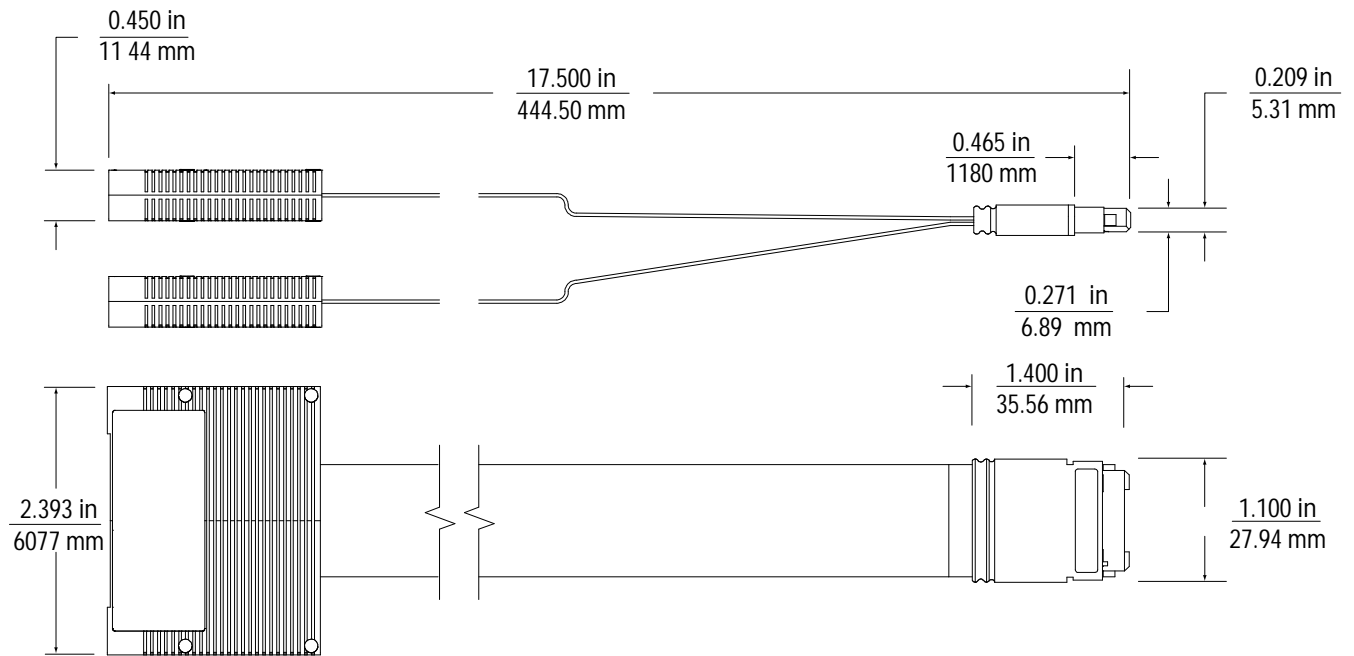


Figure 2: E5346A Dimensions

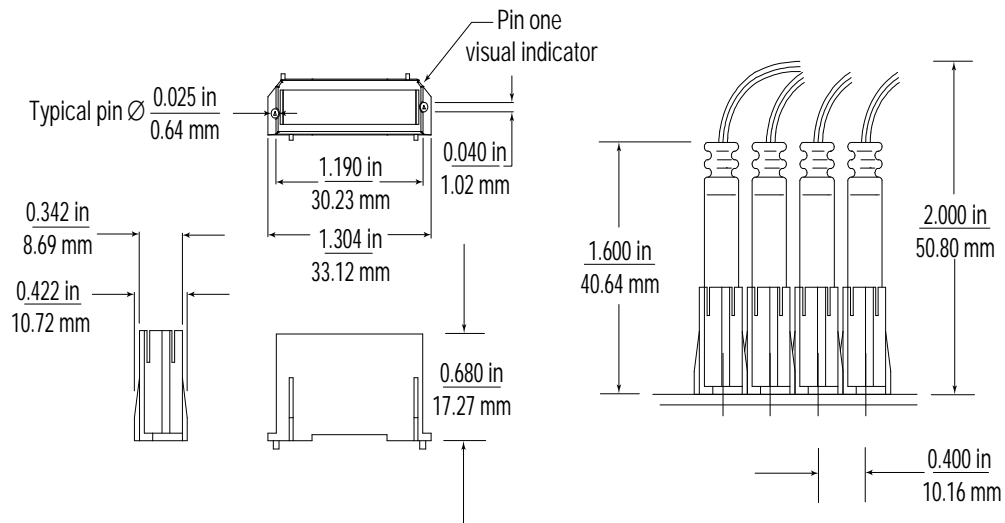


Figure 3: E5346-44701 Support shroud Mechanical Information

Pin Assignments

Each adapter is used with two logic analyzer pods. The odd-numbered pod corresponds to the even-numbered signal pins on the AMP Mictor connector as shown in figure 4. The clock signal and data signals for each logic analyzer pod are also shown in figure 1. The signals +5VDC, SCL and SDA are not used for probing and should not be connected to the target system. The AMP Mictor connectors should be placed as close as possible to the source of the signal to minimize stub length and make a more reliable measurement.

Ordering Information

- HP E5346A HighDensity Termination Adapter

Related HP literature

- *Minimizing Intrusion Effects when Probing With a Logic Analyzer*
Pub#:5962-8620E

+5VDC	1	□	□	2	SCL
GND DC	3	□	□	4	SDA
CLK	5	□	□	6	CLK
D15	7	□	□	8	D15
D14	9	□	□	10	D14
D13	11	□	□	12	D13
D12	13	□	□	14	D12
D11	15	□	□	16	D11
D10	17	□	□	18	D10
D9	19	□	□	20	D9
D8	21	□	□	22	D8
D7	23	□	□	24	D7
D6	25	□	□	26	D6
D5	27	□	□	28	D5
D4	29	□	□	30	D4
D3	31	□	□	32	D3
D2	33	□	□	34	D2
D1	35	□	□	36	D1
D0	37	□	□	38	D0

Figure 4: Mictor 38 pin assignment

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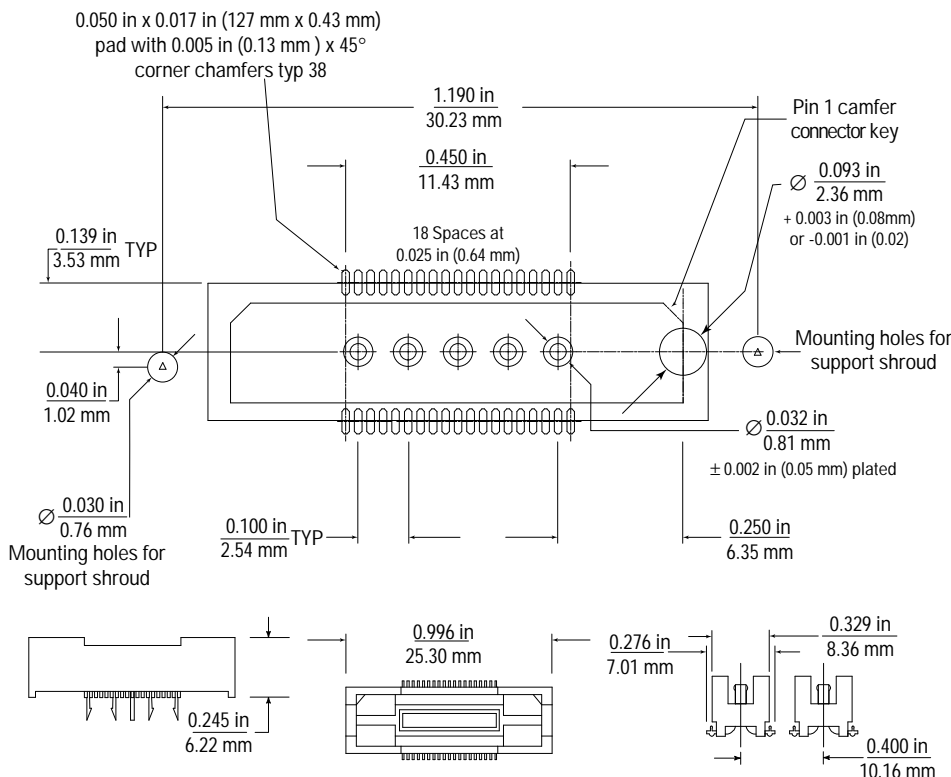


Figure 5: AMP Mictor Connector Mechanical Information
AMP P/N 2-767004-2