

4-Channel Counter/Totalizer HP E1332A

Technical Specifications

- Frequency range 4 MHz
- Seven counter functions
- Programmable direct or isolated inputs
- Programmable digital input filter and trigger levels
- Two input voltage ranges



Description

The HP E1332A counter/totalizer is a **B-size**, **1-slot**, **register-based VXI device**. It provides seven counter functions: frequency (up to 4 MHz), period average, pulse width, time interval, totalize, gated totalize, and up/down count.

With this module, you select either isolated or direct inputs through software commands. You can connect a total of eight channels to one counter card (only four can totalize simultaneously) by multiplexing between four isolated channels and four nonisolated channels. The digital input low-pass filter is also software selectable with Pass Frequency in 16 binary steps from 4 Hz to 131 kHz. It filters out the high-frequency noise of input signals, such as the bounce of mechanical switches.

Refer to the HP Website directory of addresses (URLs) for instrument driver availability and downloading instructions.

Frequency Measurement

Channels 1 and 3 measure frequency up to 4 MHz (2 and 4 not available). You can select the resolution directly in Hz or the gate time from 2 ms to 65.5 s in 16 binary steps.

Minimum Pulse Width: 125 nsResolution: 1/Gate time

Period Average Measurement

Channels 1 and 3 (2 and 4 not available) average $2^{\mathbb{N}}$ periods of an input signal. Select the resolution directly in seconds or the number of periods to be averaged.

• Minimum Pulse Width: 125 ns

• Range of N: 1 to 16

• Resolution: 1 / (5 x 10⁶ x 2^N) s

Pulse Width Measurement

Channels 2 and 4 (1 and 3 not available) measure pulse width (positive or negative) of an input signal.

Minimum Pulse Width: 500 nsMaximum Pulse Width: 858 s

• Resolution: 200 ns

Time Interval Measurement

You can measure the time interval between transitions from channel 1 to channel 2 or from channel 3 to channel 4. You select the rising or falling edge via software commands.

Minimum Interval: 500 ns
Maximum Interval: 858 s
Resolution: 200 ns

Totalizing

You can count the number of transitions (rising or falling edge) on channels 1, 2, 3 and 4.

Minimum Pulse Width: 125 ns
Range: 1 to (2³²-1) Counts

Gated Totalizing

You count the number of transitions (rising or falling edges) on channels 1 and 3. Channel 2 is used as a gate for channel 1. Channel 4 is used for channel 3. The polarity of the gate is programmable.

Minimum Pulse Width: 125 ns
Range: 1 to (2¹⁶-1) Counts

Up/Down Counting

Channels 1-2 and channels 3-4 form the up/down pairs. The count on channel 2 (4) is subtracted from that on channel 1 (3), and the result is given.

• Minimum Pulse Width: 125 ns

• Range: ± (2³¹-1)

Input Signal Conditioning commands control all channels simultaneously.

Trigger level/sensitivity commands are available for each channel.

Specifications

Functions		
Period:	Yes	
Time interval:	Yes	
Totalizer:	Yes	
Gated totalizer:	Yes	
Ratio:	No	
Pulse width:	Yes	
Rise/fall time:	No	
Phase:	No	
Vdc:	No	
Vac:	No	
Up/down counter:	Yes	
Number of channels:	4	
Frequency:	4 MHz to 0.002 Hz (input filter is OFF)	

Pass Frequency of Input Digital Filter

Pass frequency: 4 Hz to 131 kHz in 16 binary steps

Time Base

Frequency: 10 MHz
Initial accuracy: 2 ppm
Aging: 2 ppm/year
Temperature drift: 5 ppm (0 to 50 °C)

Nonisolated Input

Input impedance: (typical)100 k Ω shunted by 80 pF

Input Ranges (jumper selectable)

Low range: $\pm 5 \text{ V}$ **High range:** $\pm 42 \text{ V}$

Trigger Level

Low input range: -2.56 to 2.54 V in 20 mV step High input range: -25.6 to 25.4 V in 0.2 V step

Sensitivity

Low input range:

DC to 2 MHz: 25 mV 2 MHz to 4 MHz: 50 mV High input range: DC to 100 kHz: 250 mV 100 kHz to 1 MHz: 500 mV 1 MHz to 2.5 MHz: 1 V 2.5 MHz to 3.5 MHz: 2 V

Frequency Dynamic Range

Low input range:

DC to 2 MHz: 43 dB
2 MHz to 4 MHz: 37 dB
High input range:
DC to 100 kHz: 35 dB
100 kHz to 1 MHz: 29 dB
1 MHz to 2.5 MHz: 23 dB
2.5 MHz to 3.5 MHz: 17 dB

Isolated Input

Isolation: 170 Vp (Channel-to-channel, channel-to-

chassis)

Screw Terminal Wire Size

Maximum: 16 AWG (1.5 mm)

VXI Characteristics

VXI device type: Register-based

Size: B
Slots: 1
Connectors: P1
Shared memory: n/a
VXI busses: n/a
C-size compatibility: Yes

Instrument Drivers

See the HP Website (http://www.hp.com/go/inst_drivers) for driver availability and downloading.

Command module firmware: ROM **Command module** A.01 firmware rev: I-SCPI Win 3.1: Yes I-SCPI Series 700: Yes C-SCPI LynxOS: Yes C-SCPI Series 700: Yes **HP VEE Drivers:** Yes VXIplug&play Win Framework: No VXI plug&play Win95/NT

Framework:

VXI*plug&play* HP-UX Framework:

No (not available at time of publication)

Module Current

	I _{PM}	I _{DM}
+5 V:	0.5	0.01
+12 V:	0.03	0.01
−12 V:	0.02	0.01
+24 V:	0	0
−24 V:	0	0
−5.2 V:	0	0
−2 V:	0	0

No

Cooling/Slot

Watts/slot: 3.00 Δ P mm H₂O: 0.05 Air Flow liter/s: 0.25

Ordering Information

•	
Description	Product No.
4-channel counter/totalizer	HP E1332A
Service manual	HP E1332A 0B3
Mil std 45662A calibration w/ test data	HP E1332A 1BP
Japan - Japanese localization	HP E1332A ABJ
3 vr. retn. to HP to 1 vr. OnSite warr.	HP E1332A W01

Data Subject to Change Copyright © January 1997 Hewlett Packard Co.

HP Publication No.: 5965-5546E