

# V1000/V2100 User and Programmer Training

#### Technical Data

#### **Course Overview**

This Course teaches you how to write test programs for VK1000 and VK2100 test systems.

Lectures focus on test program development and debug, and also present pertinent details of the tester hardware and operating system software.

Daily hands-on laboratory work includes writing your own test program code for parametric and functional tests.

#### **Course Features**

- VK Test System Overview
- Tester Hardware Overview
- Tester Operating System Software Overview
- Test Program Structure
- Test Program Commands
- Test Program Compilers
- · VOS Terminal Commands
- Plan and Configuration Files

#### **Specifications**

Course Length 5 days

#### Audience

VK test program, test program developers and test engineers.

#### **Prerequisites**

Familiarity with PC Operation, DOS, and C programming language; basic semiconductor device testing knowledge.

#### **Delivery Method** Classroom, Dedicated

#### **Format**

Approximately 40% lecture and 60% hands-on labs and exercises.

#### HP Education Services: Your Key to Higher Productivity

### Classroom Training Benefits

### Experienced HP Instructors

Learn from an experienced HP instructor who is an expert in using and applying instrument systems to meet your measurement needs.

### Comprehensive Student Materials

Receive valuable references to assist you when you are back on the job.

#### Available at HP Classrooms or Your Site

Take advantage of HP's learning facilities, equipment, and interactive learning environment by attending class at one of HP's education centers. Or, save travel expenses and time by organizing a dedicated class at your location.

### Regularly Scheduled Classes

Plan training months in advance.

Course Number: E7082A+24D (Scheduled)

E7082A+24A (Dedicated)

## V1000/V2100 User and Programmer Training (E7082A+24D)

#### Detailed Course Agenda

When you complete this course you will be able to operate V1000 and V2100 test systems. You will also be able to read, debug and write test programs for V1000 & V2100 test system.

#### Day 1

- System Introduction
- System Architecture Overview
- Programmable Power Supplies
- · PMU, PPMUs, and ADC
- Example Parametric Tests
  Day 2
- Tester Software Overview
- Plan and Configuration Files
- Test Program Overview
- SHELL.C
- Test Program Compilers
- Basic System Operation
- · Lab Project 1

#### Day 3

- V1000 Pin Electronics
- Pattern Generator: Vector Mode
- Example Functional Tests
- More VOS Terminal Commands
- Lab Project II

#### Day 4

- Pattern Generator: APG Mode
- · Buffer Memory
- · Error Catch RAM
- More VOS Terminal Commands

#### Day 5

- V2000 Pin Electronics
- · Lab Project III
- Wrap-Up

#### Other Information

This class covers operations of the V1000 Pin Electronics, V2000 Pin Electronics. 64k-Vector Pattern Generator Board, 8-Megabit **Buffer Memory & Error** Catch RAM Board, and Standard Programmable Power Supply & PMU Board. It is a necessary introduction to, but not a complete description of the 256k-Vector Pattern Generator, 32-Megabit Buffer Memory & Error Catch RAM Board, and **High-Resolution** Programmable Power Supply & PMU Board

#### **Ordering Information**

To order the V1000/V2100 User and Programmer Training (E7082A+24P) course in the U.S., call 1-800-HPCLASS (800-472-5277).

HP's Customer Registration Center can provide you with price, scheduling, and enrollment information, as well as provide information about dedicated delivery (E7082A+24A).

Outside the U.S., contact your nearest local HP sales office.

#### Region Sales Headquarters:

#### **United States:**

Hewlett-Packard Company Test and Measurement Organization 5301 Stevens Creek Blvd. Bldg. 51L-SC Santa Clara, CA 95052-8059 (408) 246-4300

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