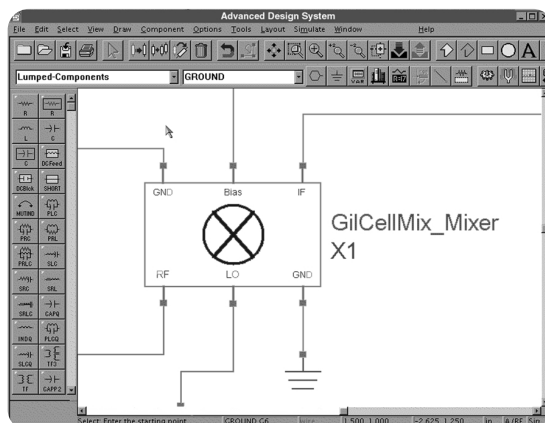
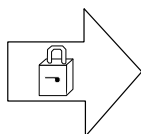
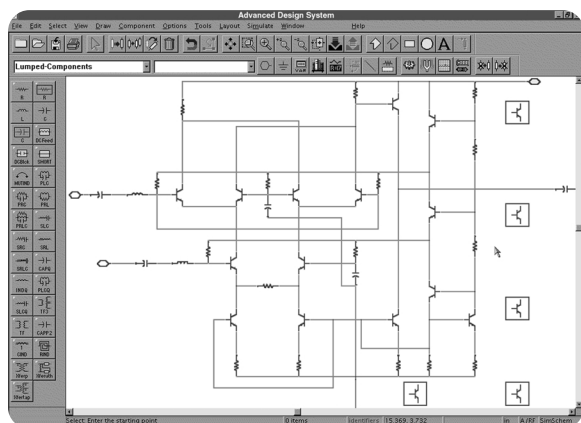


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E8894A/AN RF IP Encoder

Product Overview



Features at a Glance

- Lets you create encoded models containing Advanced Design System circuit, system, and DSP library components
- Fully integrated into the Advanced Design System user interface
- Protects Intellectual Property with fixed-key, 56-bit DES algorithm
- Easy to use and install
- Lets you use encoded models with all Advanced Design System simulators

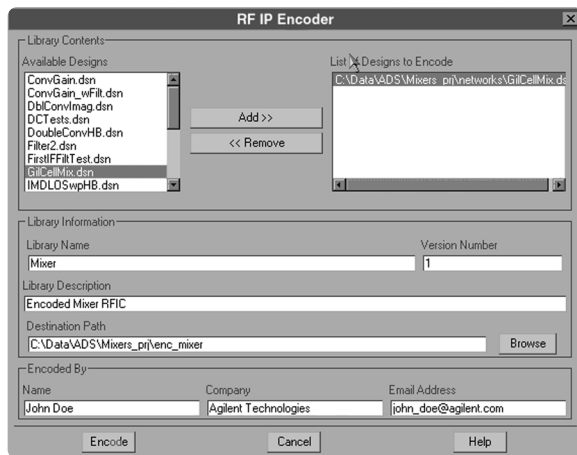
The E8894A/AN RF IP Encoder from Agilent EESof EDA generates highly detailed models of RF Integrated Circuits (RFICs), and protects the intellectual property contained in the designs. The RF IP Encoder lets you reduce time to market by integrating RFICs into wireless communication systems early in the design cycle, and by providing the ability to quickly adapt your products to changing customer specifications.

You can share the models you generate with the RF IP Encoder with RF system and board designers, allowing design and verification of wireless communication systems. Conditions are realistic, because the models are generated by encoding the transistor-level netlist of the circuit. The resulting encoded models let you simulate and analyze a complete system, taking into account the effects of RF board interconnects, signal propagation, nonlinear distortion, noise, and so on.

With the breadth of simulation technologies available in Advanced Design System, you can characterize the performance of the IC using realistic input signals and perform a wide range of measurements.



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Using the RF IP Encoder

You can generate models with the RF IP Encoder through a simple user interface that is completely integrated into the Advanced Design System environment. Just select one or more circuits, including design parameters. The circuits you select are then encoded using a fixed-key, 56-bit DES algorithm and packaged in a self-contained file that you can distribute electronically to any other Advanced Design System user.

The model recipient can easily install the file in their copy of Advanced Design System. No additional license is required to install an encoded library. The recipient can then access the models from the component palette and from the library browser and place them on the Advanced Design System schematic.

You can use encoded models to build complex circuits in combination with any component from the standard Advanced Design System libraries. During simulation, the encoded models are dynamically decoded and are available for use with all the circuit, system, and DSP simulators that are available in Advanced Design System, including co-simulation.

For example, the RF IP Encoder lets you combine encoded RFIC models with microstrip elements and SMT parts to simulate an entire RF Board for a mobile phone.

Security

Although encoded files are designed to provide you with the best possible security, we recommend that you share models only under the protection of a Non-Disclosure Agreement. We further recommend that you use a private medium to distribute models and files.

Product Configuration

The E8894A/AN is a companion product for Advanced Design System and requires the E8900A/AN Design Environment.

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