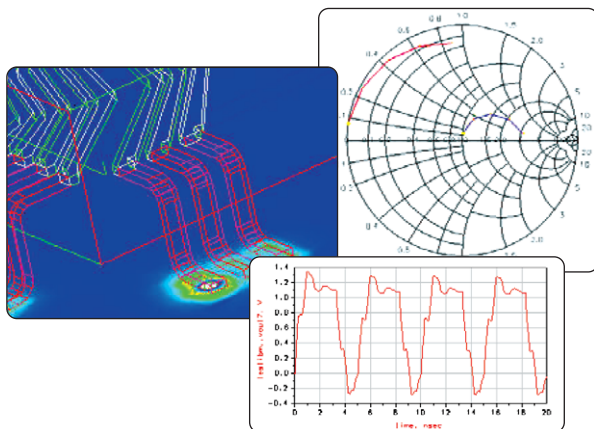


Agilent EEsof EDA

Device Modeling Services

IC Package Modeling Service



Leverage the expertise of Agilent EEsof EDA's Device Modeling Services to make your job easier and get your products to market faster and more economically.

With digital circuits clocking at higher rates and analog devices operating at higher frequencies, parasitics due to the IC packages and connectors can no longer be ignored.

Agilent EEsof EDA's package modeling service enables you to extract parasitics (bond wires, leadframes, etc.) from real-life measurements and helps you make decisions on suitable packages to avoid signal integrity issues at the design phase.

Capabilities At a Glance

- Accurate, fast turnaround, cost competitive
- Wide range of RF probes and fixtures available for various packages (QFP, BGA, etc.)
- Includes capacitance/inductance matrix, bond wire inductance and ground parasitic network descriptions

Other Modeling Services

- RF Test Structure Design
- Noise Characterization (1/f and Noise Figure)
- RF CMOS Modeling
- Passive Component Modeling
- Bipolar Transistor Modeling
- MESFET/HEMT Modeling
- Statistical/Corner Modeling
- Semiconductor RF Characterization

First Order Model

- ✓ Self/Mutual inductance and capacitance
- ✓ Measured S-parameters of critical pins
- ✓ For quick estimation of package performance

Equivalent Circuit Model

- ✓ Provides guidance for package/connector improvement
- ✓ For first-cut packaged RFIC design



Agilent Technologies

Contact your Agilent EEsof EDA field sales engineer for more information about Device Modeling Services.

For more information about EEsof's support and services offerings visit:
<http://eesof.tm.agilent.com/support>

For more information about Agilent EEsof EDA, visit:
www.agilent.com/find/eesof

For more assistance with your test and measurement needs visit:
www.agilent.com/find/contactus

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