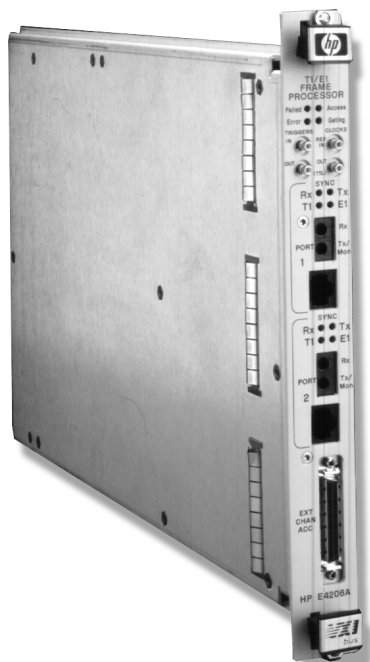


# T1/E1 Frame Processor

Agilent Technologies Broadband Series Test System

E4206A



## Product Features

- Dual Ports (two interfaces, two application processors)
- Frame-based operation with 20+ real-time measurements
- Switchable between T1 and E1
- Channelized/unchannelized/fractional operation
- Bit mask provides for flexible bit stream selection (56/64 kbps, n X 56/64 kbps, etc.)
- Two timeslots (actually bit streams) can be routed to two Application Processors
- Sophisticated, high performance Traffic Generation capability.

The Agilent Technologies E4206 T1/E1 Frame Processor is a high-performance hardware module that tests frame-based protocols at speeds up to 2 Mb/s. Two integrated T1/E1 interfaces and internal RISC-based protocol test engines can each monitor, capture traffic, and generate statistics -- even with heavily loaded links and short frame lengths. The E4206 can also generate alarms and framing errors, and provide sophisticated traffic generation functionality when used with optional test software applications.

The two physical ports and internal protocol test engines can be combined in different configurations to achieve several different test configurations. Each protocol test engine works with one channel of user-selectable timeslots. In dual-port mode, each physical port is connected to a protocol test engine for independent testing. In pass-through mode, one channel from one port is tested while other timeslots can be passed through to the other port. In dual-channel mode, two separate channels from one physical port can be tested; the second physical port is not used in this configuration.

E4206A supports unchannelized, channelized, and fractional T1/E1 operation. This is accomplished by providing a flexible hyperchannel selection scheme where n timeslots can be merged into one stream (n is 1 to 24 for T1, or 1 to 30 for E1), and a flexible substrate selection scheme where m 8Kb/s multiples can be selected (m is 1 to 8 for 8Kb/s to 64Kb/s selection).

A companion product, the E4207A V Interface Frame Processor, is functionally similar to the E4206 but has integrated interfaces which support V.11, V.28, V.35, V.36 and EIA 530 physical connections.



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## Key Features

### Real-Time Dual-Port Monitoring and Analysis

Powerful dual-port testing monitors both sides of even heavily-loaded links in real-time. You can view frames in real-time, or capture them for further analysis. The BSTS captures and decodes communications traffic into an English-language display using the same terminology found in standards documents. Errors are automatically detected and highlighted on-screen, complete with explanatory messages. Timestamps correlate events between ports. Sophisticated filters and triggers let you view only traffic of interest, and catch intermittent events. The E4206 has everything you need to see exactly what happened, and when.

### High-Performance Traffic Generation

Generates up to eight streams with individually-selectable distributions. For load generation purposes, the E4206 is scalable in two-port increments by adding extra modules up to a maximum of five modules (10 ports) per BSTS E4200 chassis, or a maximum of seven modules (14 ports) per BSTS E4210 chassis.

### Extensive Real-Time Measurements

Makes over 20 different real-time measurements including frame rate, minimum and maximum frame size, and framing errors at the physical layer.

### Test Both Sides of Frame based/ATM Interworking Devices

Combine the E4206 with other modules from the BSTS's extensive range of Line Interfaces and Test Software applications, to test both sides of a frame based device / ATM interworking device or function -- on one tester!

### Friendly User-Interface Makes Complex Testing Easy

The state-of-the-art graphical user interface makes it easy to set up, run, save and restore tests. Includes a C-language user programming environment to automate testing or create extremely complex test scenarios.

## Typical Applications

Equipment manufacturers and network operators who provide frame based equipment or frame based/ATM interworking devices need to verify that:

- Protocol implementations are functionally verified as meeting design specifications and interoperating standards
- Equipment and services are stress-tested to verify that they perform well under heavy loads, especially those which result from conditions such as very short frame lengths

The combination of an E4206 Frame Processor and E4216 Frame Relay Test Software, E4213 SMDS DXI Test Software, or E6275 FUNI Test Software facilitates testing these aspects through error isolation and traffic-generation functions.

## User Programming Environment

You can automate repetitive testing or create complex test scenarios by developing your own programs with the UNIX®-based C-language programming environment included with the BSTS.

Simply link your programs to the supplied library of test routines. The user programming library provides programmatic support of all functions available through the graphical user interface. In-depth user and programmers' manuals document test software features and the test routine libraries.

## **Configuration & Use With Other BSTS Line Interfaces, Hardware Modules & Test Software**

The E4206 T1/E1 Frame Processor requires a BSTS chassis with UNIX® controller and optional Frame Based Test Software (Frame Relay, FUNI, SMDS DXI) to perform the tests described in this datasheet.

The E4206 includes two integrated T1/E1 physical interfaces, so line interface modules are not required. The front panel has RJ-48 and mini-bantam connectors; unless noted otherwise, these cables are in a Y configuration with a male RJ-48 connector at one end, and both male and female versions of the adapted connector at the other end.

A companion product, the E4207A V Interface Frame Processor, is functionally similar to the E4206 but has integrated interfaces which support V.11, V.28, V.35, and EIA 530 physical connections.

## **Warranty & Support Options**

### **Hardware**

All BSTS hardware components are warranted for a period of 3 years. Products must be returned to an authorized Agilent service center for service. At the time of purchase, you may select warranty option W01, a no-charge option which converts the standard 3-year return to Agilent warranty to a 1-year on-site warranty.

### **Software**

Agilent Broadband Series Test System software and firmware products are supplied on transportable media such as disk, CD-ROM or integrated circuits. The warranty covers physical defects in the media, and defective media is replaced at no charge during the warranty period. When installed in an Agilent Broadband Series Test System, the software/firmware media has the same warranty period as the product.

## Product Numbers

- E4206A T1/E1 Frame Processor
- E4207A V-Interface Frame Processor
- E4216A B-ISDN Frame Relay Test Software
- E4209B 0-155 Mb/s Cell Protocol Processor (CPP)
- E4200B BSTS Form-7 Transportable Chassis
- E4210B BSTS Form-13 Mainframe Chassis
- E4118A #008 RJ-48 M/M/F T Monitor Cable
- E4118A #010 Bantam M/M/F T Monitor Cable
- E4118A #015 DB-15/RJ-48/DB-15 M/M/F T Monitor Cable Jpn
- E4118A #101 RJ-48/DB-9/DB-9 M/F/M T Monitor Cable
- E4118A #102 Mini Bantam/BNC T Monitor Cable
- E4118A #104 RJ-48 to RJ-48 (Japan) M/F Adaptor Cable (Japanese pinout, female RJ-48 connector only)
- E4213B B-ISDN DXI Test software
- E6275A B-ISDN FUNI Test Software

## Technical Specifications

### Real-Time Dual-Port Monitoring

Multiport Monitoring	<ul style="list-style-type: none"> <li>Dual-port mode</li> <li>Dual-channel / single-port mode</li> <li>Pass-through mode</li> <li>Synchronized timestamps correlate events from two physical ports</li> <li>Protocol viewer works with live traffic or plays back captured data</li> <li>4 MB capture buffer per port</li> </ul>
Modes	<ul style="list-style-type: none"> <li>Passive monitor</li> <li>Network termination (emulate network)</li> <li>Terminal equipment (emulate user)</li> </ul>
Clock Sources	<ul style="list-style-type: none"> <li>Loop (line)</li> <li>Local</li> <li>External clock reference</li> </ul>
Decode Errors	<ul style="list-style-type: none"> <li>Aborted frames</li> <li>Frame does not have an integral number of octets</li> <li>Frame is too large</li> <li>Invalid frame check sequence (FCS) or cyclical redundancy check (CRC-16)</li> </ul>
T1 Alarm Detection	<ul style="list-style-type: none"> <li>Loss of signal (LSL)</li> <li>Loss of synchronization (LOS)</li> <li>Remote alarm indication (RAI)</li> <li>Alarm indication signal (AIS)</li> </ul>
E1 Alarm Detection	<ul style="list-style-type: none"> <li>Loss of signal (LSL)</li> <li>Loss of synchronization (LOS)</li> <li>Signalling all zeroes (SA0)</li> <li>Signalling all ones (SA1)</li> <li>Unframed all ones (UA1)</li> <li>Remote alarm indication (RAI)</li> <li>Distant multiframe alarm (DMF)</li> </ul>
Pattern Matching	<ul style="list-style-type: none"> <li>Passes or blocks frames which match a 64-byte user-defined pattern</li> </ul>
T1/E1 Triggers	<ul style="list-style-type: none"> <li>Loss of signal level (LSL)</li> <li>Loss of synchronization (LOS)</li> <li>Remote alarm indication (RAI)</li> <li>Alarm indication signal (AIS)</li> <li>Distant multiframe alarm (DMF)</li> </ul>

### Trigger Actions

- Start/stop collecting statistics
- Start/stop capture
- Generate a trace statement
- Display a message
- Notify user program
- Pulse external trigger output

### Trigger Controls

- Delayed trigger activation
- Specify delay in frames of 0 to 100 milliseconds

### Channel Selection

- Channelized/unchannelized/fractional operation
- Flexible hyperchannel selection scheme (n timeslots and m subchannels) i.e. n is 24 or 30, and m is 1 to 8 e.g. for 112kps, n=2, m=7 (X 8kbps)

### High-Performance Traffic Generation

#### Traffic Streams and Controls

- Generate up to eight simultaneous streams
- Selectable throughput in kb/s and percent load parameters for each stream
- Constant, burst or random traffic distributions with distribution parameters individually selectable for each stream

#### Traffic Options (Stream 1 only)

- Embed 48-bit timestamps
- Embed 32-bit sequence numbers
- Truncate frame length to specified number of octets
- Increment frame length over a specified range
- Randomly select frame length from within a specified range

#### Error Insertion

- Send aborted frames
- Send non-octet aligned frames
- Invalid frame check sequence (FCS)

#### Alarm Generation

- Remote alarm indication (RAI)
- Alarm indication signal (AIS)
- Distant multiframe alarm (DMF)

## Real-Time Measurements

T1 D4 12 MF Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Ft bit</li> <li>• Fs bit</li> </ul>
T1 D4 4MF Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Ft bit</li> </ul>
T1 ESF Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Cyclical redundancy check (CRC)</li> <li>• Frame pattern sequence framing error (FPS)</li> </ul>
E1 PCM30 CAS Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Frame alignment signal (FAS)</li> </ul>
E1 PCM30 CCS Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Frame alignment signal (FAS)</li> </ul>
E1 CRC4 CAS Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Cyclical redundancy check (CRC)</li> <li>• Frame alignment signal (FAS)</li> <li>• Multiframe alignment signal (MFAS)</li> </ul>
E1 CRC4 CCS Framing Errors	<ul style="list-style-type: none"> <li>• Bipolar</li> <li>• Cyclical redundancy check (CRC)</li> <li>• Frame alignment signal (FAS)</li> <li>• Multiframe alignment signal (MFAS)</li> </ul>
BOP Measurements	<ul style="list-style-type: none"> <li>• Bits per second</li> <li>• Number of frames</li> <li>• Frames per second</li> <li>• Minimum, average and maximum frame length</li> <li>• Number of aborted frames</li> <li>• Number of non-octet-aligned frames</li> <li>• Number of frames matching a user-defined 64-byte pattern</li> <li>• Number of frame check sequence (FCS) errors</li> </ul>

## User Programming

Sample Programs	<ul style="list-style-type: none"> <li>• T1/E1 port setup</li> <li>• Delay measurement</li> <li>• LMI emulation</li> <li>• Lost frame, delay measurements, and payload integrity check for Frame Relay / ATM interworking</li> </ul>
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## Applicable Standards

Physical Layer Standards	<ul style="list-style-type: none"> <li>• ANSI T1.403 Network-to-Customer Installation DS1 Metallic Interface</li> <li>• Bellcore TR-NWT-000170 Digital Cross-Connect System Requirements and Objectives, January 1993</li> <li>• AT&amp;T Publication 62411</li> <li>• ITU Recommendation G.703 (04/91) - Physical/electrical characteristics of hierarchical digital interfaces</li> <li>• ITU Recommendation G.704 (07/95) - Synchronous frame structures used at 1544, 6312, 2048, 8488 and 44 726 Kbit/s hierarchical levels</li> <li>• ITU Recommendation G.742 (1988) - Second order digital multiplexing equipment operating at 8448 Kbit/s and using positive justification</li> <li>• ITU Recommendation G.823 (03/93) - The control of jitter and wander within digital networks which are based on the 2048 kbit/s hierarchy</li> </ul>
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### **Agilent Technologies Broadband Series Test System**

The Agilent Technologies BSTS is the industry-standard ATM/BISDN test system for R&D engineering, product development, field trials and QA testing. The latest leading edge, innovative solutions help you lead the fast-packet revolution and reshape tomorrow's networks.

It offers a wide range of applications:

- ATM traffic management and signalling
- Packet over SONET/SDH (POS)
- switch/router interworking and performance
- third generation wireless testing
- complete, automated conformance testing

The BSTS is modular to grow with your testing needs. Because we build all BSTS products without shortcuts according to full specifications, you'll catch problems other test equipment may not detect.

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