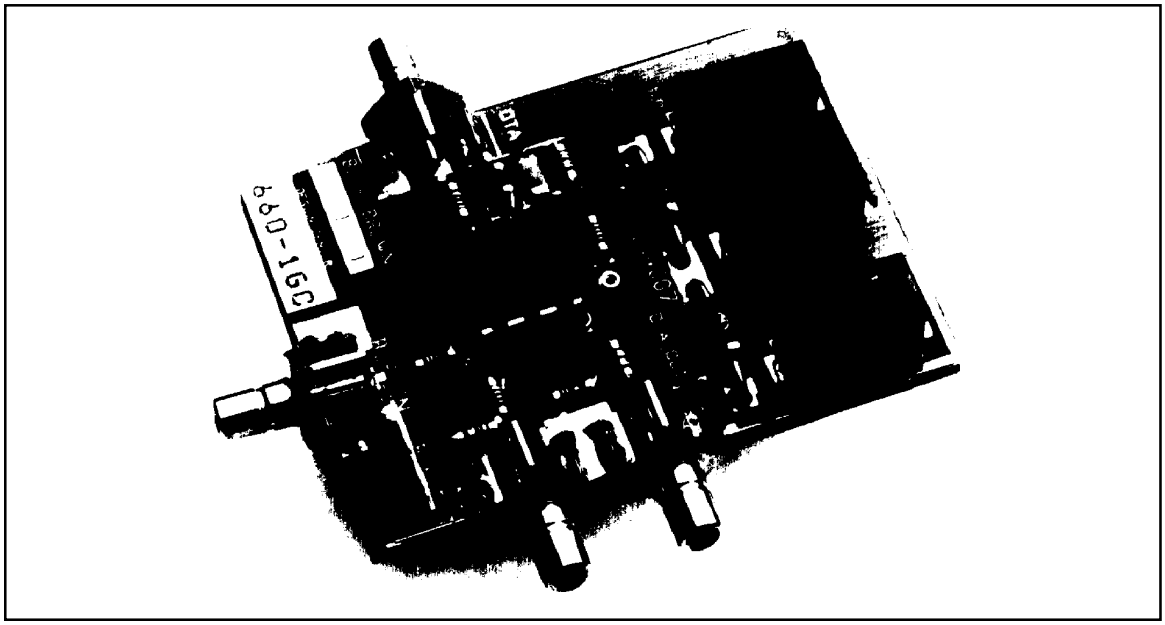
BURR - BROWN®

# DEM-OPA660-1GC
# EVALUATION FIXTURE




## FEATURES

- EASY AND FAST PERFORMANCE TESTING
- SHOWS OPTIMIZED BOARD LAYOUT
- REPLACES SELF-MADE BOARDS

## APPLICATIONS

- COMPONENTS INCOME CONTROL
- PERFORMANCE CHECKS
- CIRCUIT DESIGNS

## DESCRIPTION

The demo fixture DEM-OPA660-1GC allows easy and fast performance testing of the OPA660AP building blocks OTA and buffer stage. Figure 1 shows a diagram of the test fixture, and Figures 2, 3 and 4 show the frequency responses of the OTA stage measured at gains 1, 2, and 10 with various output levels from 0.2Vp-p to 5Vp-p.

Figure 5 illustrates the frequency response of the buffer stage. The silk-screen and the double-sided layout can be seen in Figure 6.

### ABSOLUTE MAXIMUM RATINGS

| | |
|---|---|
| Power Supply Voltage | ±6V |
| Input Voltage | $\pm V_{CC}$, ±0.7V |
| Operating Temperature | –40°C to +85°C |
| Current Output | ±15mA |
| Quiescent Current | ±26mA |

International Airport Industrial Park • Mailing Address: PO Box 11400 • Tucson, AZ 85734 • Street Address: 6730 S. Tucson Blvd. • Tucson, AZ 85706
Tel: (602) 746-1111 • Twx: 910-952-1111 • Cable: BBRCORP • Telex: 066-6491 • FAX: (602) 889-1510 • Immediate Product Info: (800) 548-6132



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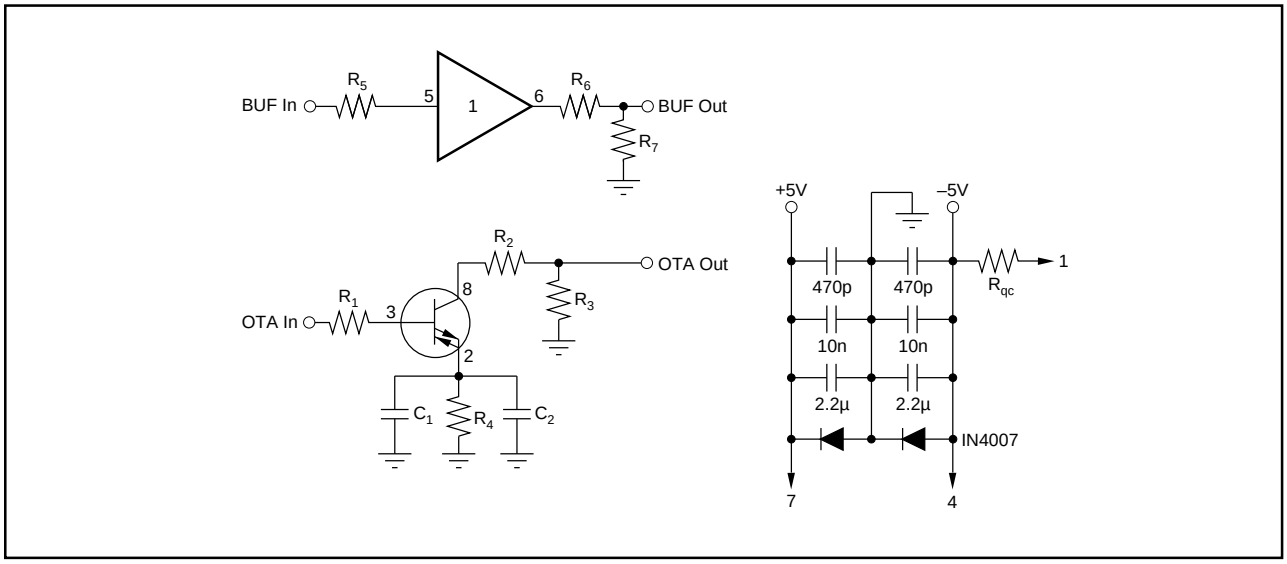


FIGURE 1. Block Diagram of Test Fixture.

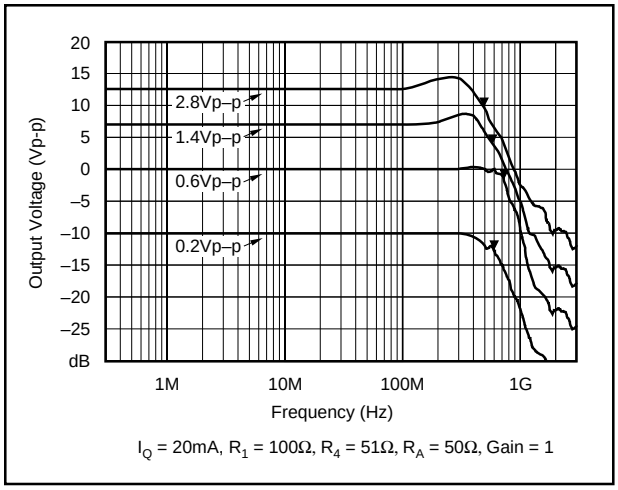


FIGURE 2. OTA Frequency Response, G = 1.

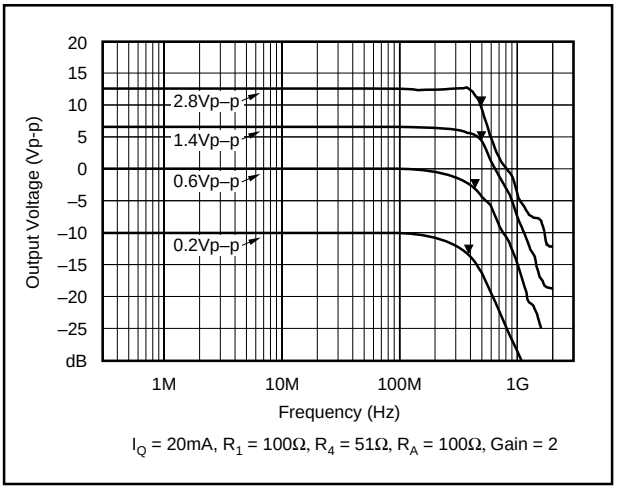


FIGURE 3. OTA Frequency Response, G = 2.

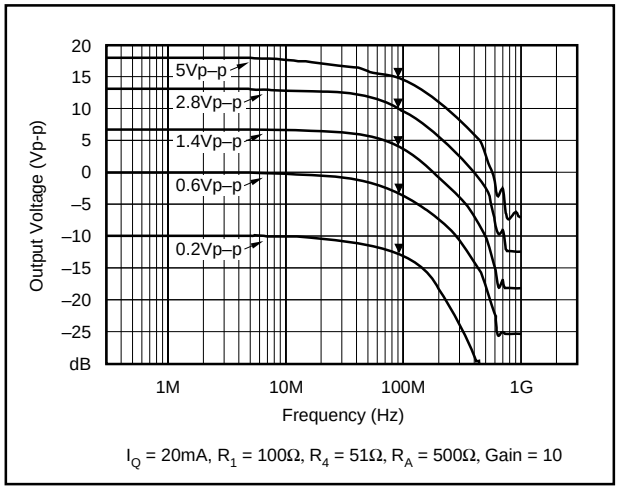


FIGURE 4. OTA Frequency Response, G = 10.

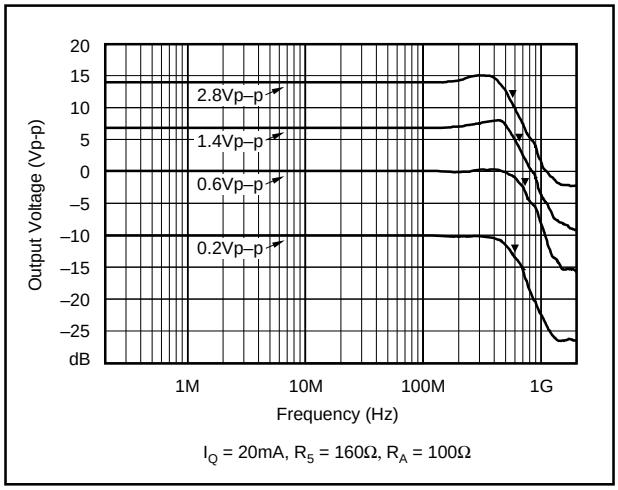


FIGURE 5. BUF Frequency Response.

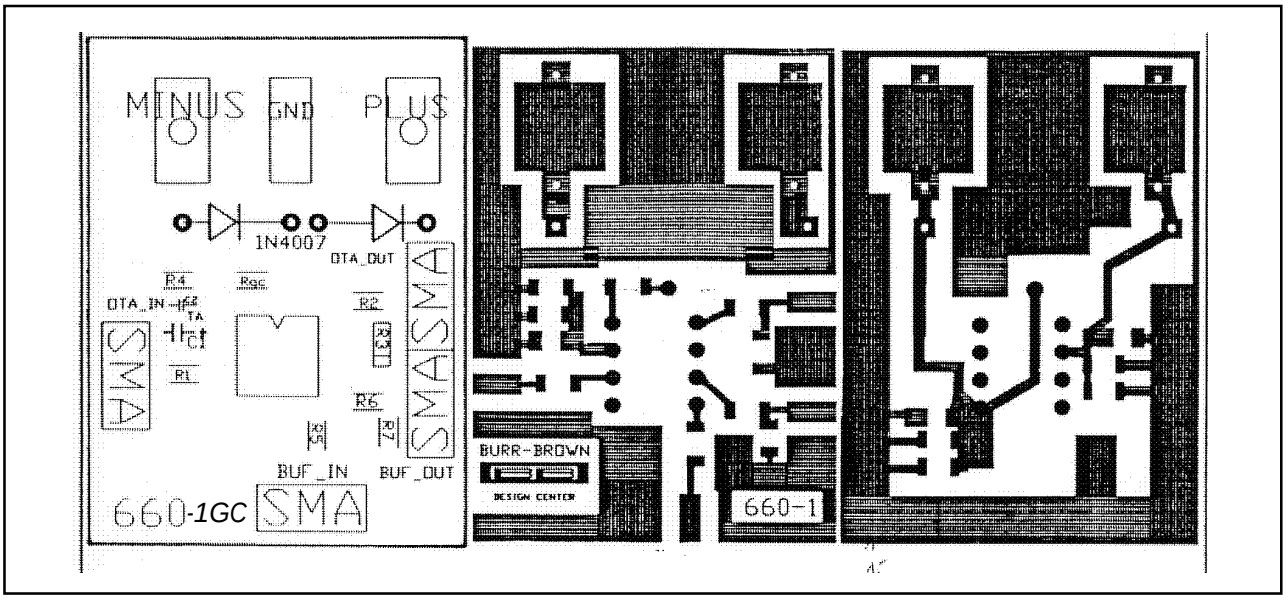


FIGURE 6. Silk Screen and Board Layouts.

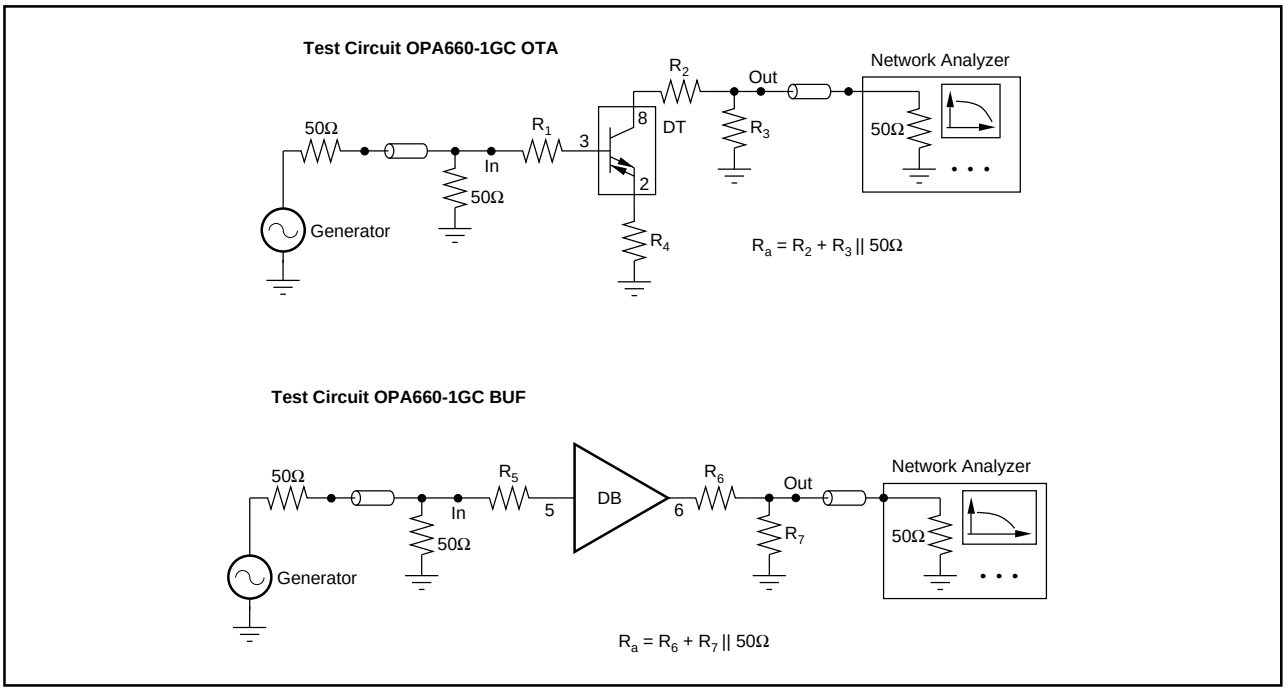


FIGURE 7. Test Configuration for Frequency Response Measurements.

## ORDERING INFORMATION

| MODEL | DESCRIPTION | TEMPERATURE RANGE |
|---|---|---|
| DEM-OPA660-1GC | Diamond Transistor and Buffer | –25°C to +85°C |