

OW Type

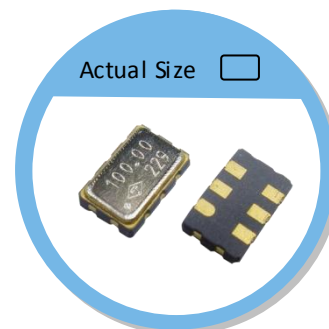
5.0 x 3.2 mm SMD PECL/LVDS Crystal Oscillator

FEATURE

- Typical 5.0 x 3.2 x 1.2 mm hermetically sealed ceramic package
- Very low jitter performance : typical 0.3 pS RMS from 12k-20MHz
- Fundamental/3rd overtone crystal design
- Output frequency up to 320MHz
- Tri-state enable/disable

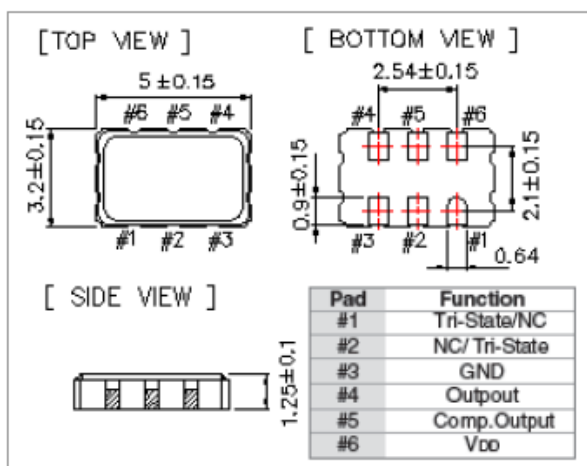
TYPICAL APPLICATION

- 10G-BIT Ethernet, Fiber Channel, Storage Area Network, SONET
- Enterprise Servers, Reference clocks for ADC and DAC
- Telecom

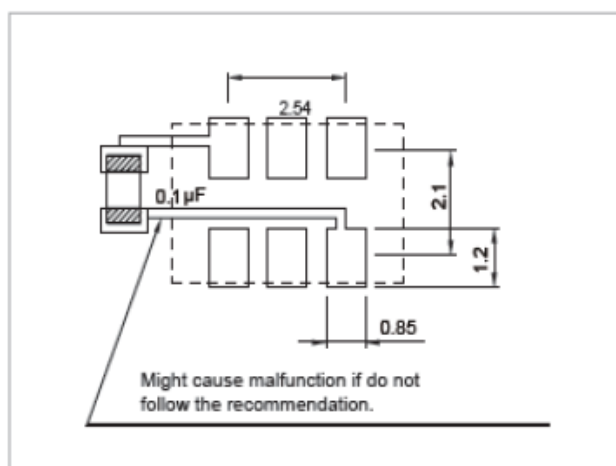


RoHS Compliant Standard

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	PECL				LVDS				Unit
	3.3V		2.5V		3.3V		2.5V		
	Min	Max.	Min.	Max.	Min	Max.	Min.	Max.	
Supply Voltage Variation (VDD) 5%	3.135	3.465	2.375	2.625	3.135	3.465	2.375	2.625	V
Frequency Range	80	320	80	320	80	320	80	320	MHz
Standard Frequency	106.25, 125, 155.52, 156.25, 187.5, 212.5, 312.5								
Supply Current 80MHz ≤ F0 ≤ 160MHz	—	75	—	75	—	50	—	50	mA
160MHz ≤ F0 ≤ 250MHz	—	100	—	100	—	50	—	50	
250MHz ≤ F0 ≤ 320MHz	—	100	—	100	—	65	—	65	
Output Level Output High (Logic “1”)	2.275	—	1.475	—	—	1.6	—	1.6	V

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

+Transition times are measured between 20% and 80% of VDD.

Packing: Tape & Reel, 1000/3000pcs per Reel.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	ppm	±25	±50
-10 ~ +60		△	○
-20 ~ +70		△	○
-40 ~ +85		X	○

* O: Standard △:Conditional X: Not available

*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration