INSTRUMENTATION OP AMPS

Part Number	V_{OSI} Max 25°C (µV)	TC of V_{OS} Max (µV/°C)	I_{BIAS} Max 0-70°C (nA)	e_n Typ (nV/√Hz)	Gain	Gain Error Max (G=10) %	Gain Drift Max ppm/°C	Gain Nonlinearity Max (G=10) ppm	CMRR (G=100) Min dB	I_{SUPPLY} Max/Amp mA	Supply Voltage Min V	Supply Voltage Max V	Rail to Rail I/O	GBW Typ 25°C MHz (G=10)	Slew Min 0-70°C (V/µs)	Packaging	Price 1K Qty	Important Features
LT1167	40	0.3	450	7.5	1 to 10k	0.08	50	10		1.5	4.6	40		0.8	0.65	N8/S8	$3.20	Single R gain programmable. Level 4 ESD with ext. 5k R's
LT1168	40	0.3	350	10	1 to 10k	0.4	200	20		0.53	4.6	40		0.4	0.5	N8/S8	$3.70	Single R gain programmable. Level 4 ESD with ext. 5k R's
LT1789	40	0.3	350	52	1 to 10k	0.25	50	40	100	0.08	2.2	36		0.6	0.2	S8	$3.20	Single R gain programmable. Lowest power IA
LTC1100	10	0.1	65pA	1.9µVPP	100	0.075	4 Typ	20	90	3.3	4.5	16		18		N8/SW16	$6.15	Zero drift, fixed gain of 100
LT1101	160	2	8	0.9µVPP	10 or 100	0.05	4	8	95	0.13	4.6	44		33	0.06	N8/SW16	$4.75	Micropower
LT1102	600	8	40pA	2.8µVPP	10 or 100	0.05	18	14	84	5	10	44		3.5	21	N8	$4.75	JFET input stage
LTC1043									120	8.4	3	18				N18/SW18	$2.85	Precision switched Cap building block
LTC2053	10	50nV/°C	5 Typ	2.5µVPP	any	0.01	N/A	10	105	1.1	2.5	11	yes	0.2	0.2	MS8	$3.20	MSOP package with Zero Drift