

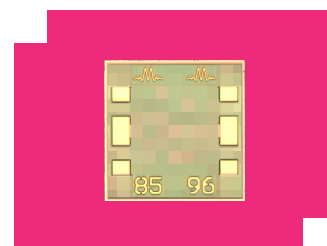
ATN01-0067CH

Passive GaAs MMIC Attenuator Family

DEVICE OVERVIEW

General Description

The ATN01-0067CH is a family of precision GaAs MMIC fixed attenuators. These attenuators are an ideal solution for attenuating a signal and they can be used in a wide range of applications. They are ideal for test equipment's protection and signal overload prevention in various RF systems. A 50-ohm match is maintained over the entire operating frequency range. Attenuation values range from 0dB to 10dB with 1dB steps. ATN-67CH-KIT is an evaluation kit with 5 units of each value.



[Download s-parameters here](#)

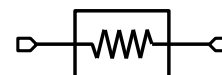
Features

- 0dB to 10dB attenuation values in 1dB steps
- Excellent Return Loss

Applications

- Test Equipment
- Precision Characterization
- High Channel Count Systems
- Amplitude Matching
- 5G transceivers

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Packing Size	Green Status	Product Lifecycle	Export Classification
ATN09-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN07-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN02-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN10-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN04-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN00-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN05-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99

Part Number	Description	Package	Packing Size	Green Status	Product Lifecycle	Export Classification
ATN05-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN-67CH-KIT	Passive GaAs MMIC Attenuator Family	KIT	-	RoHS REACH	Released	EAR99
ATN08-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN03-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN01-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99
ATN06-0067CH	Passive GaAs MMIC Attenuator Family	CH	-	RoHS REACH	Released	EAR99

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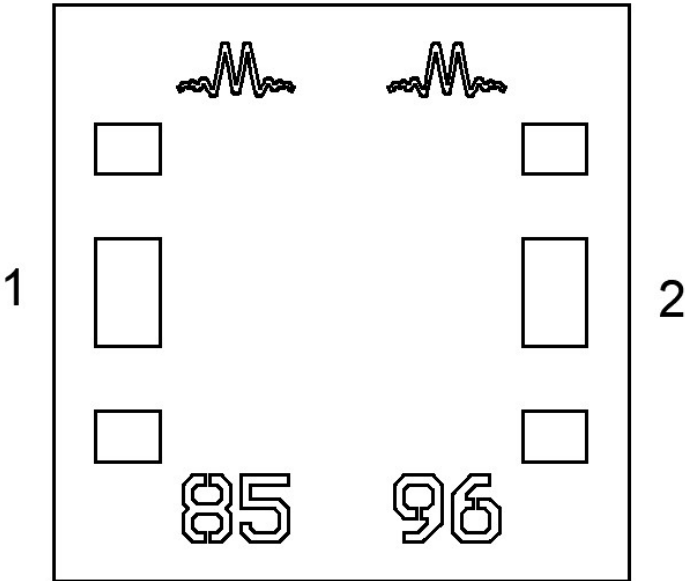
Revision History

Revision Code	Revision Date	Comment
-	2023-05-01	Datasheet Initial Release

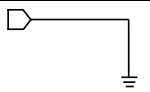
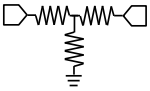
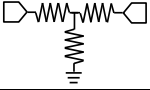
Port Configuration and Functions

Port Diagram

A top-down view of the ATN01-0067CH package outline drawing is shown below. The MMIC attenuators are symmetrical allowing Port 1 or Port 2 to be used as the input.



Port Functions

Port	Function	Description	Equivalent Circuit for Package
Pad	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	Input/Output	Port 1 and port 2 are DC connected to each other and ground through a T-network of resistors.	
Port 2	Input/Output	Port 1 and port 2 are DC connected to each other and ground through a T-network of resistors.	

Specifications

Absolute Maximum Ratings

Parameter	Maximum Rating	Unit
DC Current at any Port	100	mA
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
RF Power Handling	2	W

Package Information

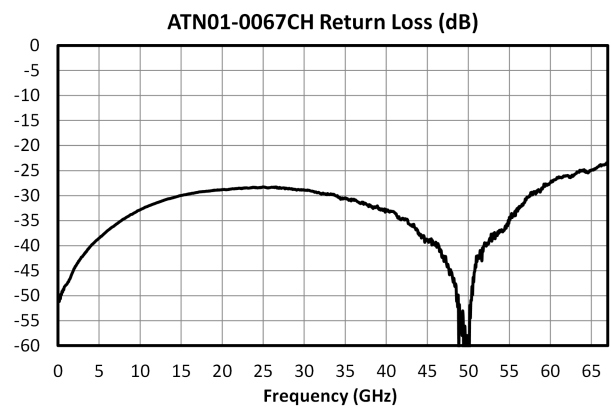
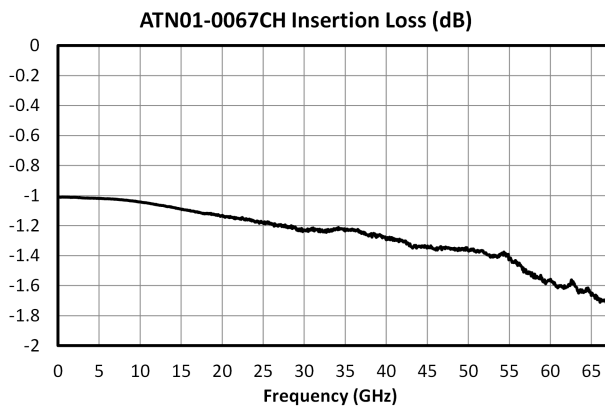
Parameter	Details	Rating
ESD	250 to < 500 Volts	HBM Class 1A
Dimensions	-	0.60 x 0.60 mm

Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for the filter in a CH package with a sine wave input applied to port 1. Min and Max limits are guaranteed at TA=+25°C. All bare die are 100% DC tested and visually inspected.

Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
Attenuation	-	0	67	-	1	-	dB
Attenuation Accuracy	-	0	35	-	0.2	-	dB
Attenuation Accuracy	-	35	67	-	0.7	-	dB
Return Loss	-	0	35	-	30	-	dB
Return Loss	-	35	67	-	33	-	dB
Impedance	-	0	67	-	50	-	Ω

Typical Performance Plots



Die Mounting Recommendations

Mounting and Bonding Recommendations

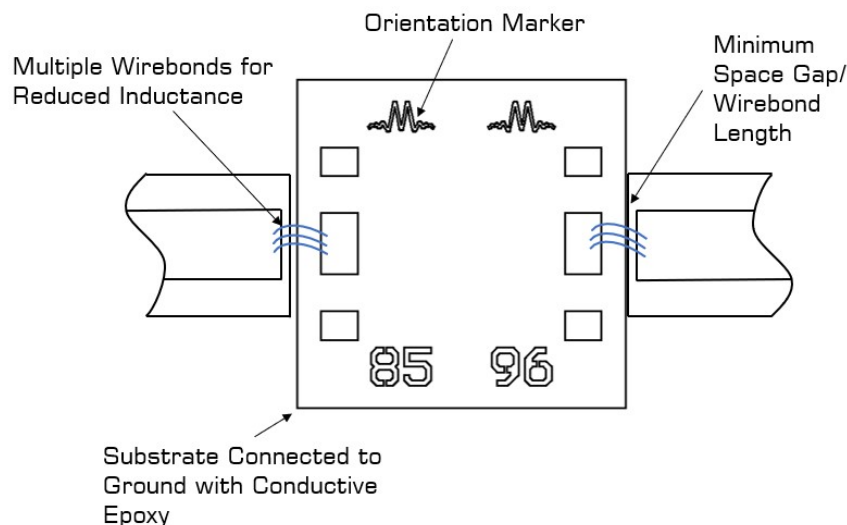
Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting - To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. Bond wire inductance will improve return loss. Bondwire inductance in the range of 30pH to 200pH will improve performance.

Circuit Considerations – 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. In circumstances where the chip more than .001" thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

Bonding Diagram



Handling Precautions

General Handling

Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity

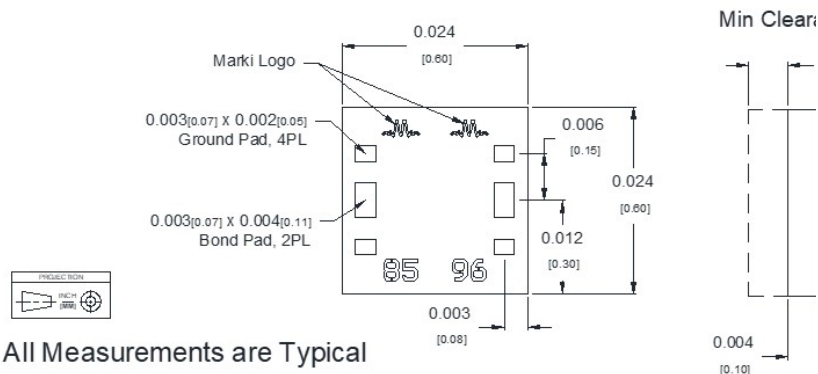
GaAs MMIC devices are sensitive to ESD and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

Mechanical Data

Outline Drawing

Part Number	Die Number
ATN00-0067CH	8595
ATN01-0067CH	8596
ATN02-0067CH	8597
ATN03-0067CH	8598
ATN04-0067CH	8599
ATN05-0067CH	8600
ATN06-0067CH	8601
ATN07-0067CH	8602
ATN08-0067CH	8603
ATN09-0067CH	8604
ATN10-0067CH	8605



All Measurements are Typical

Notes:

- CH substrate is .004 in Thick GaAs.
- Traces are 5 microns Au.
- Tolerance for X, Y dimensions is ± 0.002 in.
Tolerance for Z dimension is ± 0.0005 in.
Tolerance for pad location is ± 0.0001 in.

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