

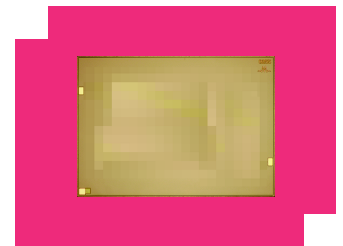
MFBC-00020CH

Passive GaAs MMIC 77.35-107.80 GHz Bandpass Filter

DEVICE OVERVIEW

General Description

The MFBC-00020CH family of passive MMIC bandpass filters are an ideal solution for small form factor, mmWave, high rejection filtering. Passive GaAs MMIC technology allows production of smaller filter constructions that replace larger form factor circuit board constructions. Tight fabrication tolerances allow for less unit-to-unit variation than traditional filter technologies. Low unit-to-unit variation allows for accurate simulations using the provided S2P file taken from measured production units. The MFBC-00020CH is available as a wire bondable die.



[Download s-parameters here](#)

Features

- Excellent Return Loss
- High Stop Band Suppression
- Wide Stop Band with Fast Roll-Off

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Packing Size	Green Status	Product Lifecycle	Export Classification
MFBC-00020CH	Passive GaAs MMIC 77.35-107.80 GHz Bandpass Filter	CH	-	RoHS REACH	Released	EAR99

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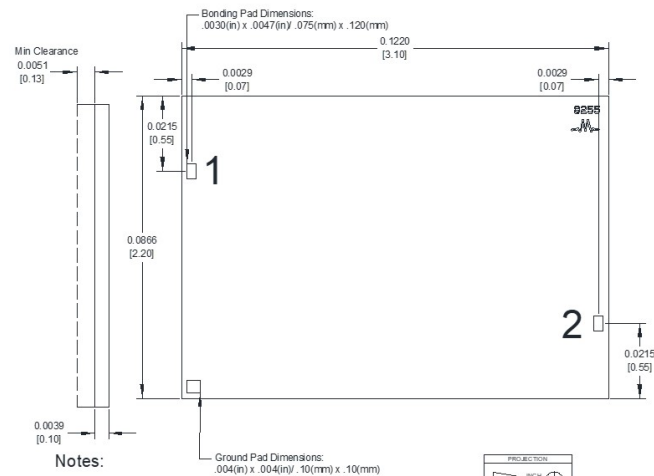
Revision History

Revision Code	Revision Date	Comment
-	2023-03-01	Datasheet Initial Release

Port Configuration and Functions

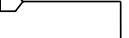
Port Diagram

The MMIC bandpass filters are symmetrical allowing Port 1 or Port 2 to be used as the input.



1. CH substrate is .004 Thick GaAs.
2. I/O trace is 5 microns Au. Ground is 4 microns Au.
3. Tolerance for X, Y dimensions is ± 0.002 in.
Tolerance for Z dimension is ± 0.0005 in.
Tolerance for pad location is ± 0.0001 in.

Port Functions

Port	Function	Description	Equivalent Circuit for Package
Pad	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	Input/Output	Port 1 is DC open to ground for the CH package.	
Port 2	Input/Output	Port 2 is DC open to ground for the CH package.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded or met simultaneously, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C

Package Information

Parameter	Details	Rating
Dimensions	-	3.10 x 2.20 mm

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Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data for all filters is shown with a sine wave input applied to port 1. Min and Max limits are guaranteed at TA=+25°C. All bare die are 100% DC tested and visually inspected. Due to measurement limitations of the characterization test setup, performance for the MFBC-00020CH were measured for its connectorized M-package variants: MFBC-00020M. For the CH die filters, the insertion loss would be expected to improve by approximately 0.5 dB from the data shown below. Improvements may be also seen in rejection steepness and group delay.

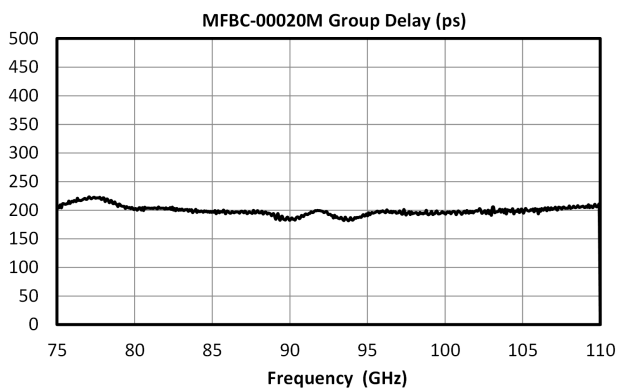
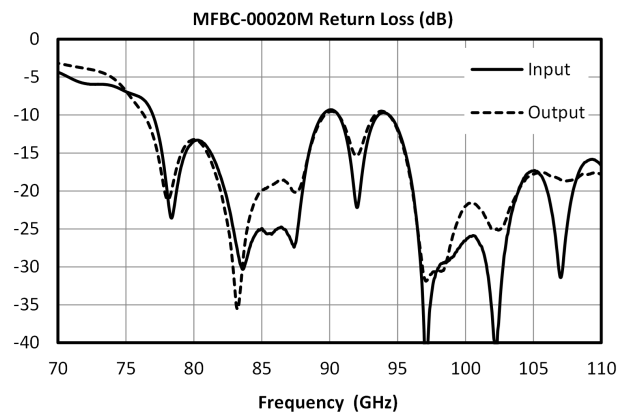
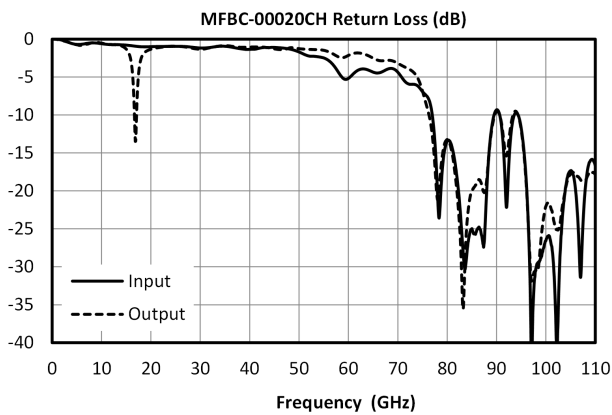
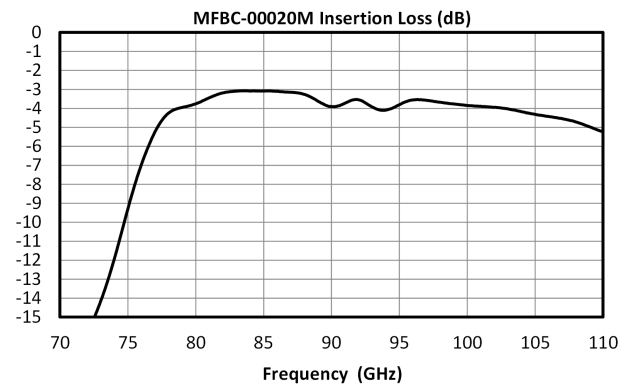
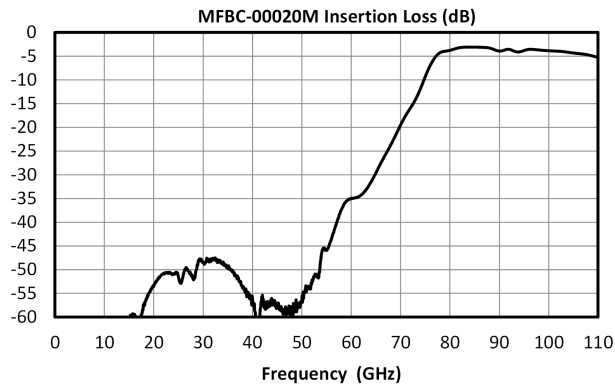
Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
1dBc Passband Low Frequency	-	-	-	-	77.35	-	GHz
1dBc Passband High Frequency	-	-	-	-	107.8	-	GHz
3dBc Passband Low Frequency	-	-	-	-	76.1	-	GHz
3dBc Passband High Frequency	-	-	-	-	112	-	GHz
Passband Return Loss	-	77.35	107.8	-	10	-	dB
Stopband Suppression	-	0	63	30	52	-	dB
Group Delay	-	-	-	-	198	-	ps
Impedance	-	-	-	-	50	-	Ω
Center Frequency, f_c	-	-	-	-	93.5	-	GHz
Insertion Loss @ F_c	-	93.5	93.5	-	3.6	-	dB

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Typical Performance Plots



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Application Circuit

Filter Name	Number of Bondwires	Bondwire Length (um)	Bondwire Diameter (mil)	Bondwire Inductance (pH)
MFBC-00019M	3	250	1	30
MFBC-00020M	3	250	1	30

Application Circuit Description

The bond wire (BW) specifications for this section refer to bond wires used in the M package filters.

Die Mounting Recommendations

Mounting and Bonding Recommendations

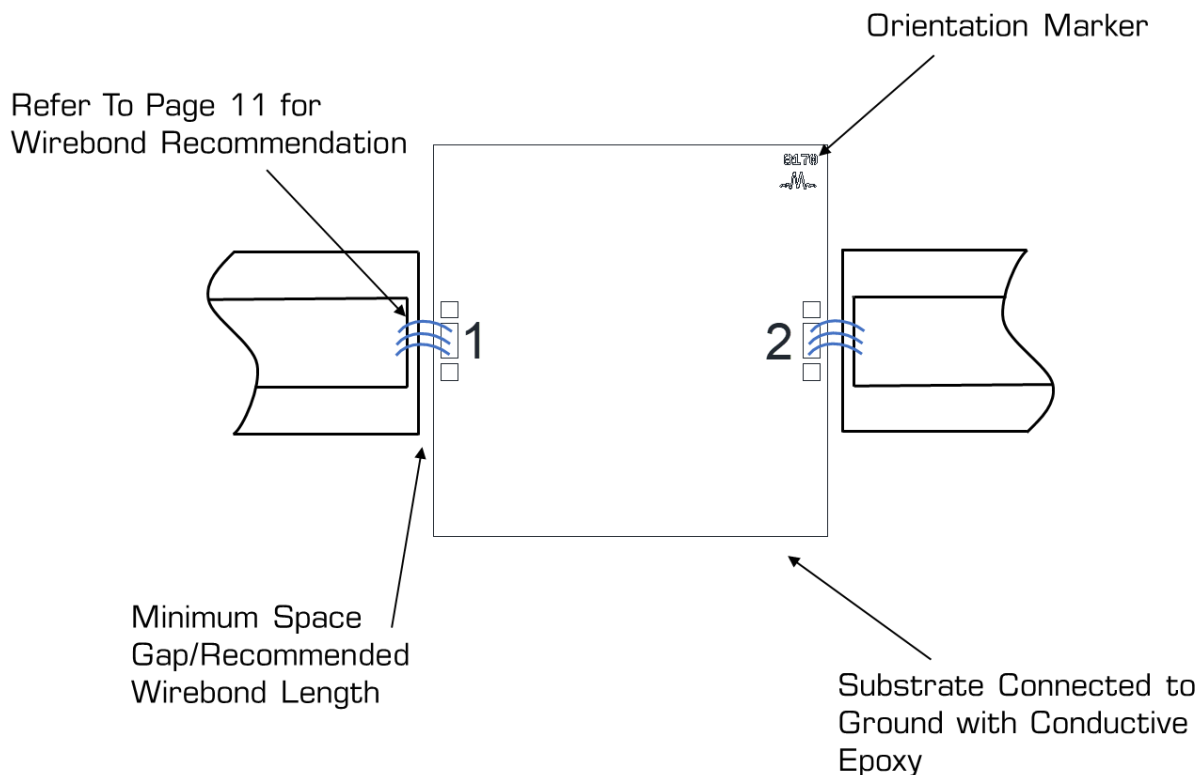
Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting- To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. Bond wire inductance will improve return loss. Bondwire inductance in the range of 30pH to 200pH will improve performance.

Circuit Considerations – 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. Refer to table on page 11 for wirebond recommendation. In circumstances where the chip is more than .001" thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

Bonding Diagram



Handling Precautions

General Handling

Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity

GaAs MMIC devices are sensitive to ESD and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

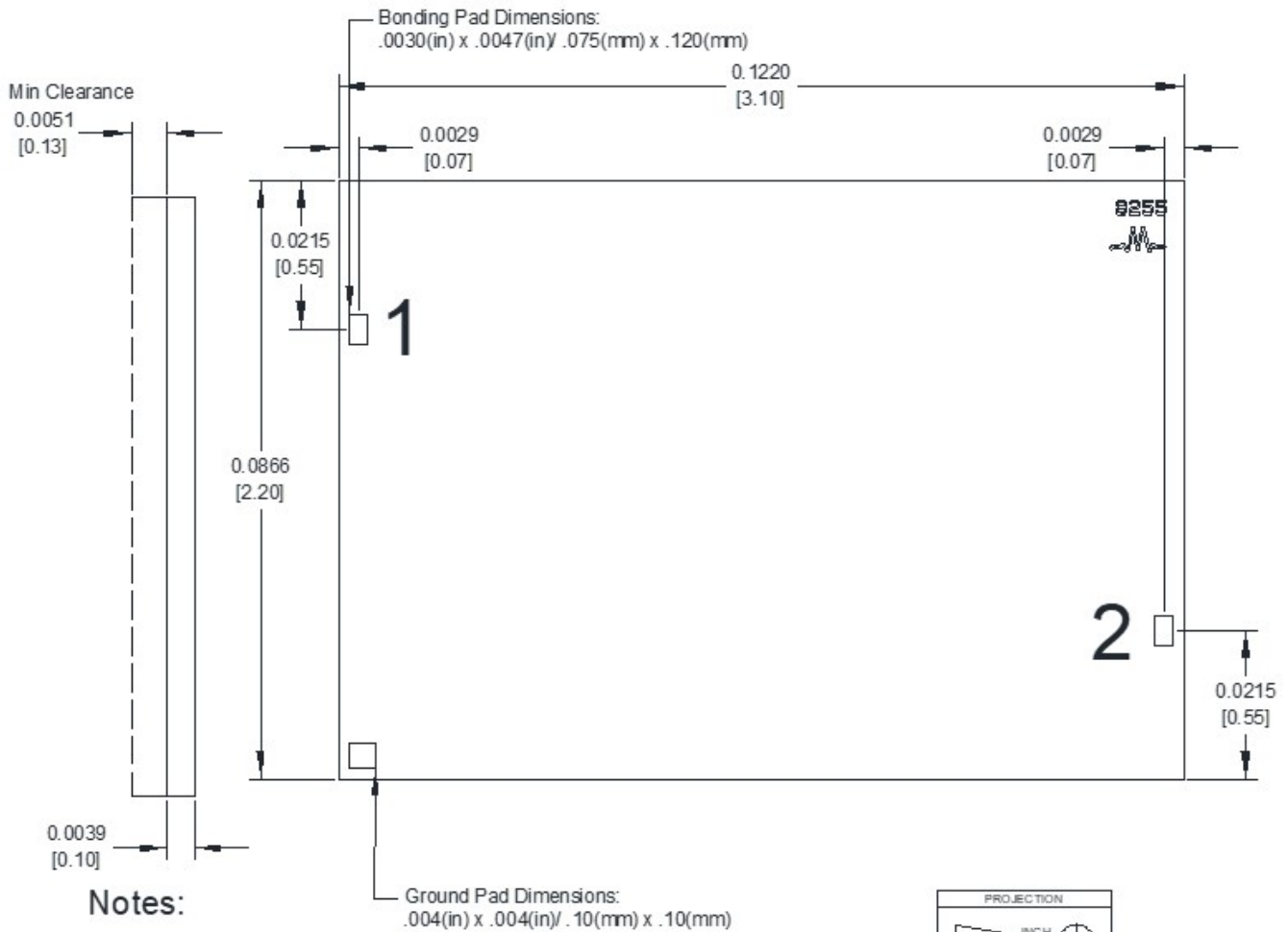
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Bandpass Filter

Mechanical Data

Outline Drawing



Notes:

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