

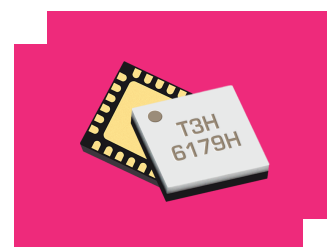
MT3H-0113HSM-2

GaAs MMIC High Dynamic Range Mixer

DEVICE OVERVIEW

General Description

The MT3H-0113HSM is a triple balanced passive diode mixer offering high dynamic range, low conversion loss, and excellent repeatability. As with all T3 mixers, this mixer offers unparalleled nonlinear performance in terms of IIP3, P1dB, and spurious performance with a flexible LO drive requirement from +16 dBm to +24 dBm. RF, LO, and IF ports are all operated single ended due to integrated baluns. The MT3H-0113HSM is available in a 4x4mm QFN, or in an SMA connectorized evaluation fixture. The MT3H-0113HSM is a superior alternative to Marki Microwave carrier and packaged T3 mixers.



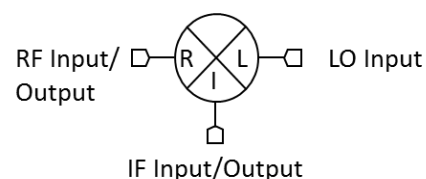
Features

- Industry-Leading Spurious, IP3, and P1dB Performance
- RoHS Compliant
- Broadband, Overlapping RF, LO, and IF

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Packing Size	Green Status	Product Lifecycle	Export Classification
EVAL-MT3H-0113H	Evaluation Board, GaAs MMIC High Dynamic Range Mixer	-	-	RoHS	Released	EAR99
MT3H-0113HSM-2	GaAs MMIC High Dynamic Range Mixer	SM	-	RoHS	Released	EAR99

Table Of Contents

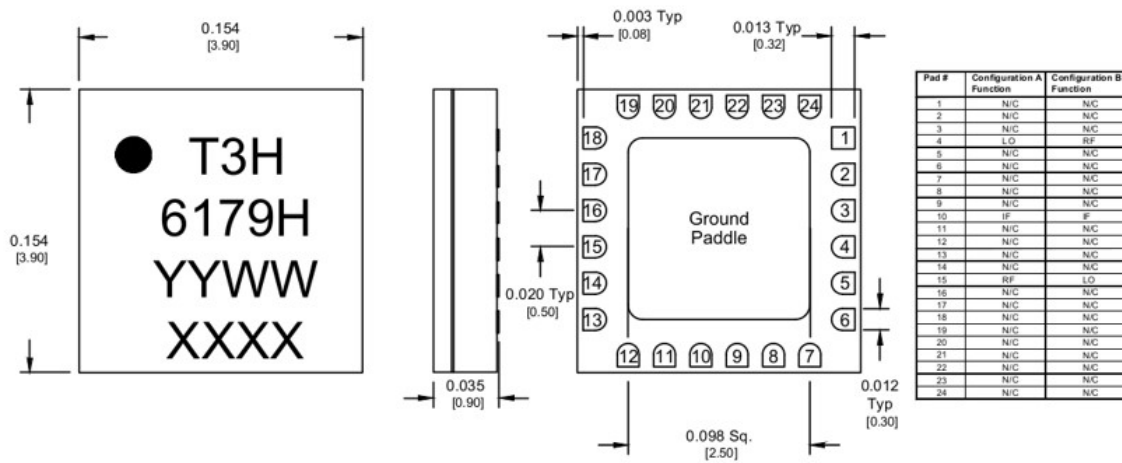
- **Device Overview**
 - General Description
 - Features
 - Applications
 - Functional Block Diagram
- **Port Configuration and Functions**
 - Port Diagram
 - Port Functions
- **Revision History**
- **Specifications**
 - Absolute Maximum Ratings
 - Package Information
 - Recommended Operating Conditions
 - Electrical Specifications
 - Typical Performance
 - Spur Tables
- **Mechanical Data**
 - Outline Drawing
- **Footprint Image**
- **Notes**

Revision History

Revision Code	Revision Date	Comment
-	2017-01-01	Initial Release
A	2019-01-01	Revised square wave LO IP3 plots. End note 5 updated to reflect new test condition.

Port Configuration and Functions

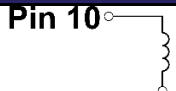
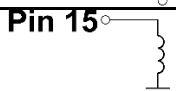
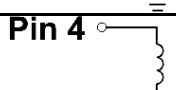
Port Diagram



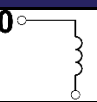
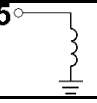
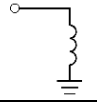
1. Substrate material is Ceramic.
2. All unconnected pads should be connected to PCB RF ground.
3. ENEPIG Plating/Finish: Ni: 8.89 micron|max, 1.27 micron min. Pd: 0.17 micron max, 0.07 micron min. Au: 0.254 micron max, 0.03 micron min.

Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
Pin 10	IF	Pin 10 is DC open. Blocking capacitor is optional.	
Pin 15	RF	Pin 15 is DC short and AC matched to 50 Ω from 0.8 to 13 GHz. Blocking capacitor is optional.	
Pin 4	LO	Pin 4 is DC short and AC matched to 50 Ω from 0.8 to 13 GHz. Blocking capacitor is optional.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
Pin 10	IF	Pin 10 is DC open. Blocking capacitor is optional.	Pin 10 
Pin 15	LO	Pin 15 is DC short and AC matched to 50 Ω from 0.8 to 13 GHz. Blocking capacitor is optional.	Pin 15 
Pin 4	RF	Pin 4 is DC short and AC matched to 50 Ω from 0.8 to 13 GHz. Blocking capacitor is optional.	Pin 4 

Specifications

Absolute Maximum Ratings

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
RF Power Handling (RF+LO)	30	dBm

Package Information

Parameter	Details	Rating
Dimensions	-	3.90 x 3.90 mm

Recommended Operating Conditions

Parameter	Min	Nominal	Max	Unit
LO Input Power	16	-	24	dBm

Electrical Specifications

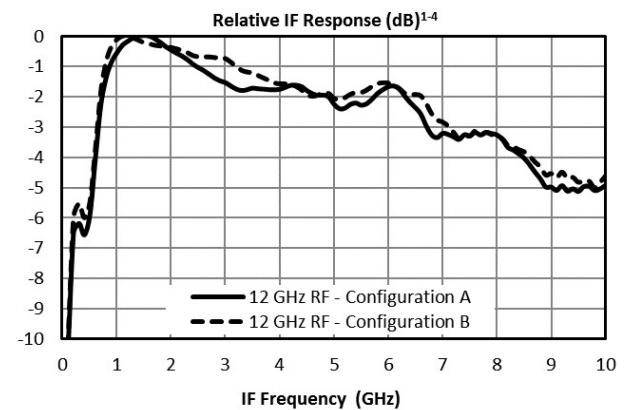
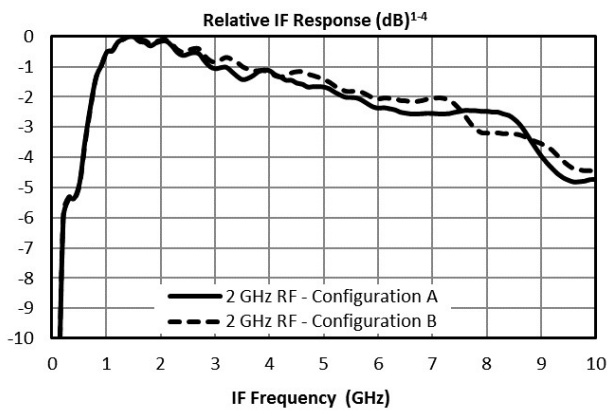
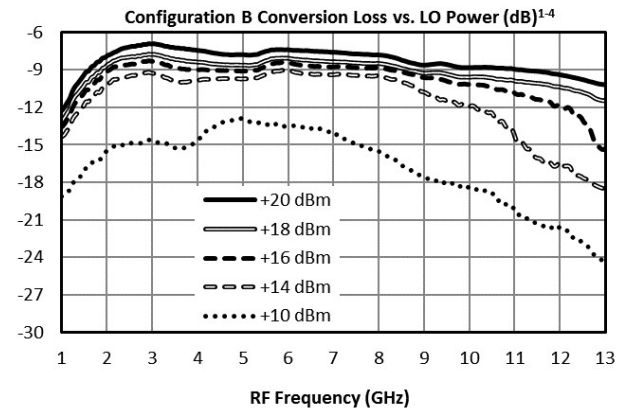
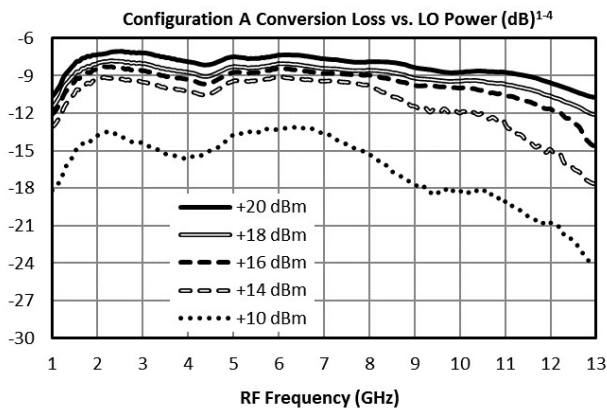
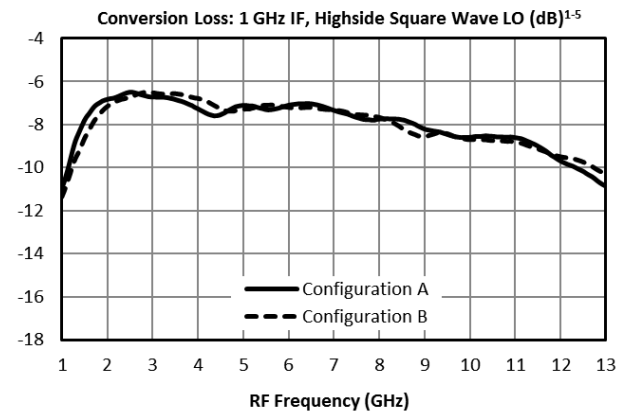
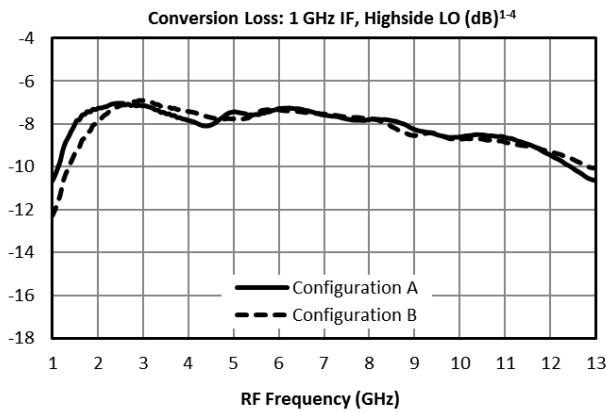
Specifications guaranteed from -55 to +100°C, measured in a 50Ω system. All bare die are 100% DC tested and 100% visually inspected. RF testing is performed on a sample basis to verify conformance to datasheet guaranteed specifications. Consult factory for more information.

Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
RF Frequency Range	A	-	1.5	-	13	GHz
LO Frequency Range	A	-	1.5	-	13	GHz
Conversion Loss ¹	A	LO/RF=1.5-13 GHz IF=0.8-1 GHz LO Drive Level=20dBm	-	8	12	dB
Conversion Loss ²	B	LO/RF=1.5-13 GHz IF=1-8.5 GHz LO Drive Level=20dBm	-	11.5	-	dB
Isolation, LO to RF	-	-	-	37	-	dB
Input IP3 ³	A	LO/RF=1.5-13 GHz IF=0.8-8.5 GHz LO Drive Level=16-24dBm	-	29	-	dBm
Input IP3 ⁴	B	LO/RF=1.5-13 GHz IF=0.8-8.5 GHz LO Drive Level=16-24dBm	-	30	-	dBm
IF Frequency Range	A	-	0.8	-	8.5	GHz

[1][2] Measured Conversion Loss measured at 1 GHz fixed IF.

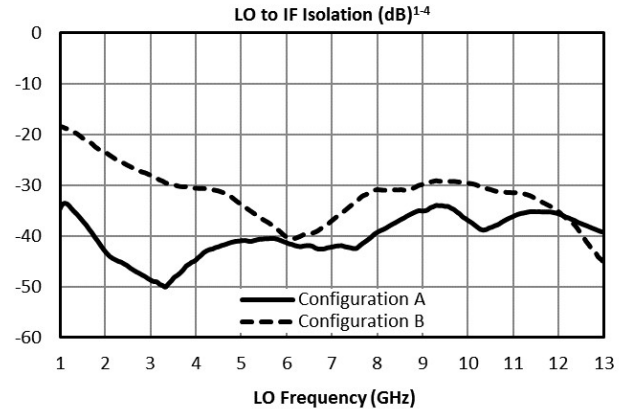
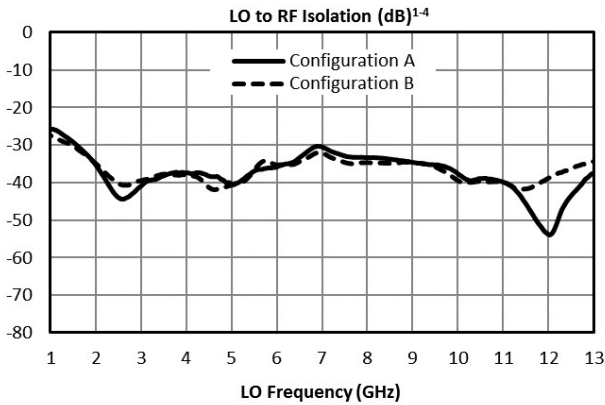
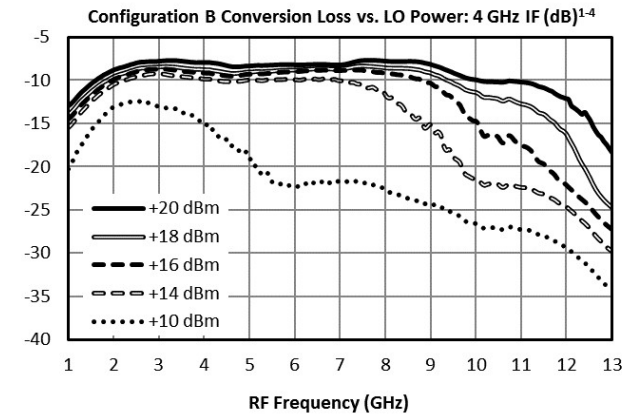
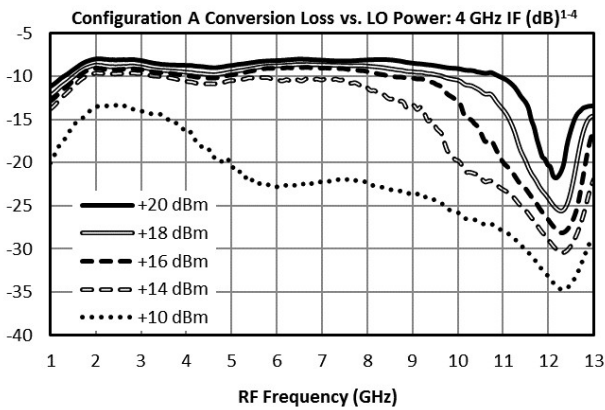
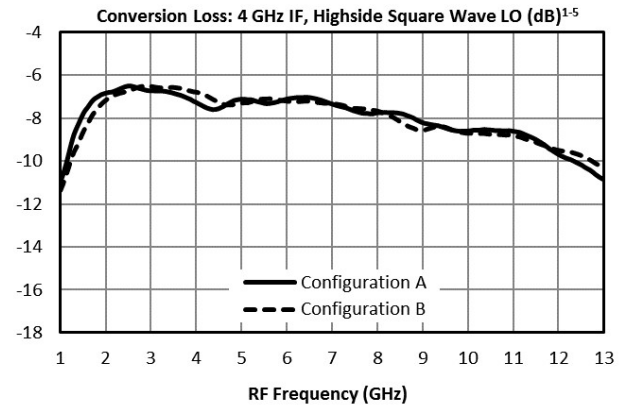
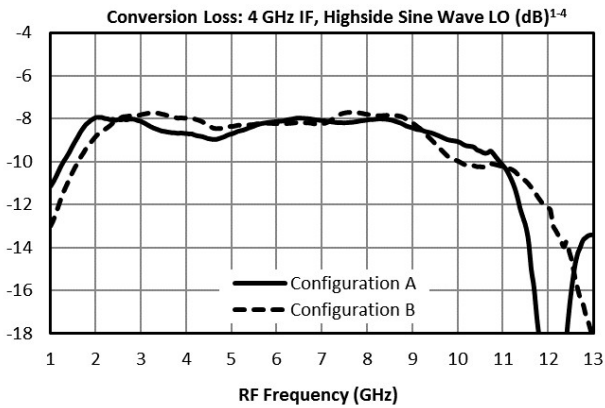
[3][4] IP3 depends on LO drive conditions, see plots for more details.

Typical Performance



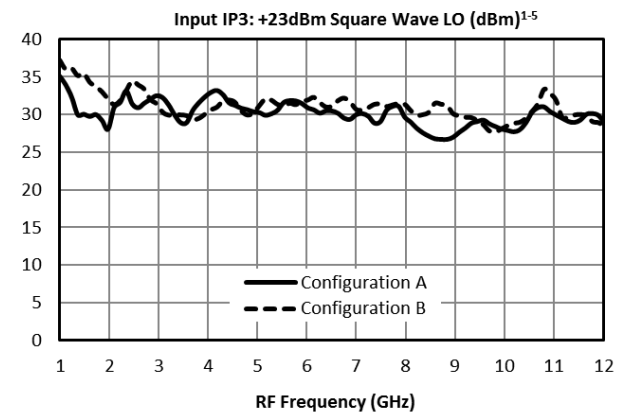
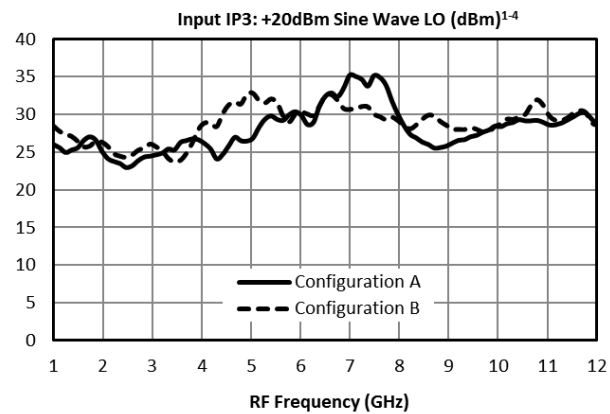
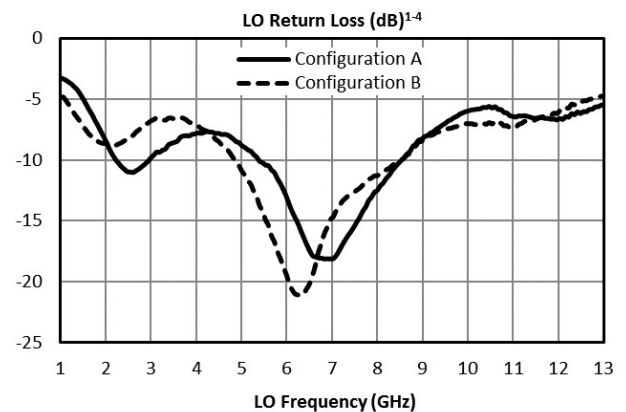
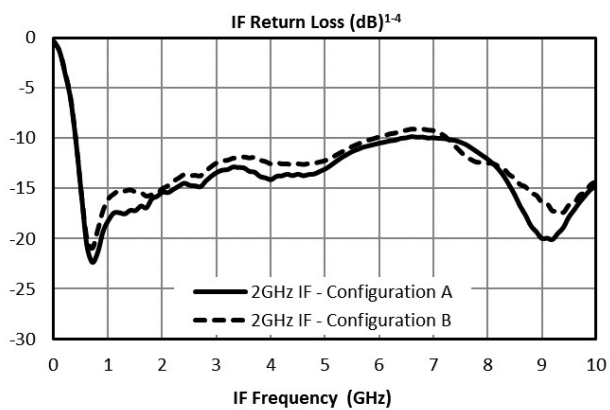
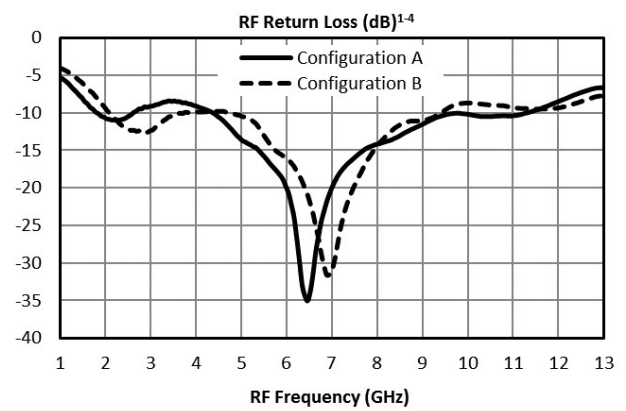
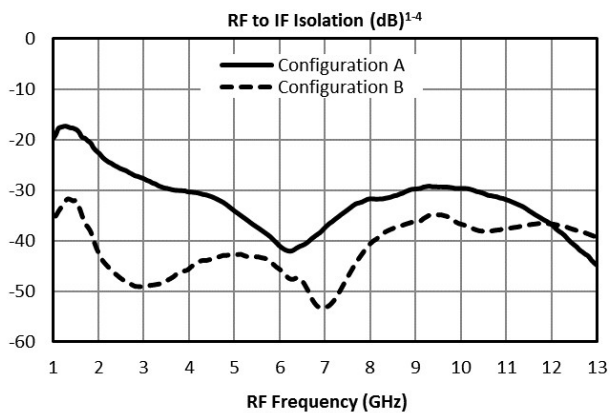
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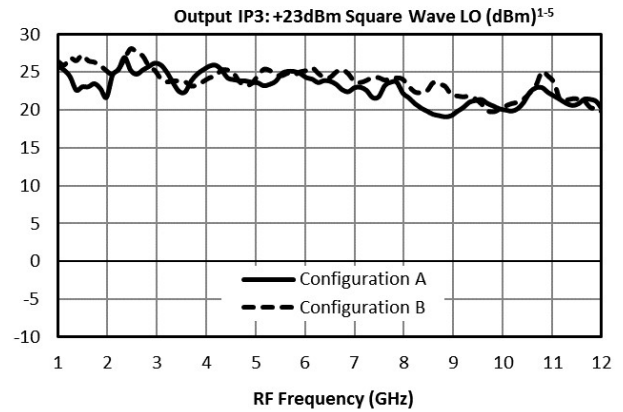
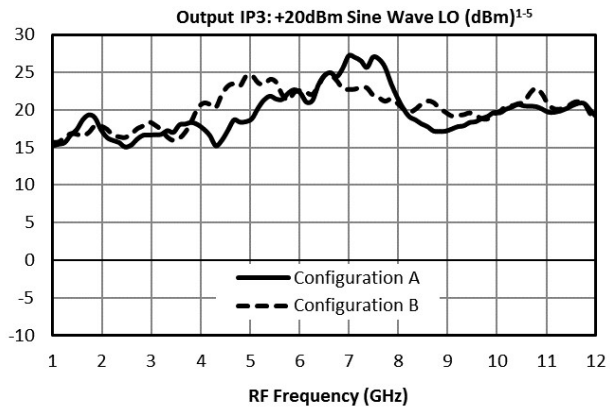
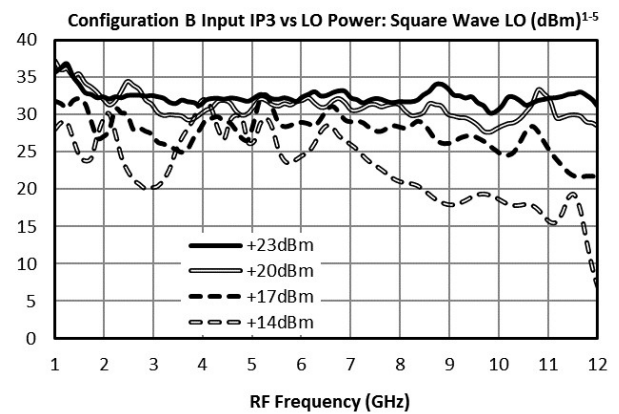
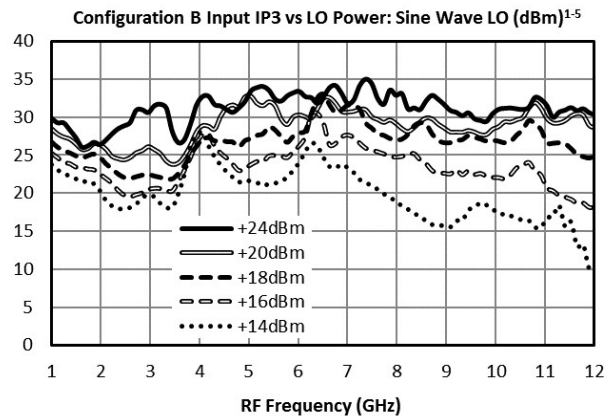
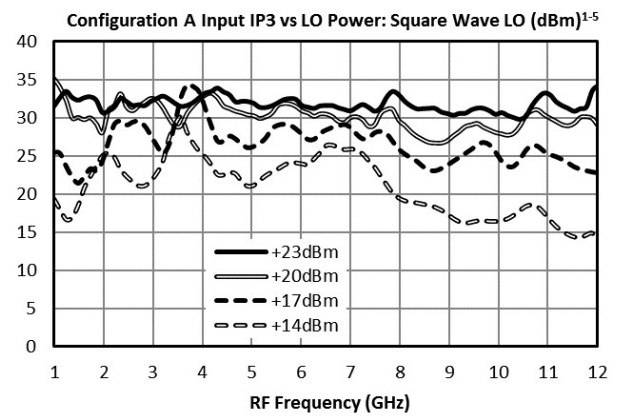
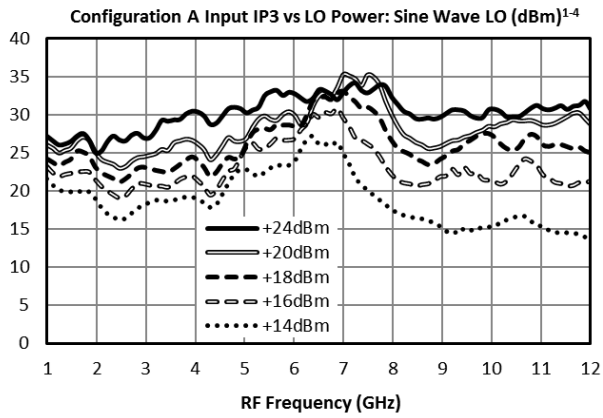
GaAs MMIC High Dynamic Range Mixer

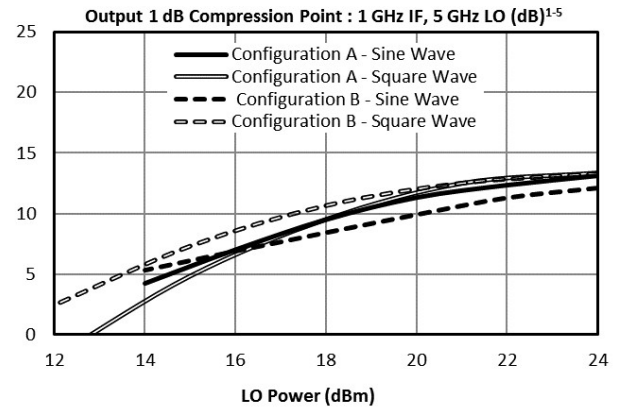
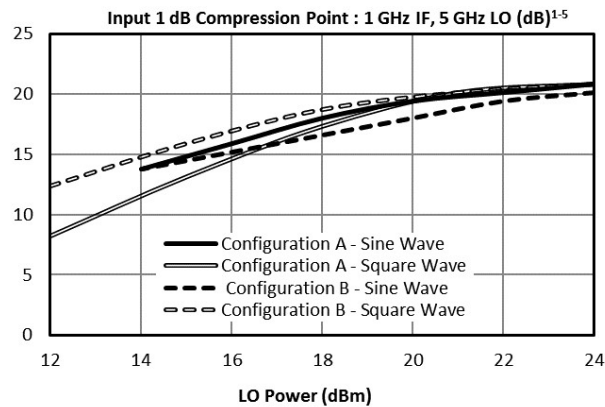
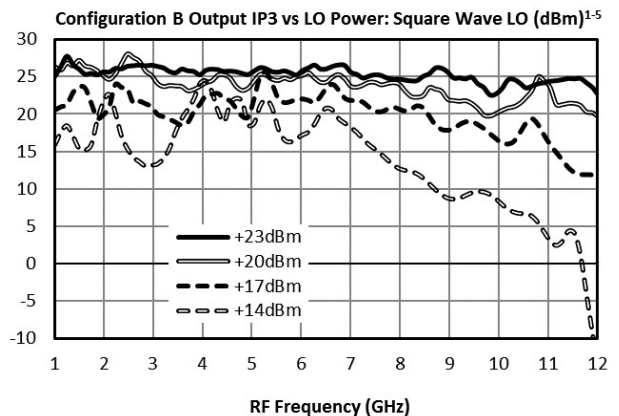
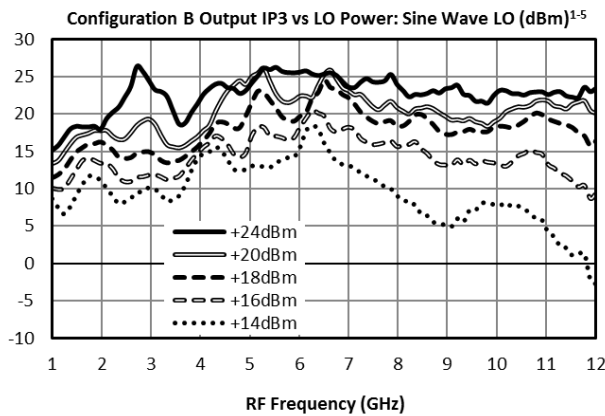
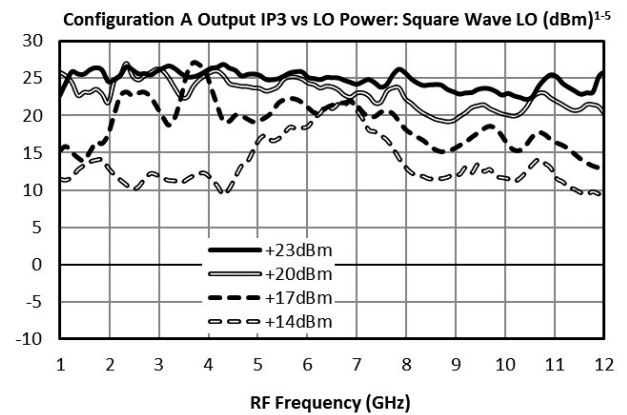
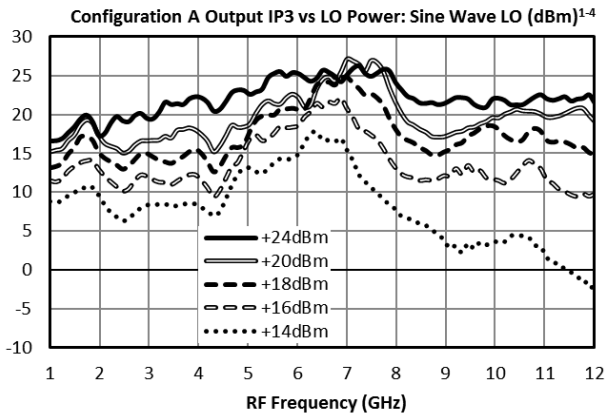


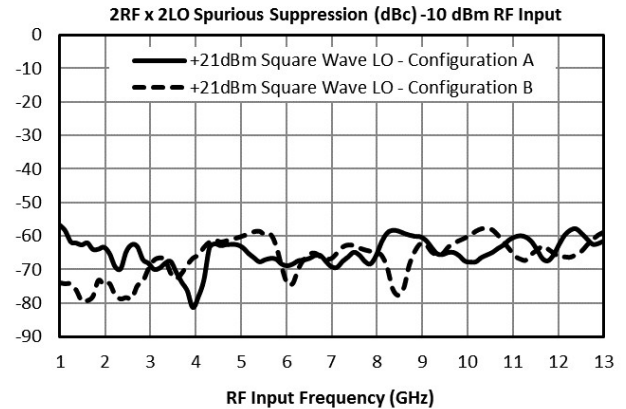
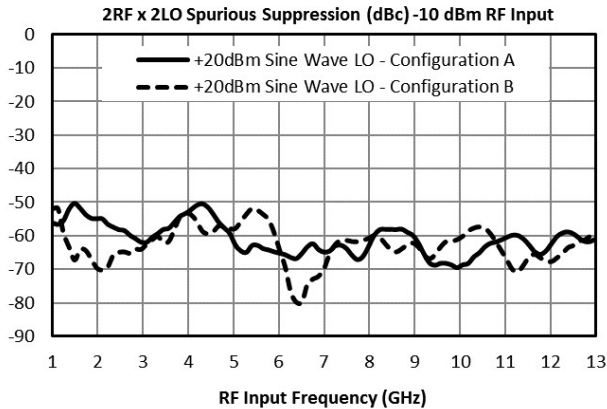
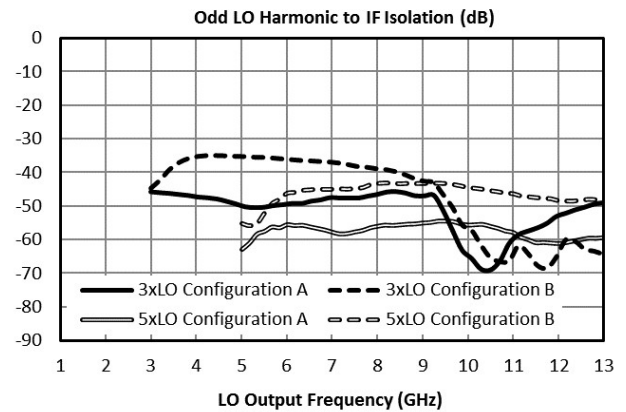
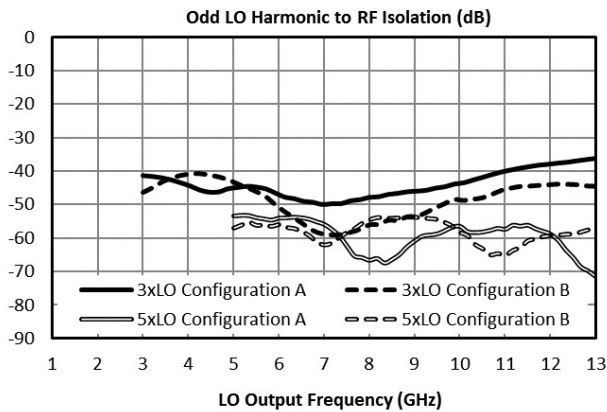
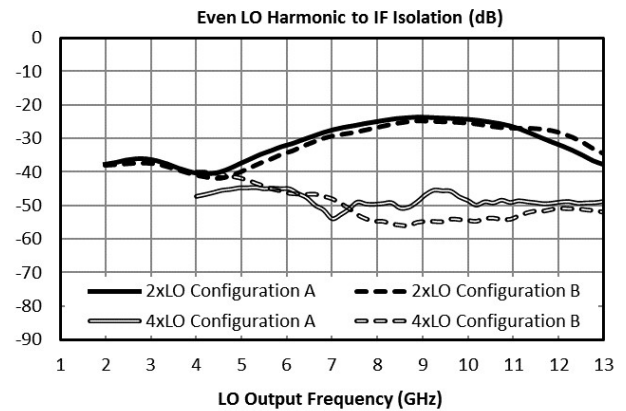
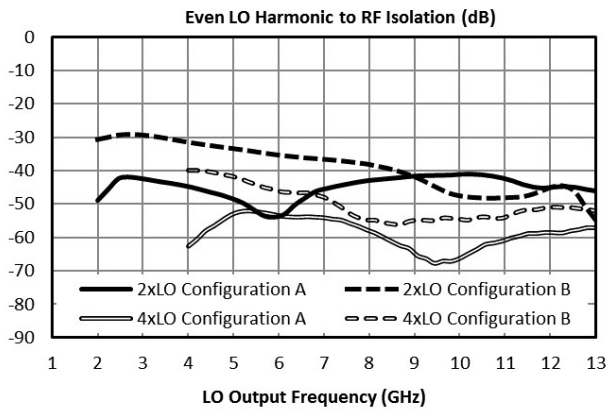
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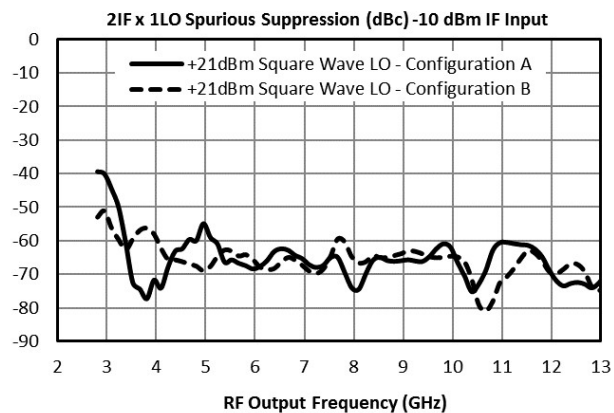
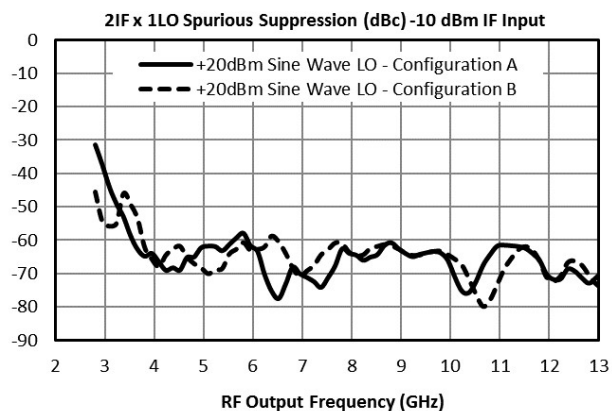
GaAs MMIC High Dynamic Range Mixer











Spur Table

Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies (+mLO+nRF) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where "n" is the RF spur order. For example, the 2RFx2LO spur is 61 dBc for the A configuration for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) dB lower, or 71 dBc.

Typical Downconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁶

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	27 (31)	Reference	30 (36)	21 (19)	22 (33)	20 (24)
2xRF	60 (59)	66 (62)	61 (63)	63 (59)	61 (64)	66 (58)
3xRF	105 (97)	83 (86)	91 (93)	77 (78)	94 (93)	118 (120)
4xRF	126 (128)	125 (126)	123 (123)	122 (121)	119 (121)	121 (124)
5xRF	152 (157)	149 (154)	148 (149)	144 (144)	147 (149)	140 (141)

Typical Downconversion Spurious Suppression (dBc): A Configuration (B Configuration), Square Wave LO ⁶

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	27 (32)	Reference	29 (36)	15 (13)	24 (39)	19 (20)
2xRF	62 (58)	67 (63)	65 (67)	66 (62)	64 (67)	68 (64)
3xRF	104 (103)	97 (99)	103 (108)	91 (94)	100 (107)	129 (128)
4xRF	126 (127)	131 (134)	128 (134)	130 (131)	128 (134)	126 (129)
5xRF	163 (162)	164 (169)	164 (168)	163 (163)	166 (170)	160 (165)

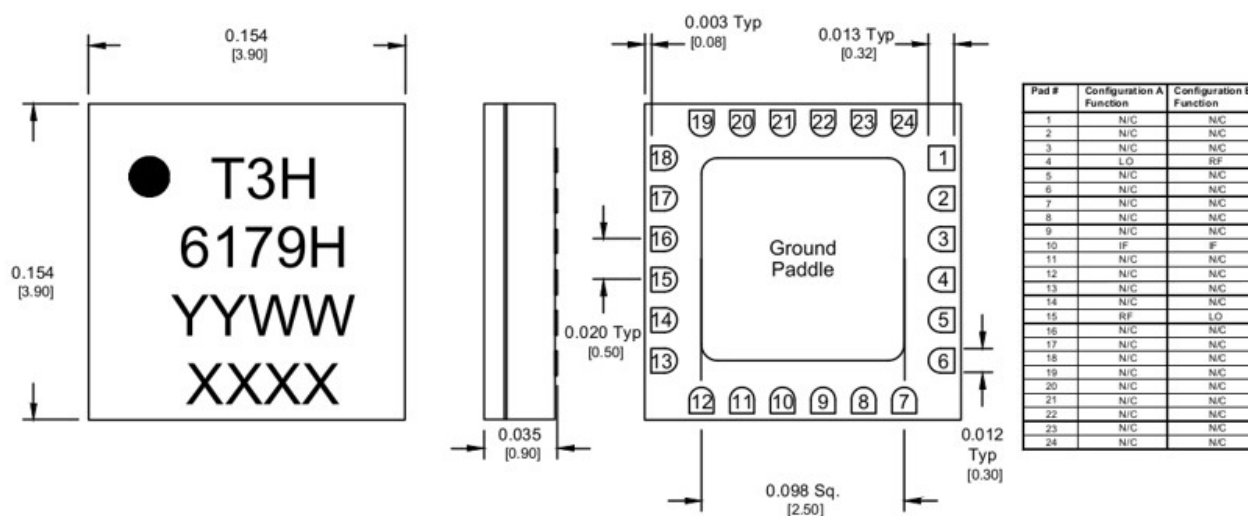
Upconversion Spurious Suppression

Spurious data is taken by mixing an input within the IF band, with LO frequencies (+mLO+nIF), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where "n" is the IF spur order. For example, the 2IFx1LO spur is typically 65 dBc for the A configuration for a -10 dBm input, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) dB lower, or 75 dBc.

Typical Upconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁶

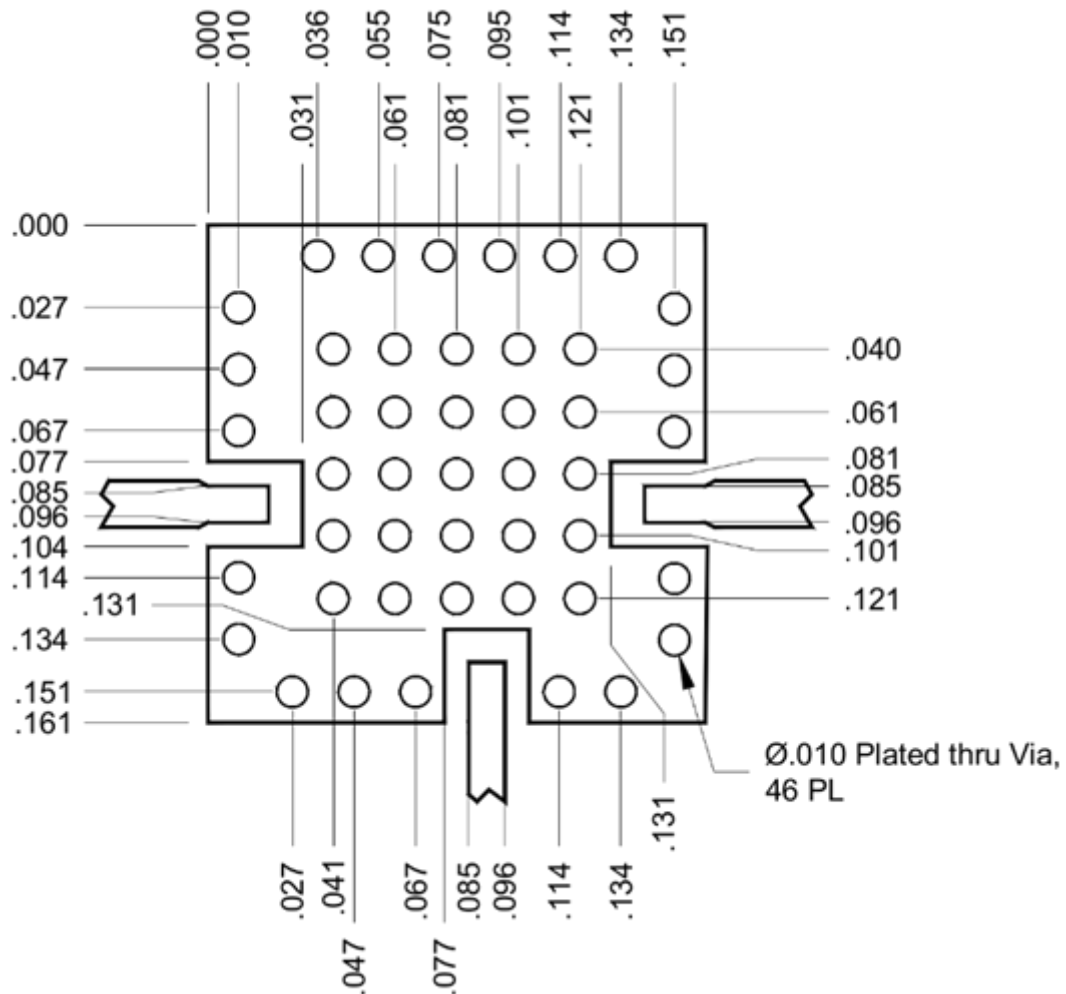
Mechanical Data

Outline Drawing



1. Substrate material is Ceramic.
2. All unconnected pads should be connected to PCB RF ground.
3. ENEPIG Plating/Finish: Ni: 8.89 micron|max, 1.27 micron min. Pd: 0.17 micron max, 0.07 micron min. Au: 0.254 micron max, 0.03 micron min.

Footprint Image



Notes

1. Mixer Conversion Loss Plot IF frequency is 1 GHz unless otherwise specified.
2. Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
3. Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
4. Unless otherwise specified, data is taken with highside, +20 dBm sine wave LO drive.
5. Square wave LO generated using 2x ADM1-0026PA with +10 dBm input into the first stage. LO Power reported in plots is of the fundamental tone only. Square wave LO power in plots is stepped down using broadband DC-40 GHz attenuators.
6. Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
7. Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

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