CMOS Process Options
CMOS Selection Guide

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Mitel CMOS processes cover a wide range of Operating Voltages from 1.2 Volts up to 15 Volts. All processes are available with double poly and double metal layers, and high accuracy/low TCR resistance option. The following Selection Guide will help you to quickly locate the process and specifications to match your requirements.

* Process Code description

Prefix in the Process Code	Number of Polysilicon layers	Number of Metal layers
CS	1	1
CP	2	1
CA	1	2
CD	2	2

Process Code *	**Geometry Size** [µm]	**Operating Voltage** [Volts]	**Transistor L-Eff N / P** [µm]	**Starting Material, Well**	**Alignment System**	**Poly Pitch P1 / P2** [µm]	**Metal Pitch M1 / M2** [µm]	**Contact Size** [µm x µm]	**Via Size** [µm x µm]	**Design Rules** IDS	**Spice Parameters** SPS	**Electrical Parameters Spec.** SPE
C*120	1.2	5.0	0.9 / 0.8	Bulk/Epi, N-Well	Stepper	1.2 / 3	3 / 3.8	1.5 x 1.5	1.8 x 1.8	044	020	020
C*152	1.5	1.2	1.2 / 1.2	Bulk/Epi, P-Well	Stepper	3 / 3	3 / 3.8	1.5 x 1.5	1.8 x 1.8	041	018	018
C*151	1.5	3.0	1.2 / 1.2	Bulk/Epi, P-Well	Stepper	3 / 3	3 / 3.8	1.5 x 1.5	1.8 x 1.8	041	007	007
C*150	1.5	5.0	1.2 / 1.2	Bulk/Epi, P-Well	Stepper	3 / 3	3 / 3.8	1.5 x 1.5	1.8 x 1.8	041	015	015
C*201	2.0	3.0	1.8 / 1.7	Bulk/Epi, P-Well	Stepper	4 / 4	4 / 5	2 x 2	2.4 x 2.4	008	013	013
C*200	2.0	5.0	1.8 / 1.7	Bulk/Epi, P-Well	Stepper	4 / 4	4 / 5	2 x 2	2.4 x 2.4	008	001	001
C*31	3.0	3.0	2.4 / 2.2	Bulk/Epi, P-Well	Scanner	6 / 6	7 / 7	3 x 3	3 x 3	005	016	016
C*32	3.0	5.0	2.4 / 2.2	Bulk/Epi, P-Well	Scanner	6 / 6	7 / 7	3 x 3	3 x 3	005	002	002
C*33	3.0	10.0	2.6 / 2.2	Bulk/Epi, P-Well	Scanner	6 / 6	7 / 7	3 x 3	3 x 3	045	012	012
C*42	4.0	10.0	1.6 / 2.6	Bulk, P-Well	Scanner	8 / 8	8 / 7	4 x 4	3 x 3	003	003	003
C*43	4.0	15.0	1.9 / 2.6	Bulk, P-Well	Scanner	8 / 8	8 / 7	4 x 4	3 x 3	003	009	009
C*52	5.0	12.0	1.8 / 2.8	Bulk, P-Well	Scanner	10 / 10	10 / 10	5 x 5	5 x 5	004	004	004
CM9	9.0	15.0	5.2 / 4.8	Bulk, P-Well	Scanner	Metal Gate	13	8 x 8	N/A	92D-0000	006	006

Note: Process development is ongoing, contact us for current process availability.
Ask for our Custom Silicon Book for complete process specifications.

Notes: