

Phase-Locked Oscillators

50 Years of
Excellence

26.4 GHz

**LMDS / PTP Phase-Locked
Dielectric Resonator Oscillator**

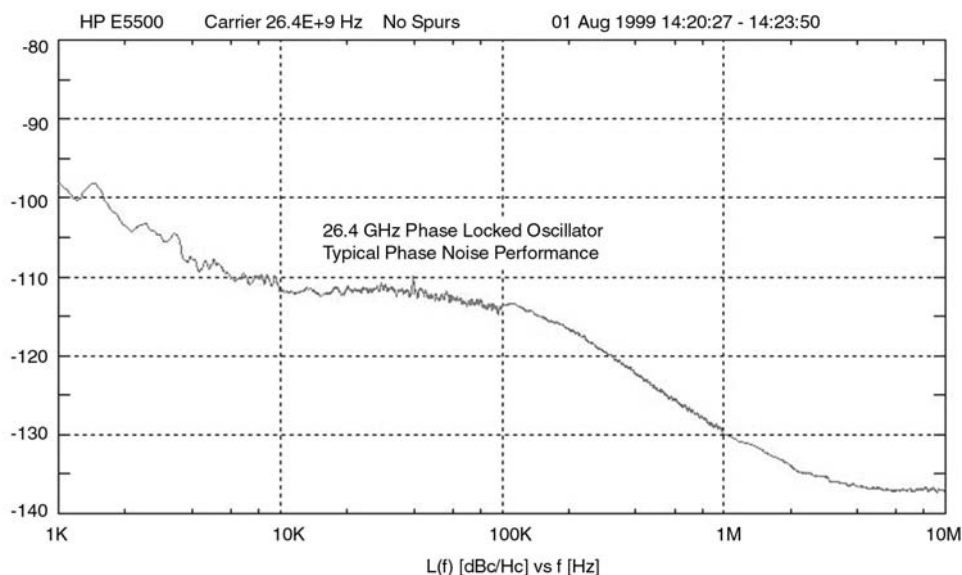
- Exceptionally Low Phase Noise
Typically 110 dBc/Hz @ 10 kHz Offset
- Designed for Severe Shock and Vibration Environments
< ± 500 Hz microphonically induced frequency transients
due to .4 joule impact
- Surface Mount Design
- Low Cost / High Volume Design
- Superior Spectral Purity
- Options
 - Output Frequency From 18-38 GHz
 - Connectorized Housing
 - Internal Reference

Specifications

PARAMETER	SPECIFICATION	UNITS	NOTES
Frequency	26.4	GHz	Additional Frequencies Available
Output Power	+6 dBm (Typ) $\pm$ 2 dB (max)	dBm	Over Operating Temperature Range
Frequency Reference	External Crystal Reference		
Reference Frequency	100	MHz	Customer Provided
Reference Power Required	0 to +5	dBm	Sine Wave
Spurious	- 65	dBc	
Harmonics	- 30	dBc	
Phase Noise	-85 @ 1 kHz offset -100 @ 10 kHz -100 @ 100 kHz -125 @ 1 MHz -125 @ > 1 MHz	dBc/Hz (min)	Phase Noise Performance Is Reference-Oscillator Dependent
Operating Temperature	-30°C to +75°C		
Power Supply Voltage	10 $\pm$ 2	Volts	
Operating Current	250	MA	
Phase Lock Indicator	"O" Indicates Lock +6.5 V Indicator – Unlocked		
Output Connector	Ground-Signal-Ground Pads on circuit board		Connectorized Versions Available
Size	2.25" x 2.25" x 0.82"	Inches	Maximum
Weight	6	Ounces	Maximum



Phase Noise Performance Chart



LMDS PLDRO Block Diagram

