

OC-768 CLOCK OSCILLATOR

Application: Designed for critical timing applications required in SONET/SDH OC-768 systems.

Features:

- Ultra-Stable DRO Technology
- Lowest Phase Noise / Jitter
- Minimal DC Power Dissipation
- Low Cost SMT Construction

Phase Noise, Max (-30° to +70°C):

1 kHz offset	-45 dBc/Hz
10 kHz offset	-76 dBc/Hz
100 kHz offset	-105 dBc/Hz
1 MHz offset	-120 dBc/Hz

Output Characteristics (High K Series):

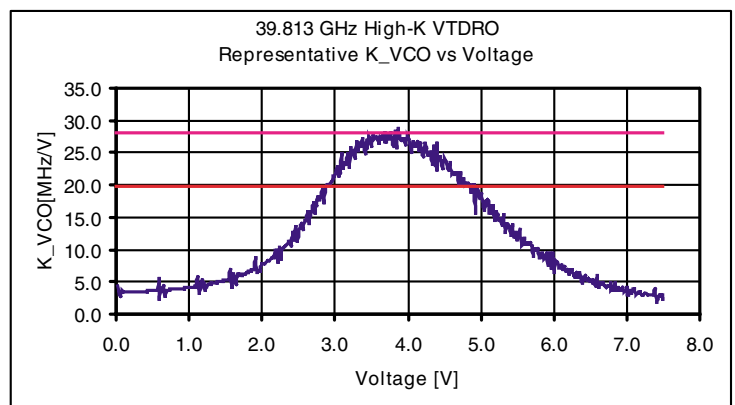
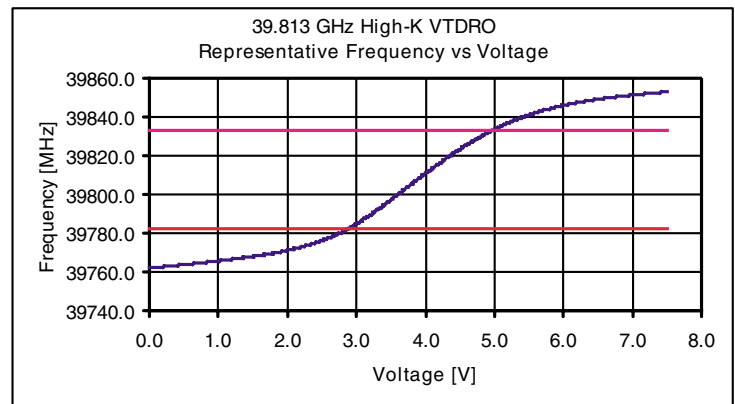
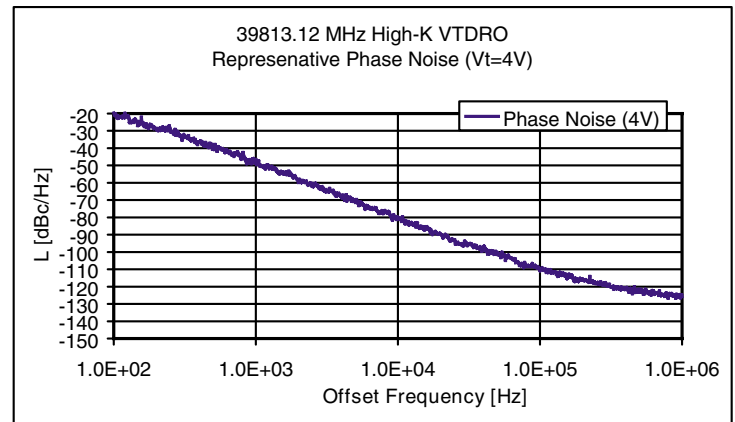
Typical Frequency:	39 to 44 GHz
Linear Tuning Range:	51 MHz
Tuning Voltage:	3 to 5 Volts (High K option, other ranges available)
Modulation Sensitivity:	24 MHz/V Typical
Output Power:	+5 dBm (Other power levels available)

Connectors:

RF: V Connector (alternative connectors available)
DC: Solder Pin

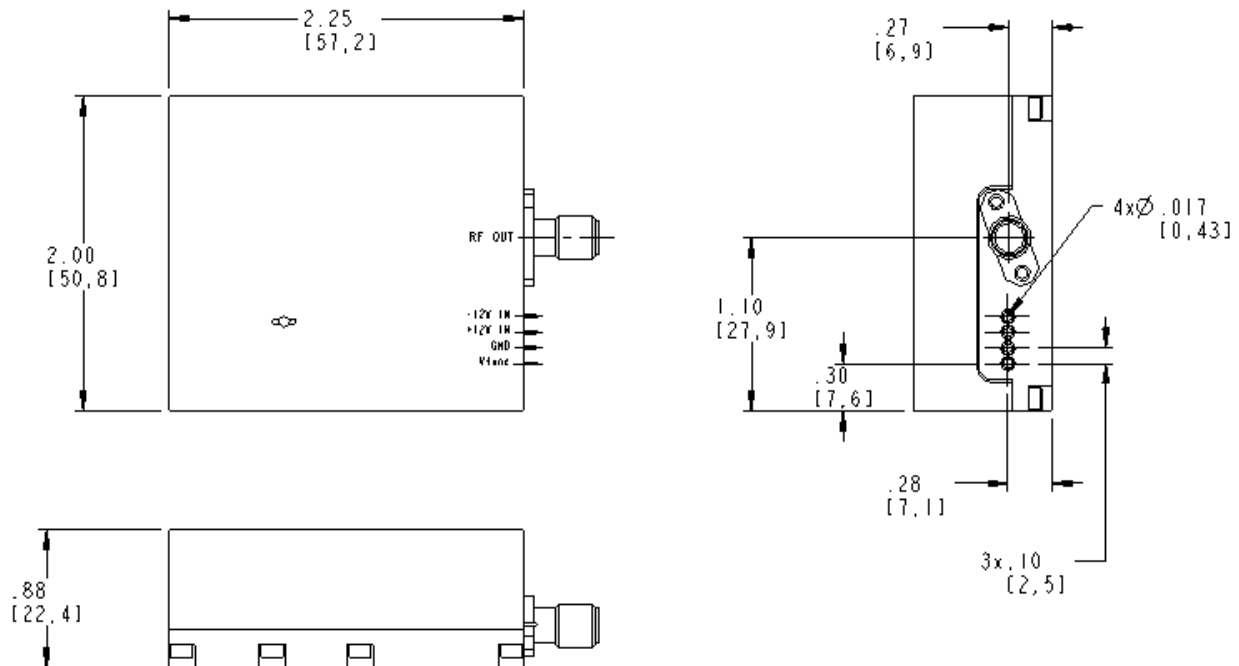
DC Power:

+8 to +12V @ 190 mA
-8 to -12V @ 15 mA
Internally Regulated



Fiber Optic Products

OUTLINE DRAWING



Contact Narda for your custom requirements.

L-3 Communications has patent applications pending which protects this and related fiber optic products. These products may also be protected by other intellectual property rights including trademarks and copyrights.