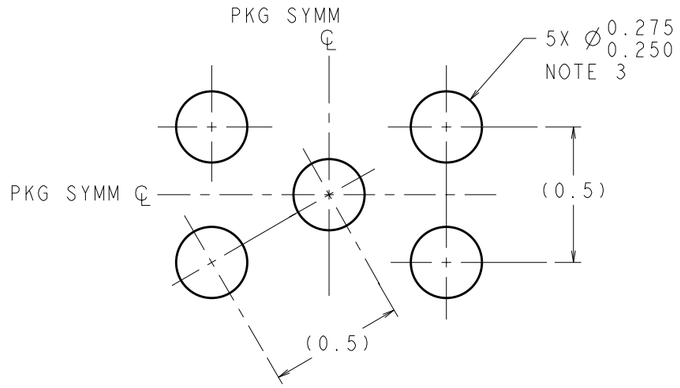
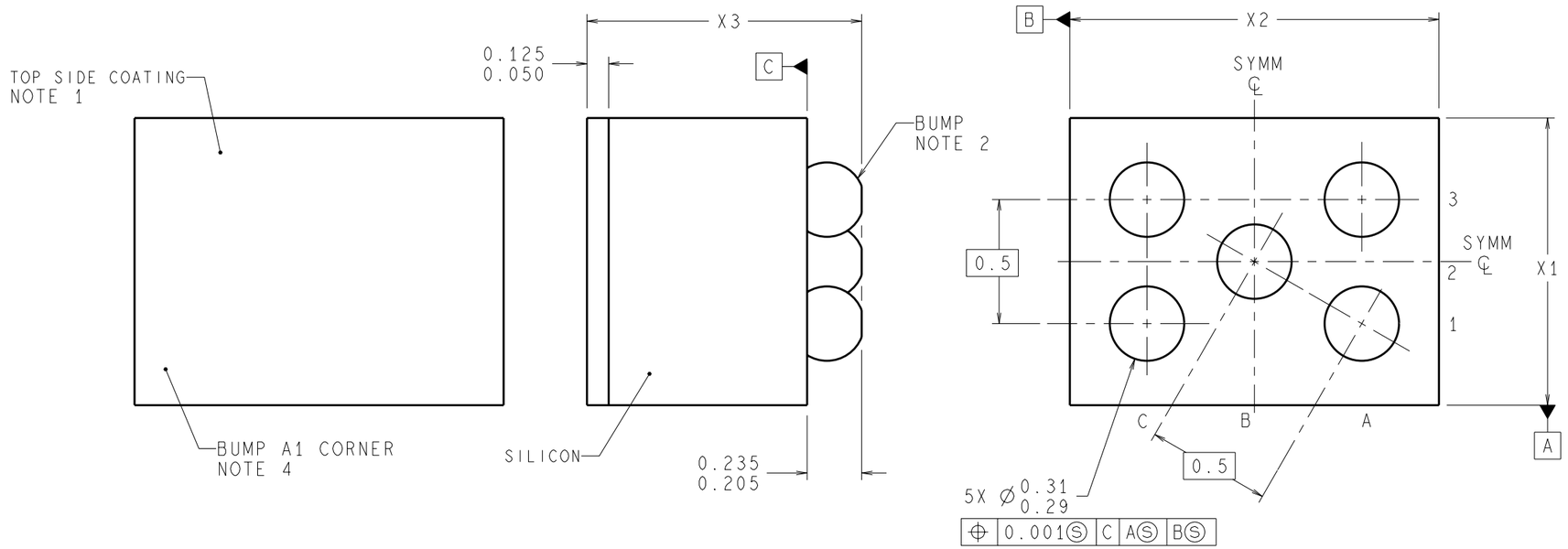
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	275	03/29/2001	MS/VA
B	UPDATE X1 & X2 DIMENSIONS IN TABLE	355	06/26/2001	MS/VP
C	REVERSE BUMP NUMBERING SEQUENCE; UPDATE NOTES 4 & 6; REMOVE LAND No'S.	515	02/06/2002	MS/VP



DIMENSIONS ARE IN MILLIMETERS

LAND PATTERN RECOMMENDATION



NOTES: UNLESS OTHERWISE SPECIFIED

1. EPOXY COATING.
2. 63Sn / 37Pb EUTECTIC BUMP.
3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.
4. PIN A1 IS ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION.
5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE X1 IS PACKAGE WIDTH, X2 IS PACKAGE LENGTH AND X3 IS PACKAGE HEIGHT (SEE TABLE, SHEET 2). EXAMPLE: BLA05GAB HAS WIDTH=1158, LENGTH=1336, HEIGHT=945
6. NO JEDEC REGISTRATION AS OF FEBRUARY 2002.

APPROVALS	DATE
DRAWN *MARTA SUCHY*	03/29/2001
DFTG. CHK. *THANH LEQUANG*	02/06/2002
ENGR. CHK. *VIRAJ PATWARDHAN*	02/06/2002

PROJECTION — INCH [MM]

National Semiconductor
2900 Semiconductor Dr, Santa Clara, CA 95052-8090

MICRO SMD, 5 BUMP(LARGE), 0.5mm PITCH

SCALE	SIZE	DRAWING NUMBER	REV
N/A	C	(SC)MKT-BLA05XXX	C

DO NOT SCALE DRAWING — SHEET 1 of 2

REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
	SEE SHEET 1			

PACKAGE DIMENSIONS					
X1 DESIGNATOR	X1 PACKAGE WIDTH ±30µm	X2 DESIGNATOR	X2 PACKAGE LENGTH ±30µm	X3 DESIGNATOR	X3 PACKAGE HEIGHT ±100µm
A	1006	A	1361	A	795
B	1031	B	1387	B	945
C	1057	C	1412	C	995
D	1082	D	1438		
E	1107	E	1463		
F	1133	F	1488		
G	1158	G	1514		
H	1184	H	1539		
J	1209	J	1565		
K	1234	K	1590		
L	1260	L	1615		
M	1285	M	1641		
N	1311	N	1666		
P	1336	P	1692		
Q	1361	Q	1717		
R	1387	R	1742		
S	1412	S	1768		
T	1438	T	1793		
U	1463	U	1819		
V	1488	V	1844		
		W	1869		
		X	1895		
		Y	1920		
		Z	1946		
		1	1971		
		2	1996		

APPROVALS	DATE
DRAWN MARTA SUCHY	03/29/2001
DFTG. CHK. THANH LEQUANG	02/06/2002
ENGR. CHK. VIRAJ PATWARDHAN	02/06/2002

PROJECTION

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SCALE	SIZE	DRAWING NUMBER	REV
N/A	C	(SC)MKT-BLA05XXX	C

DO NOT SCALE DRAWING