

REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
	SEE SHEET 1			

PACKAGE DIMENSIONS					
X1 DESIGNATOR	X1 PACKAGE WIDTH ±30μm	X2 DESIGNATOR	X2 PACKAGE LENGTH ±30μm	X3 DESIGNATOR	X3 PACKAGE HEIGHT ±100μm
A	1996	A	2225	A	795
B	2022	B	2250	B	945
C	2047	C	2276	C	995
D	2073	D	2301		
E	2098	E	2327		
F	2123	F	2352		
G	2149	G	2377		
H	2174	H	2403		
J	2200	J	2428		
K	2225	K	2454		
L	2250	L	2479		
M	2276	M	2504		
N	2301	N	2530		
P	2327	P	2555		
Q	2352	Q	2581		
R	2377	R	2606		
S	2403	S	2631		
T	2428	T	2657		
U	2454	U	2682		
V	2479	V	2708		
		W	2733		
		X	2758		
		Y	2784		
		Z	2809		
		1	2835		
		2	2860		
		3	2885		
		4	2911		
		5	2936		
		6	2962		

APPROVALS		DATE		<i>National Semiconductor</i> 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	MARTA SUCHY	04/18/2001			
DFTG. CHK.	THANH LEQUANG	02/01/2002			
ENGR. CHK.	VIRAJ PATWARDHAN	02/01/2002			
				MICRO SMD, 18 BUMP(LARGE), 0.5mm PITCH	
PROJECTION  INCH [MM]		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	(SC)MKT-BLA18XXX	B
DO NOT SCALE DRAWING				SHEET 2 of 2	