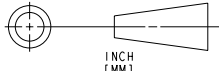


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
	SEE SHEET 1			

PACKAGE DIMENSIONS					
X1 DESIGNATOR	X1 PACKAGE WIDTH ±30μm	X2 DESIGNATOR	X2 PACKAGE LENGTH ±30μm	X3 DESIGNATOR	X3 PACKAGE HEIGHT ±100μm
A	777	A	1107	A	700
B	803	B	1133	B	850
C	828	C	1158	C	900
D	853	D	1184		
E	879	E	1209		
F	904	F	1234		
G	930	G	1260		
H	955	H	1285		
J	980	J	1311		
K	1006	K	1336		
L	1031	L	1361		
M	1057	M	1387		
N	1082	N	1412		
P	1107	P	1438		
Q	1133	Q	1463		
R	1158				
S	1184				
T	1209				
U	1234				
V	1260				
W	1285				
X	1311				
Y	1336				
2	1361				
3	1387				
4	1412				
5	1438				
6	1463				

APPROVALS		DATE		 <i>National Semiconductor</i> 2900 Semiconductor Dr., Santa Clara, CA 95052-8090 MICRO SMD, 5 BUMP, 0.5mm PITCH							
DRAWN MARTA SUCHY		08/26/1999									
DFTG. CHK. THANH LEQUANG		02/06/2002									
ENGR. CHK. VIRAJ PATWARDHAN		02/06/2002									
PROJECTION  INCH [MM]				SCALE N/A		SIZE C		DRAWING NUMBER (SC)MKT-BPA05XXX		REV C	
DO NOT SCALE DRAWING								SHEET 2 of 2			