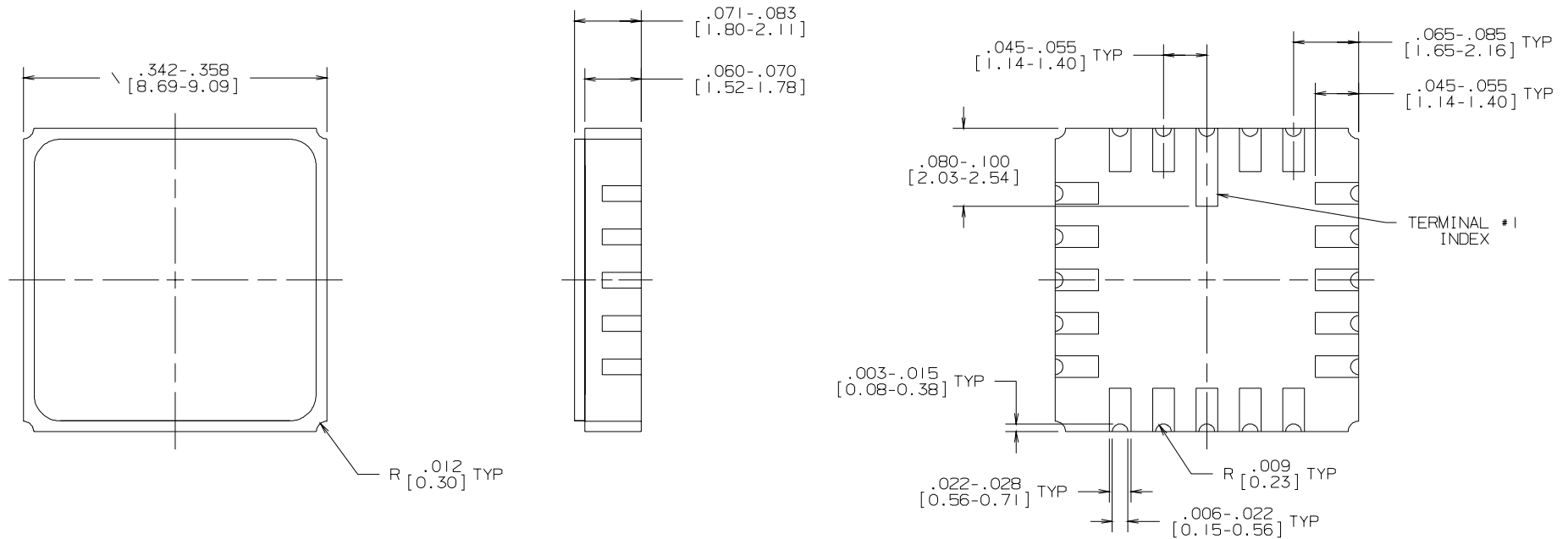
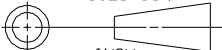


R E V I S I O N S				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	09023	01/28/92	MS/



NOTES: UNLESS OTHERWISE SPECIFIED

- ALL EXPOSED METAL AND METALIZED AREAS TO BE GOLD PLATED 60 MICROINCHES/ 1.5 MICROMETERS MINIMUM OVER 100 MICROINCHES/ 2.54 MICROMETERS MINIMUM OF NICKEL.
- NO JEDEC REGISTRATION AS OF 01-21-92.

CONTROLLING DIMENSION: INCH								
APPROVALS		DATE		 NATIONAL SEMICONDUCTOR CORPORATION 2900 Semiconductor Drive, Santa Clara, CA 95052-8090				
DRAWN MARTA SUCHY		01/28/92						
DFTG. CHK.								
ENGR. CHK.								
APPROVAL								
				LEADLESS CHIP CARRIER, CER., 20 TERMINALS, LG. CAV.				
 PROJECTION INCH [MM]				SCALE	SIZE	DRAWING NUMBER		REV
				N/A	B	MKT-EA20B		A
				DO NOT SCALE DRAWING			SHEET	I