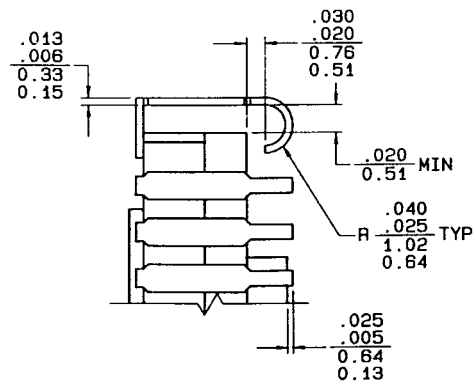
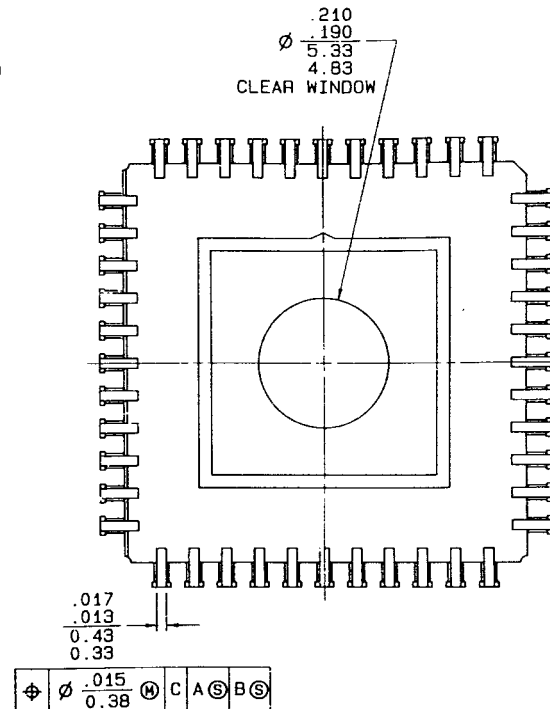
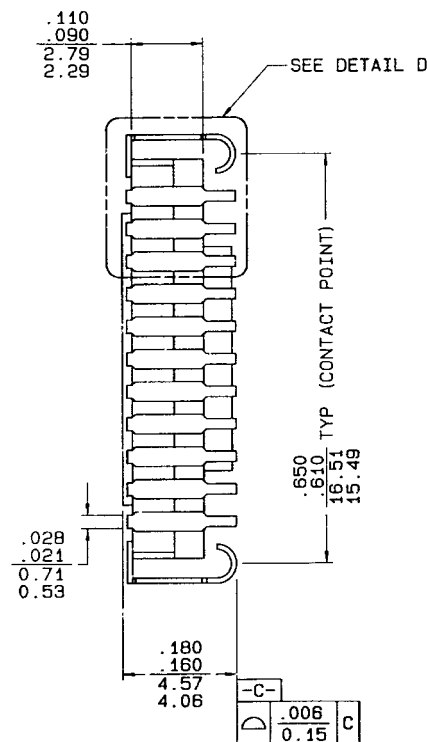
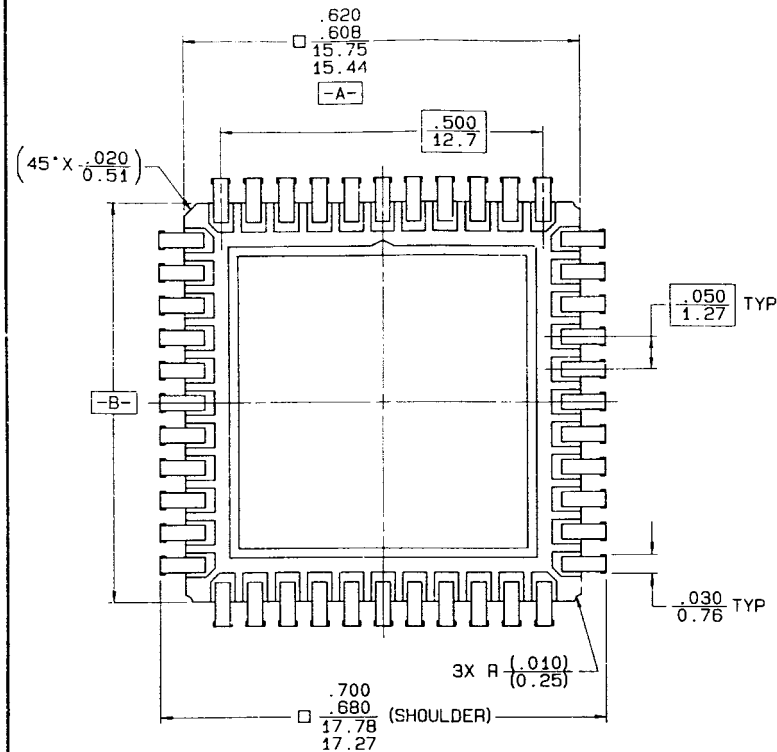


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REVISIONS			
LTR	DESCRIPTION	E.C.N.	DATE BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	07172	06/09/89 KES/kzf



NOTES: UNLESS OTHERWISE SPECIFIED

1. LEAD FINISH TO BE 50 TO 100 MICROINCHES GOLD PLATING OVER 50 TO 350 MICROINCHES NICKEL.

DETAIL D

APPROVALS		DATE	NATIONAL SEMICONDUCTOR CORPORATION	
DRAWN BY: <i>Paul S. Stancin</i>		08/09/89	2900 Semiconductor Drive, Santa Clara, CA 95052-8080	
CHECKED BY: <i>Walter R. Huley</i>		06/14/89	LEADED CHIP CARRIER, 44 LEAD, DUAL CAVITY	
APPROVED BY: <i>W. Huley</i>		6/14/89		
PROJECTION		1/1	SCALE	SIZE
1 INCH = 10 MM			N/A	C
			DRAWING NUMBER	REV
			MKT-EL44B	A
			DO NOT SCALE DRAWING	SHEET 1 OF 1