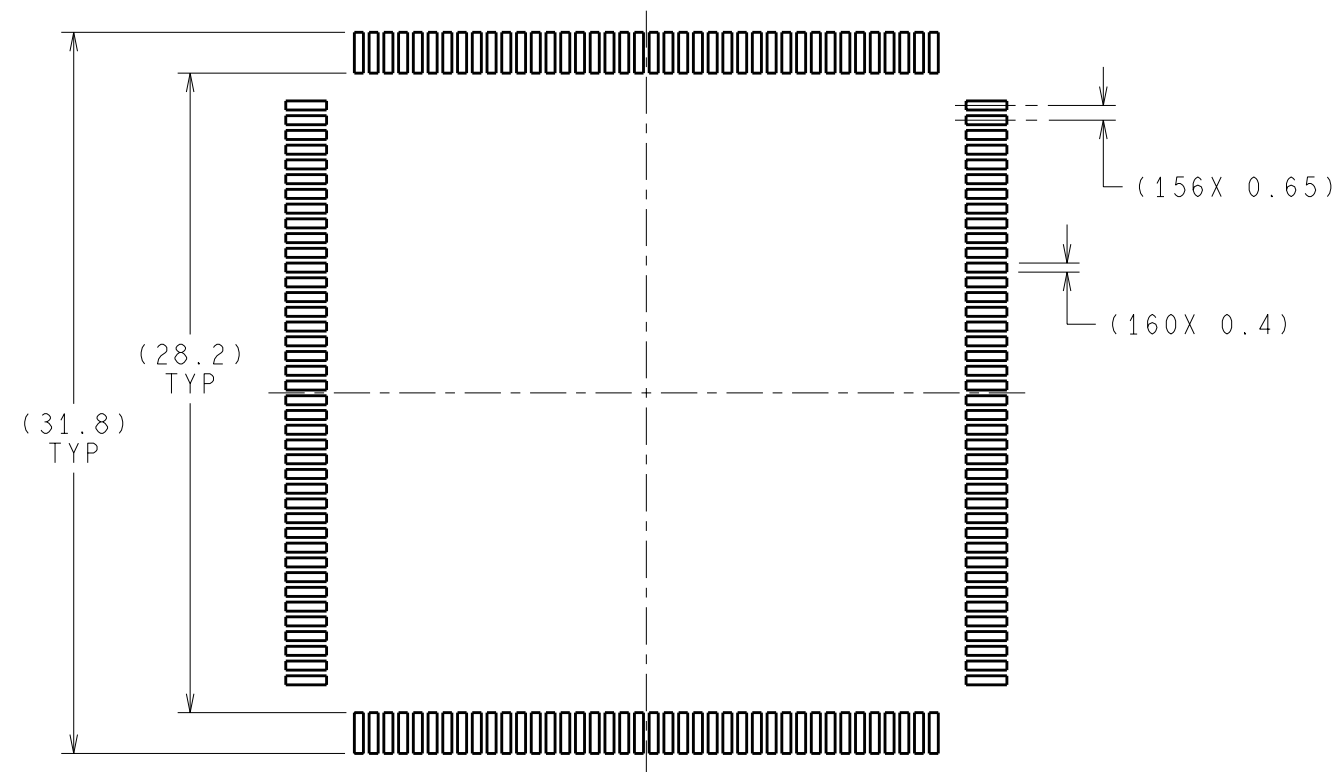
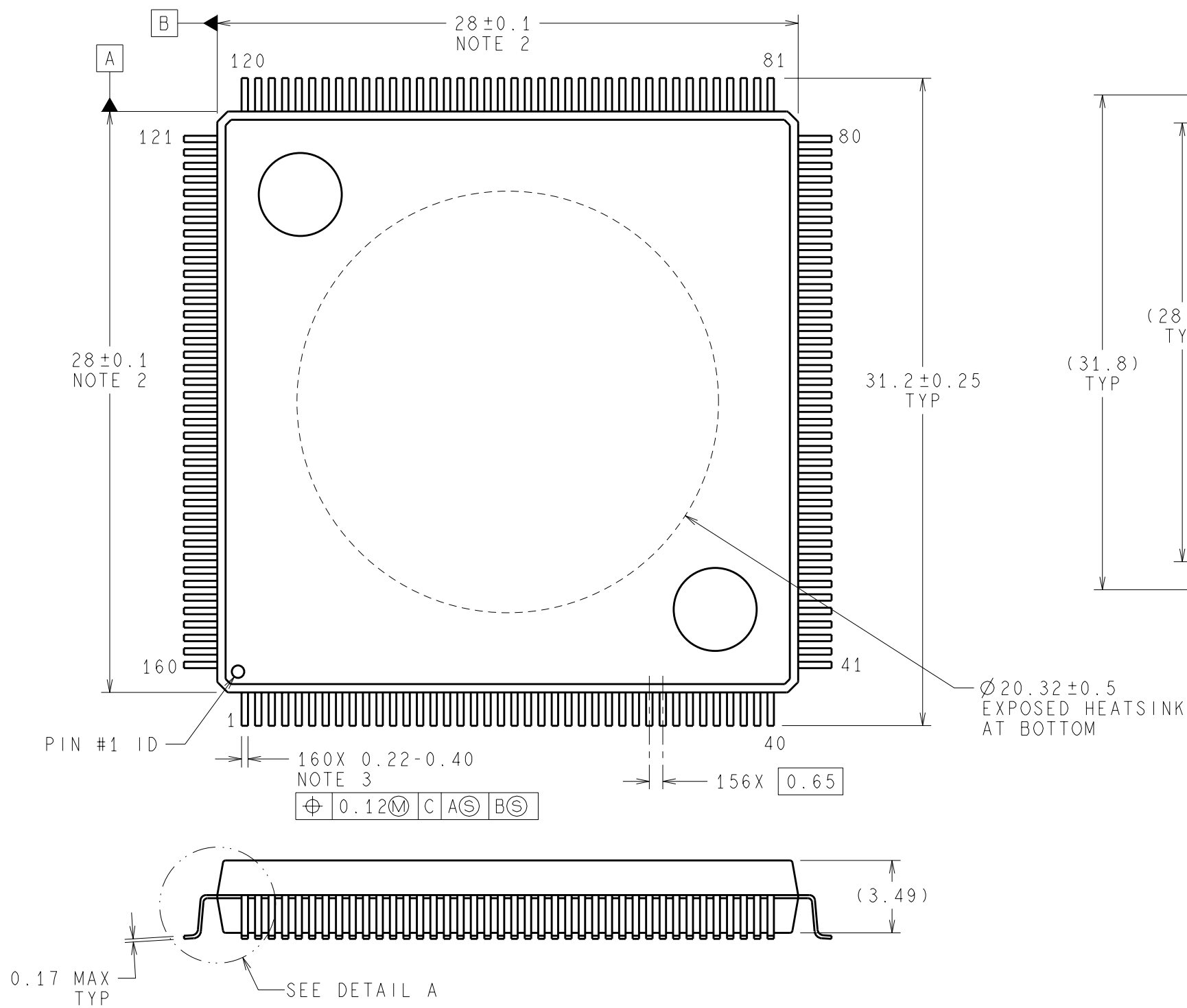
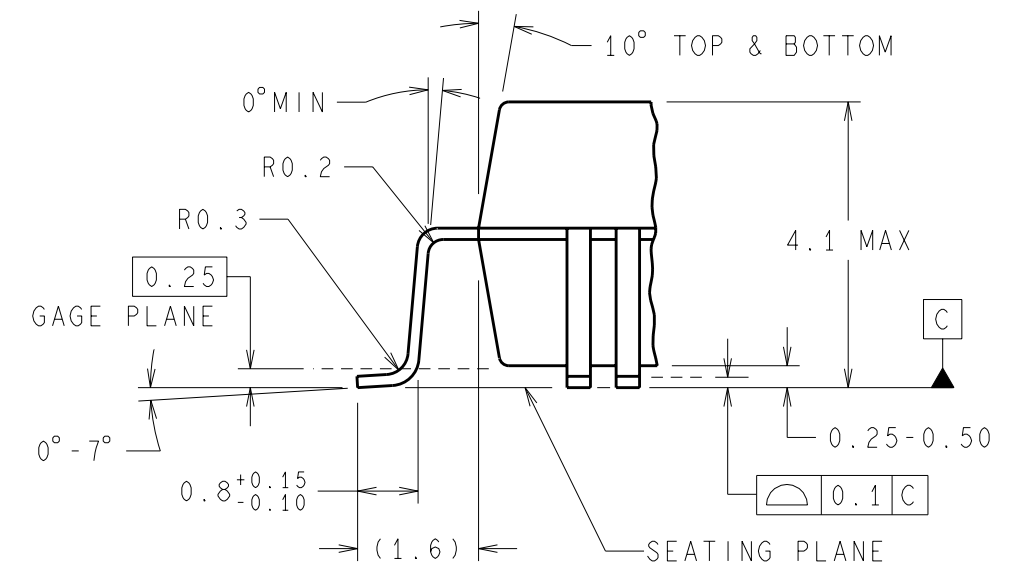


REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	947	02/28/2003	TL/JS



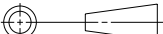
RECOMMENDED LAND PATTERN



DETAIL A
TYP, SCALE: 10X

- NOTES: UNLESS OTHERWISE SPECIFIED
- FOR LEAD FINISH THICKNESS AND COMPOSITION, SEE "SOLDER INFORMATION" IN THE PACKAGING SECTION OF THE NATIONAL SEMICONDUCTOR WEB PAGE (www.national.com).
 - DIMENSION DOES NOT INCLUDE MOLD PROTRUSION. MAXIMUM ALLOWABLE MOLD PROTRUSION 0.25mm PER SIDE.
 - DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.15mm.
 - NO JEDEC REGISTRATION AS OF FEBRUARY 2003.

DIMENSIONS ARE IN MILLIMETERS

APPROVALS		DATE		 National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090							
DRAWN	T. LEQUANG		02/28/2003								
DFTG. CHK.	MARTA SUCHY		02/28/2003								
ENGR. CHK.	JOE SMITH		02/28/2003								
PROJECTION				SCALE		SIZE		DRAWING NUMBER		REV	
				NTS		B		(SC)MKT-EQB160A		A	
MM				FORMERLY: N/A						SHEET 1 of 1	