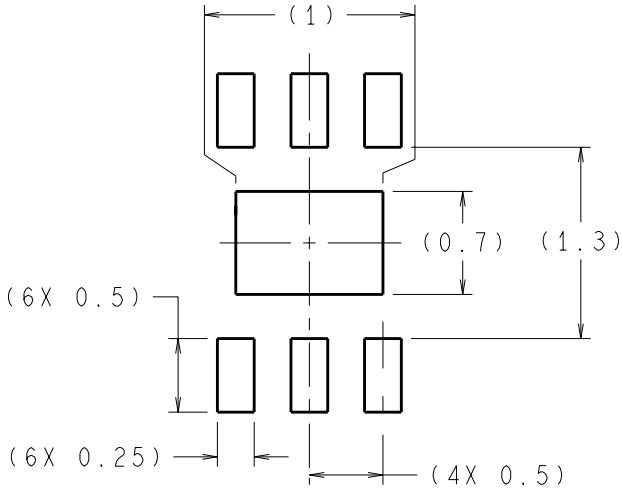
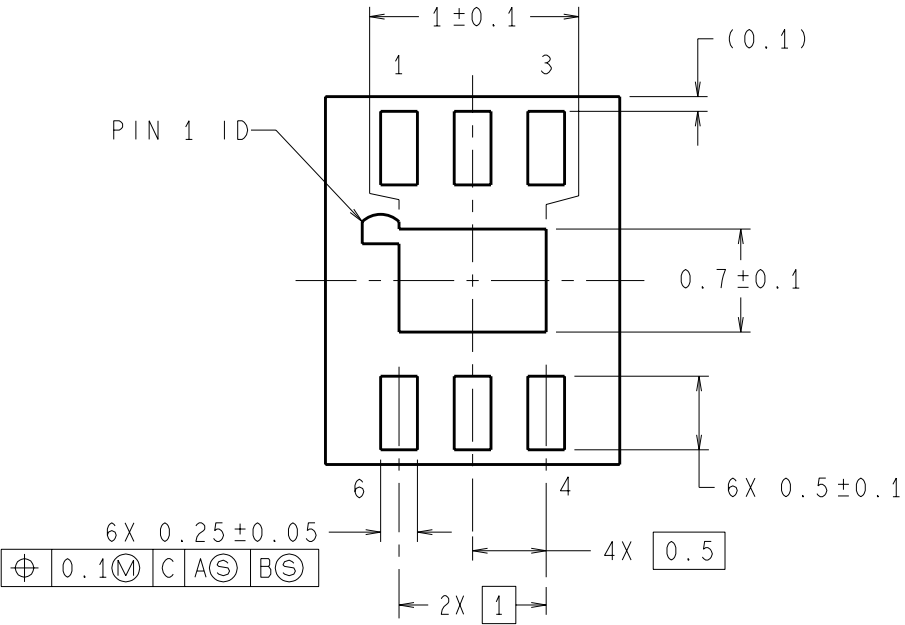
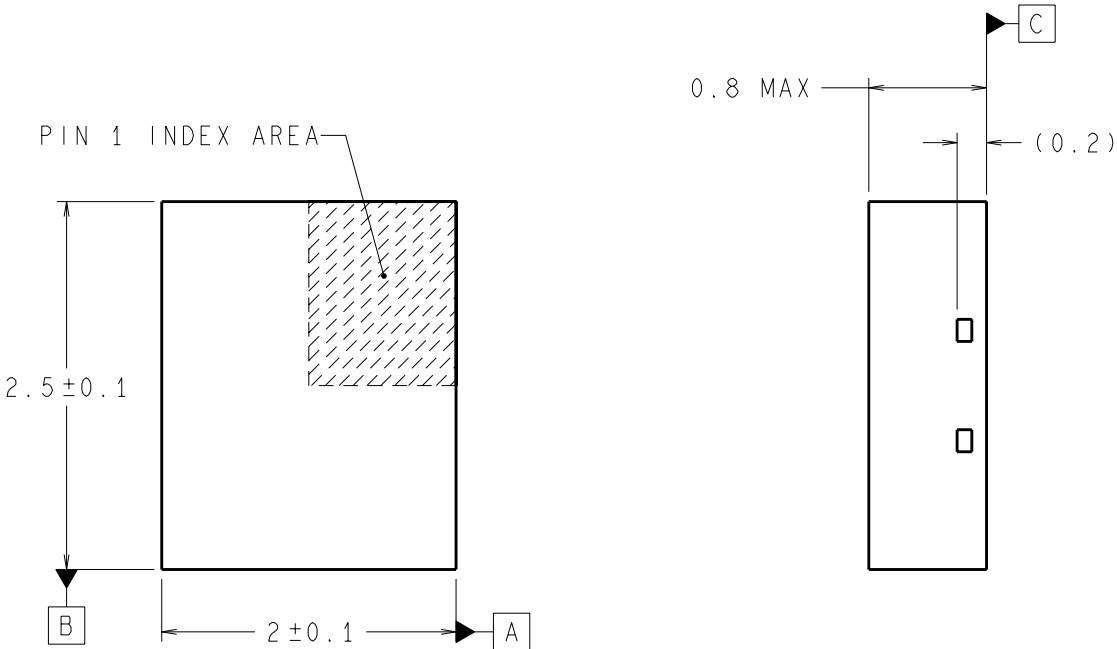


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12519	04/04/2001	SN/MS/AL



RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS

DIMENSIONS ARE IN MILLIMETERS



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF APRIL 2001.

APPROVALS		DATE		 National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	N. SANTHIRAN	04/04/2001			
DFTG. CHK.	MARTA SUCHY	04/04/2001		LLP, PLASTIC, DUAL, 2.0 X 2.5 X 0.75 mm BODY, 6 LD, 0.5 mm PITCH	
ENGR. CHK.	ANTHONY LAW	04/04/2001			
				SCALE	SIZE
				N/A	C
				DRAWING NUMBER	REV
				(SC)MKT-LDA06A	A
				FORMERLY: N/A	SHEET 1 of 1