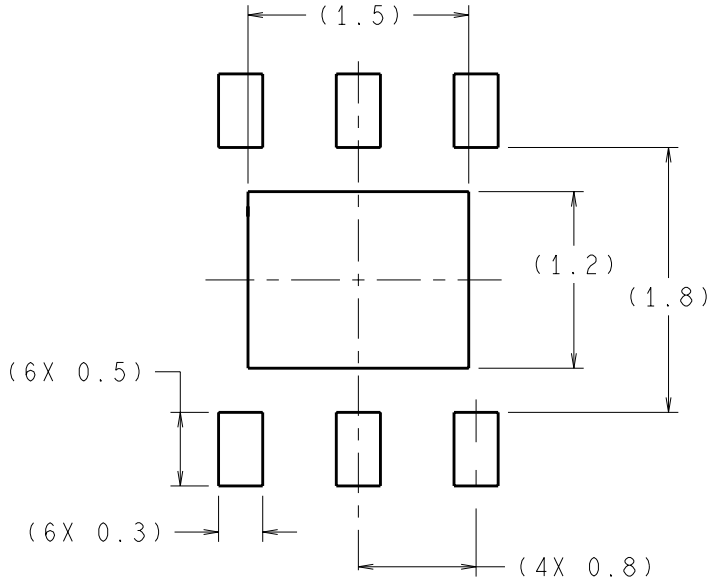
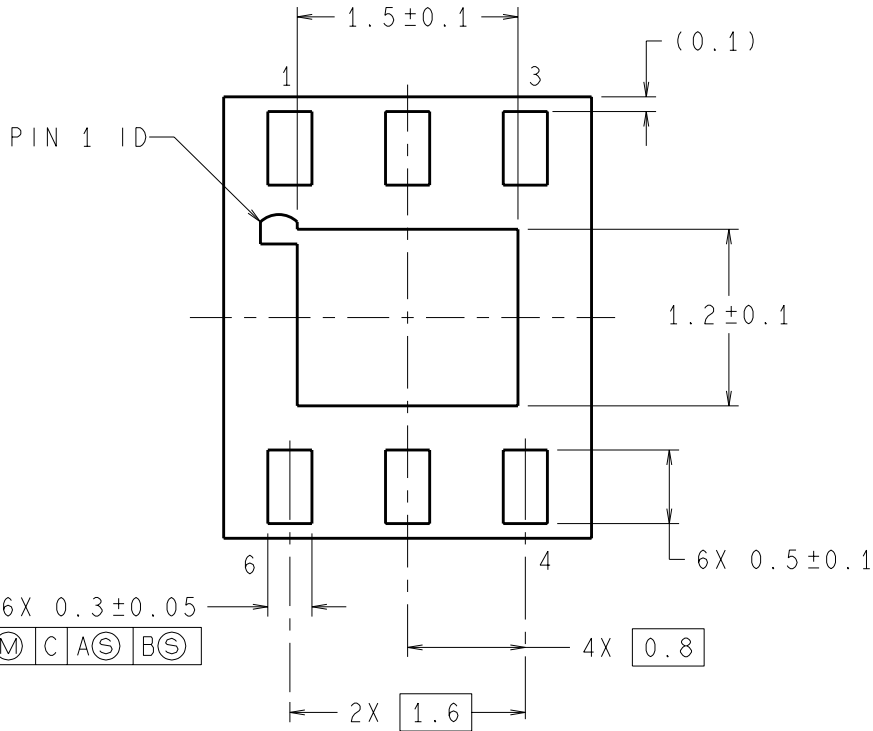
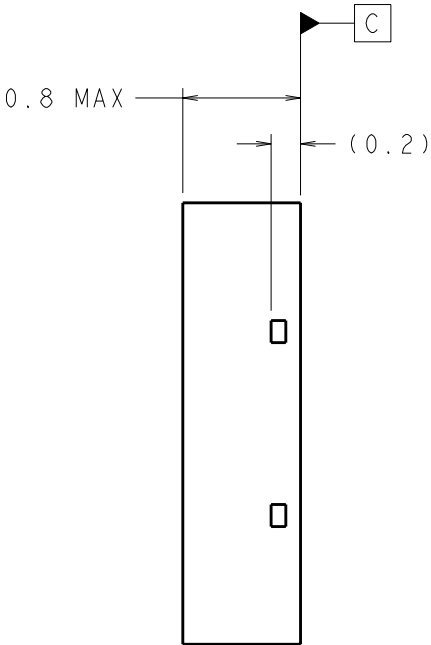
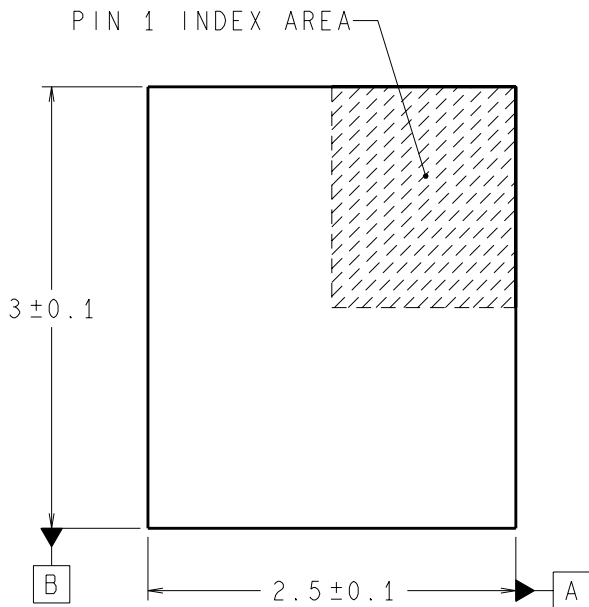


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12522	12/04/2000	SN/TL/AL



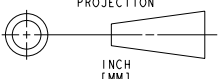
DIMENSIONS ARE IN MILLIMETERS

RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



NOTES: UNLESS OTHERWISE SPECIFIED.

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF DECEMBER 2000.

APPROVALS		DATE	 National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	W. SANTHIRAN & TL	12/04/2000		
DFTG. CHK.	THANH LEQUANG	12/04/2000	LLP, PLASTIC, DUAL, 2.5 X 3 X 0.75 mm BODY, 6 LD, 0.8 mm PITCH	
ENGR. CHK.	ANTHONY LAW	12/04/2000		
 INCH [MM]		SCALE	SIZE	DRAWING NUMBER
		N/A	C	(SC)MKT-LDC06B
		FORMERLY: N/A		REV
				A
		SHEET 1 of 1		