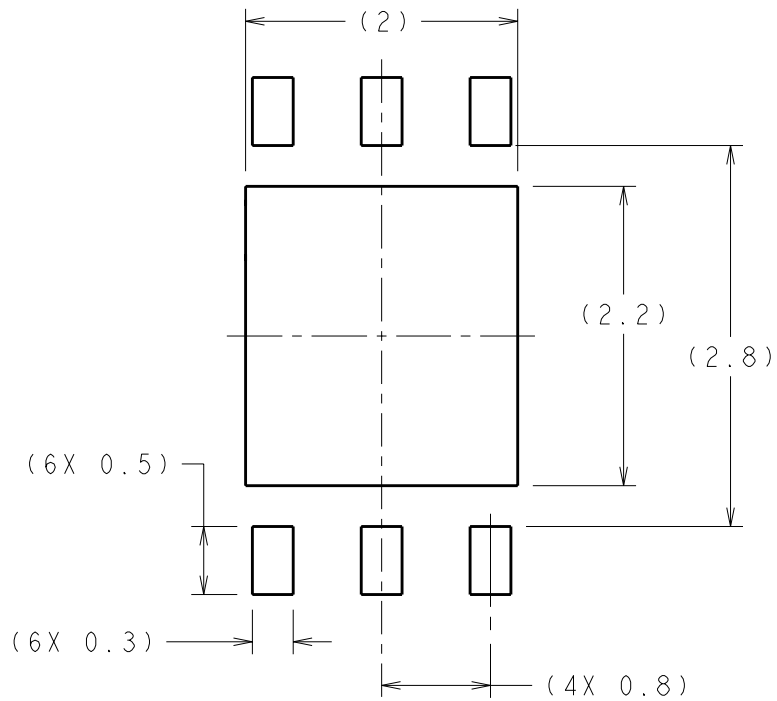
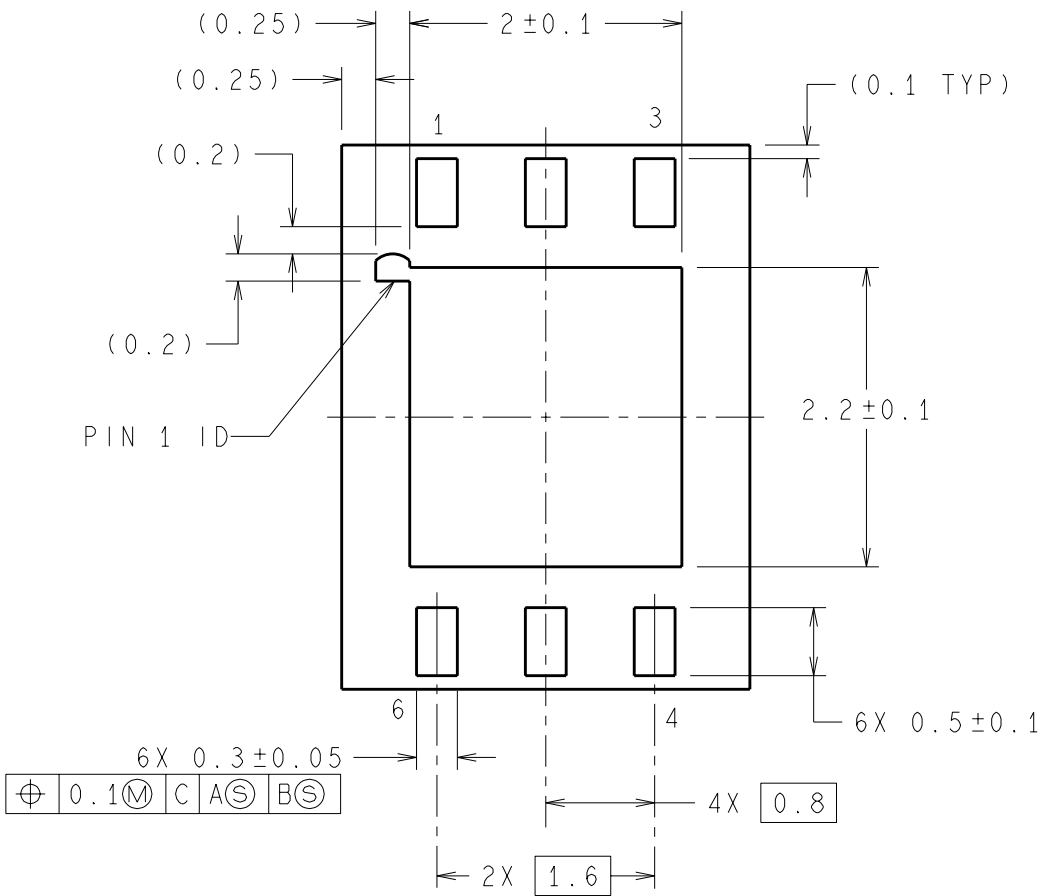
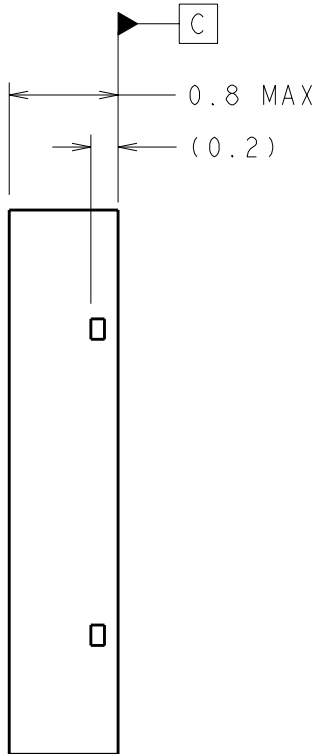
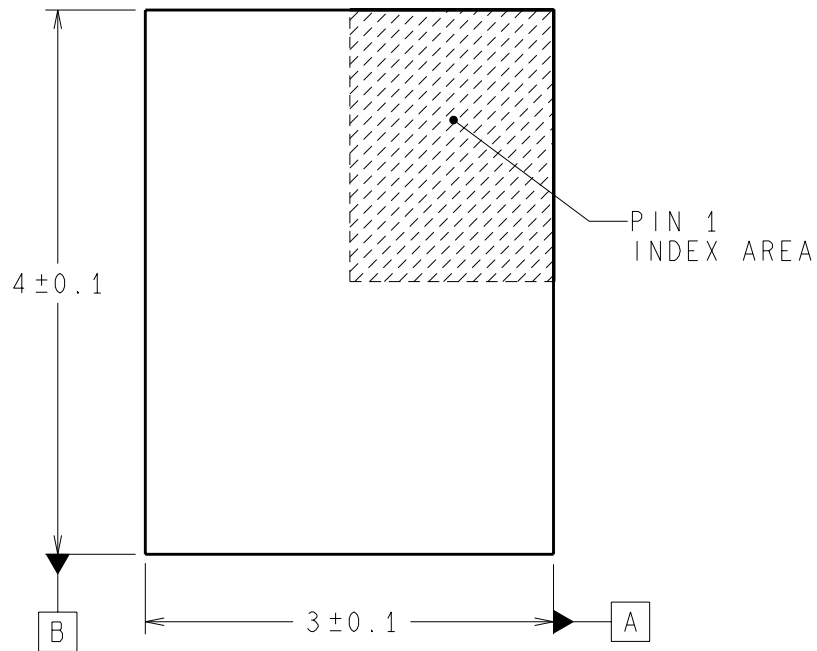


REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12532	12/06/2000	SN/TL/AL
B	ADD PIN 1 ID DIM'S; CHANGE FORMAT TO B SIZE; UPDATE NOTE 2	884	11/14/2002	TL/FL



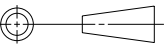
DIMENSIONS ARE IN MILLIMETERS

RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



NOTES: UNLESS OTHERWISE SPECIFIED.

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF NOVEMBER 2002.

APPROVALS		DATE		 National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	NSANTHIRAN & TL		12/06/2000		
DFTG. CHK.	MARTA SUCHY		11/14/2002		
ENGR. CHK.	FELIX LI		11/14/2002		
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		NTS	B	(SC)MKT-LDC06D	B
FORMERLY: N/A		SHEET 1 of 1			