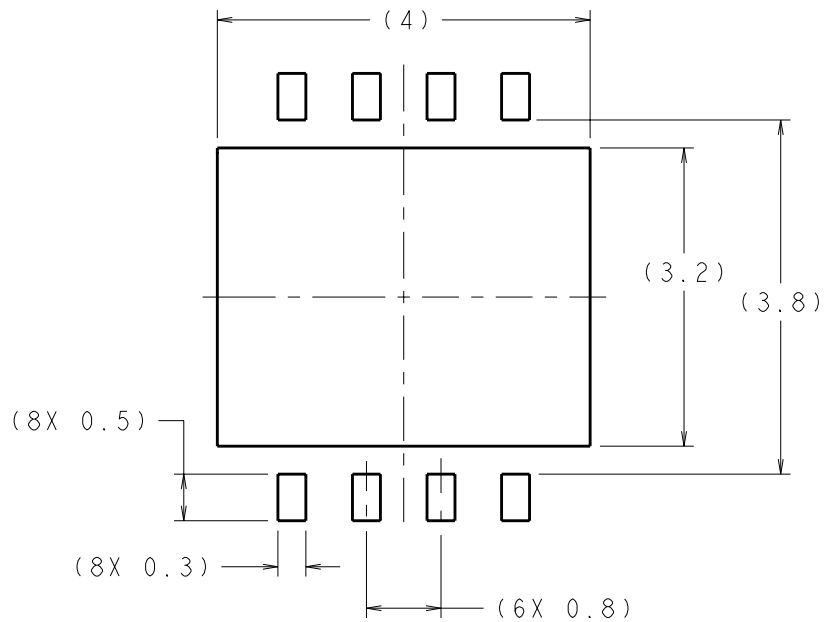
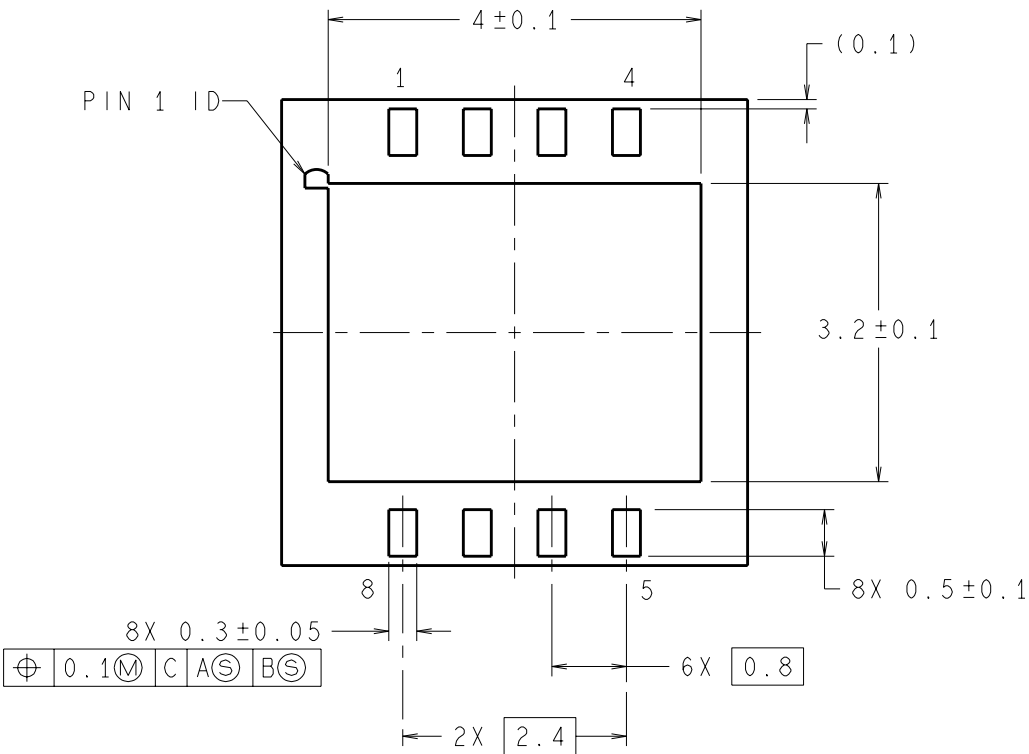
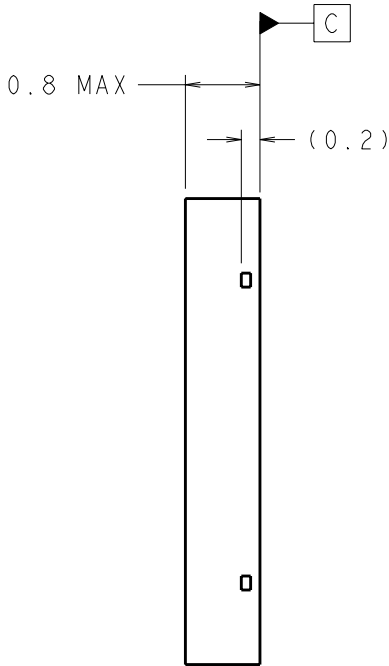
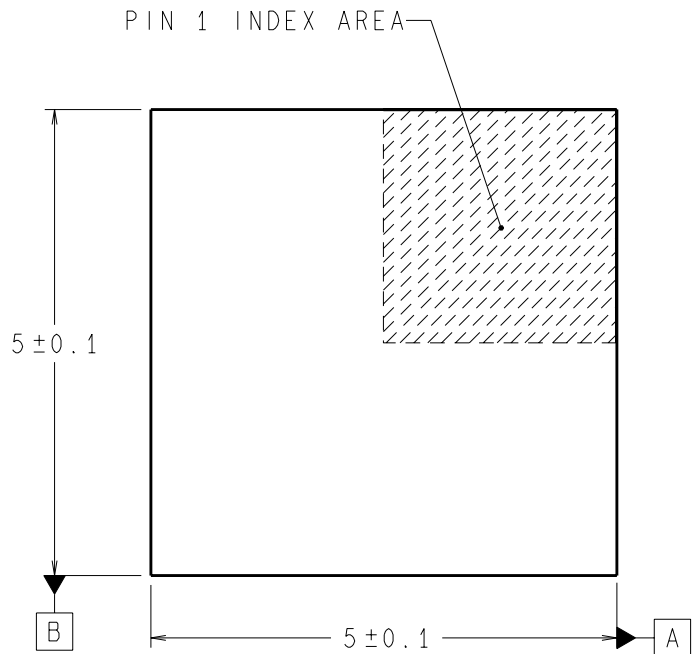


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12534	12/20/2000	SN/TL/AL



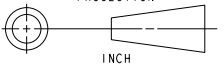
DIMENSIONS ARE IN MILLIMETERS

RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



NOTES: UNLESS OTHERWISE SPECIFIED.

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF DECEMBER 2000.

APPROVALS		DATE		 <i>National Semiconductor</i> 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN N. SANTHIRAN & TL		12/20/2000			
DFTG. CHK. THANH LEQUANG		12/20/2000		LLP, PLASTIC, DUAL, 5 X 5 X 0.75 mm BODY, 8 LD, 0.8 mm PITCH	
ENGR. CHK. ANTHONY LAW		12/20/2000			
		SCALE		SIZE	DRAWING NUMBER
		N/A		C	(SC)MKT-LDC08C
FORMERLY: N/A		SHEET 1 of 1		REV	A