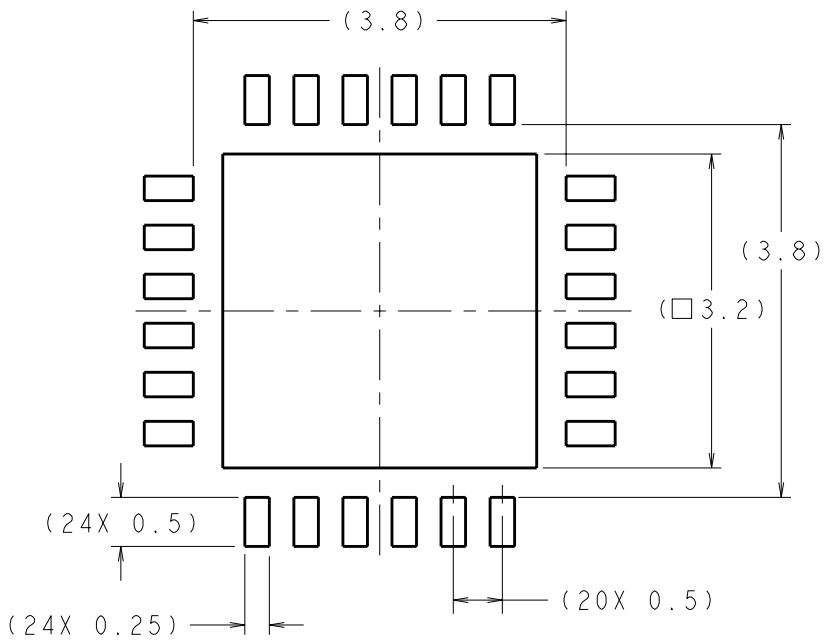
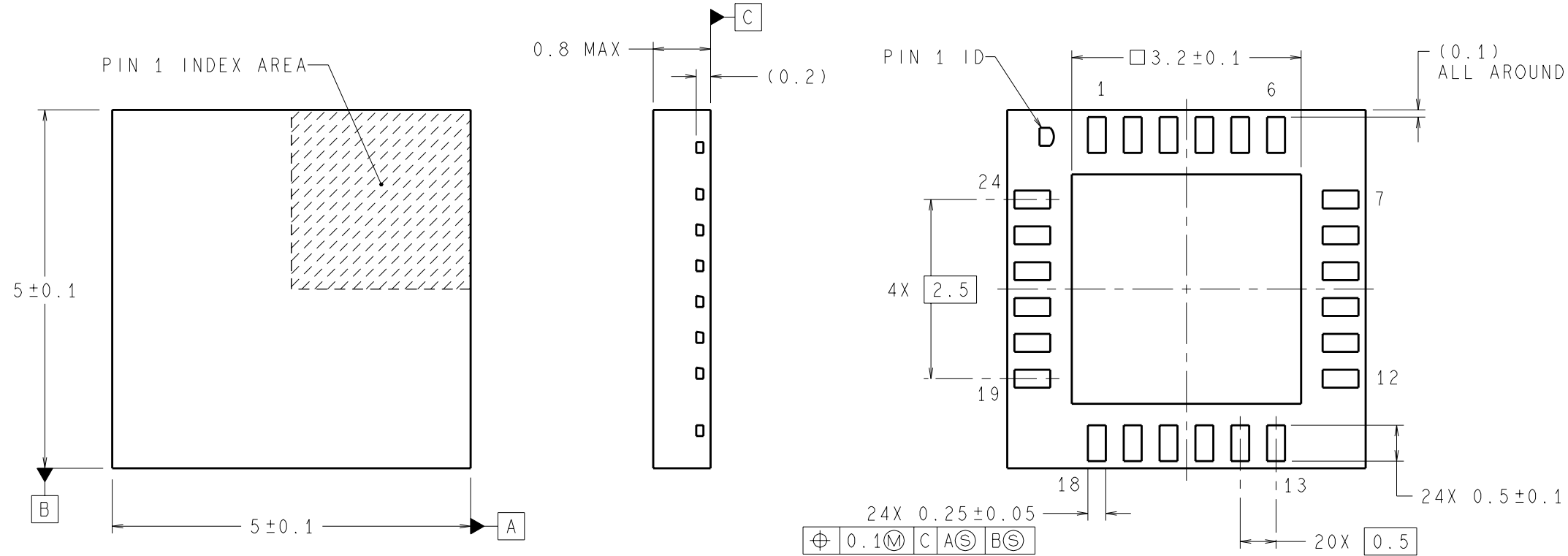


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12559	02/15/2001	AS/MS/NS

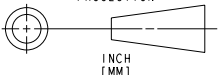


RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



DIMENSIONS ARE IN MILLIMETERS

- NOTES: UNLESS OTHERWISE SPECIFIED
1. STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
 2. NO JEDEC REGISTRATION AS OF FEBRUARY 2001.

APPROVALS		DATE		 <i>National Semiconductor</i> 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	AFZNIAM S.		02/15/2001		
DFTG. CHK.	MARTA SUCHY		02/15/2001	LLP, PLASTIC, QUAD, 5 X 5 X 0.8mm BODY, 24 LD, 0.5mm PITCH	
ENGR. CHK.	N. SANTHIRAN		02/15/2001		
		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	(SC)MKT-LQA24B	A
DO NOT SCALE DRAWING				SHEET 1 of 1	