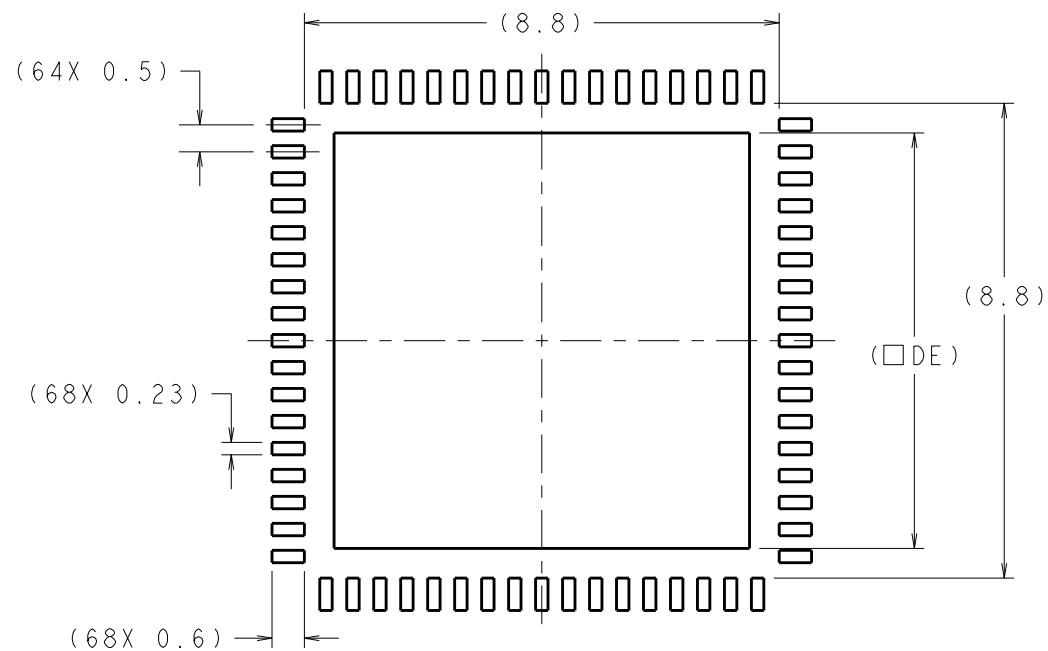
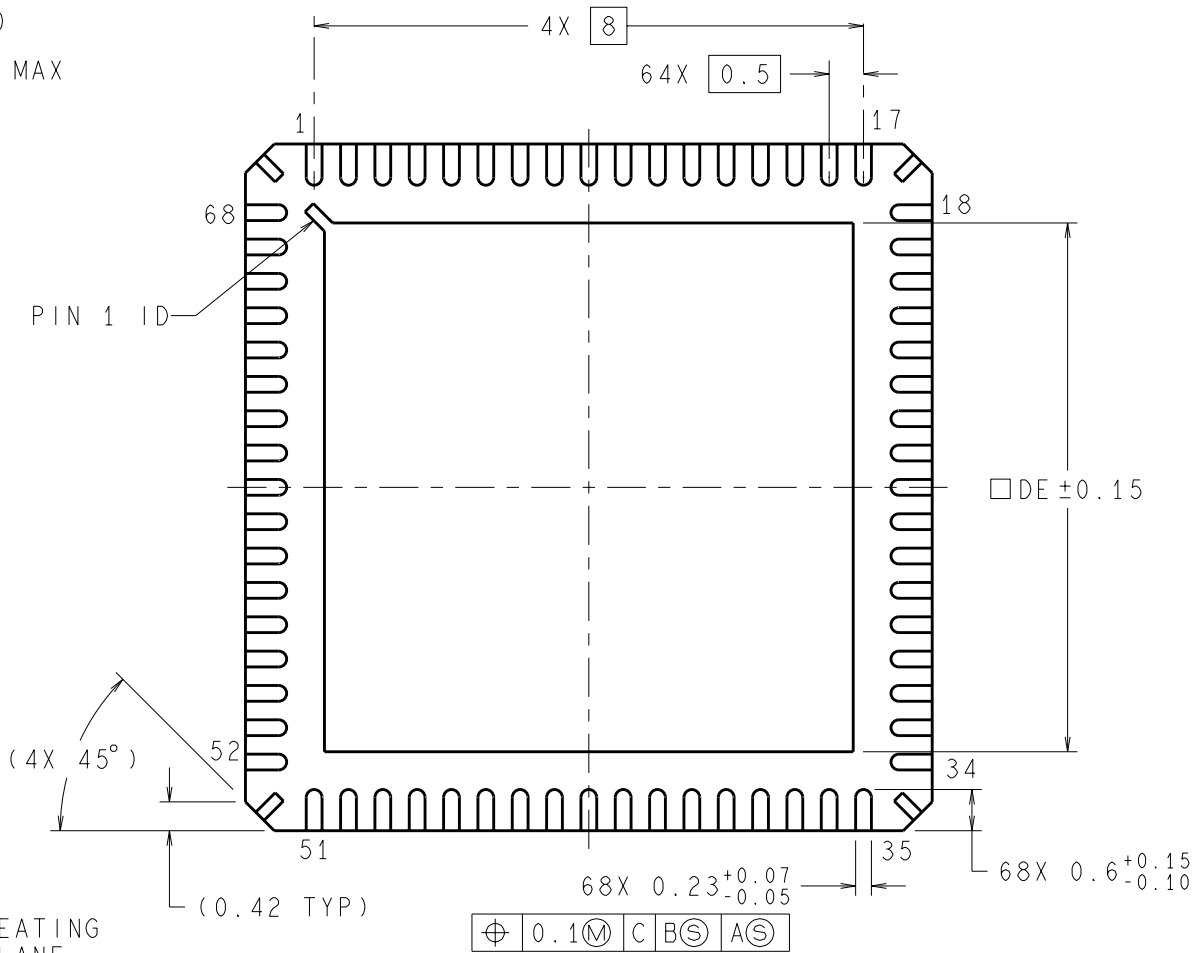
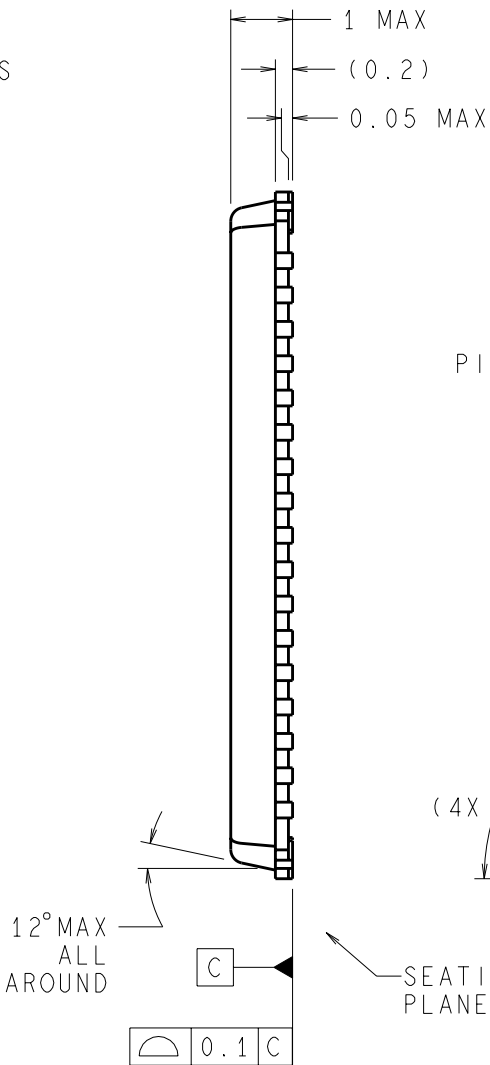
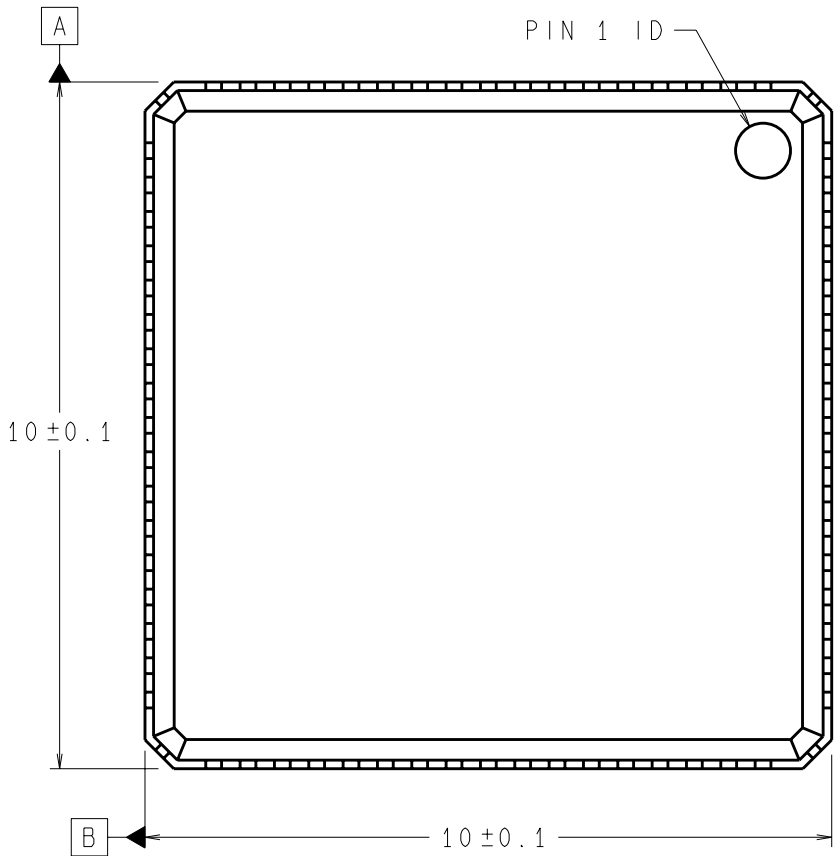




DIM DE	OPT 1	OPT 2
	7.7	8.1

DIMENSIONS ARE IN MILLIMETERS

RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF APRIL 2001.

REVISIONS			
LTR	DESCRIPTION	E.C.N.	DATE
A	RELEASE TO DOCUMENT CONTROL	187	04/19/2001

APPROVALS		DATE	 <i>National Semiconductor</i> 2900 Semiconductor Dr., Santa Clara, CA 95052-8090			
DRAWN T. LEQUANG		04/19/2001				
DFTG. CHK. MARTA SUCHY		04/19/2001				
ENGR. CHK. CORNEL DEVERA		04/19/2001				
			LLP, PLASTIC, QUAD, 10 X 10 X 1 mm BODY, 68 LD, 0.5 mm PITCH			
 PROJECTION INCH [MM]			SCALE N/A	SIZE C	DRAWING NUMBER (SC)MKT-LQA68A	REV A
			DO NOT SCALE DRAWING		SHEET 1 of 1	