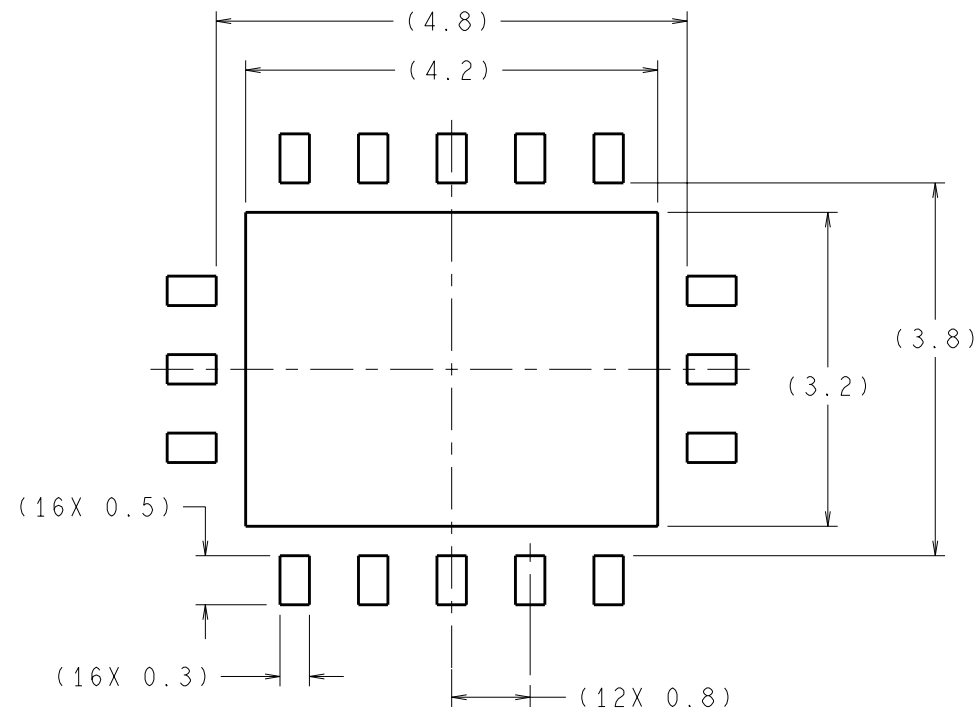
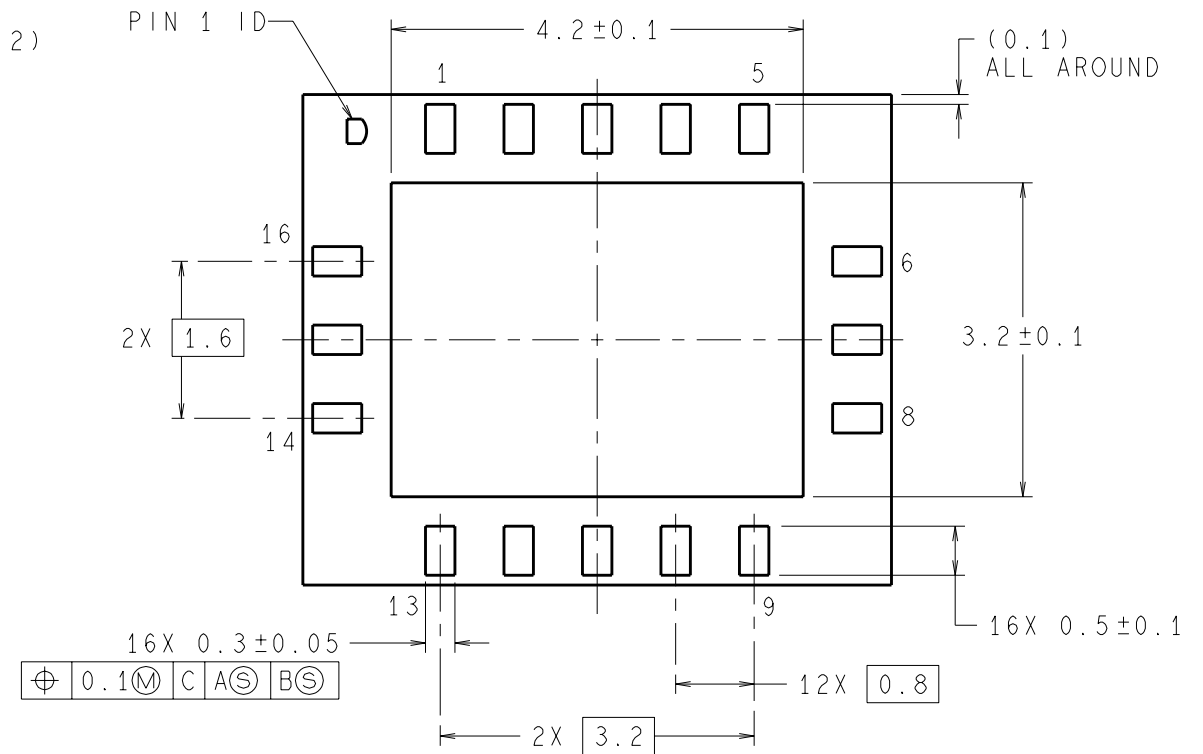
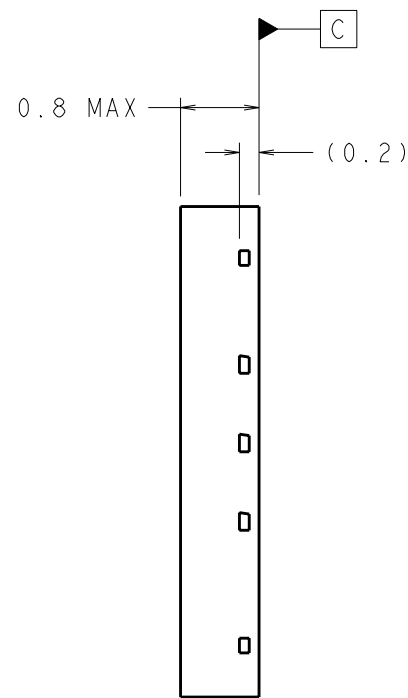
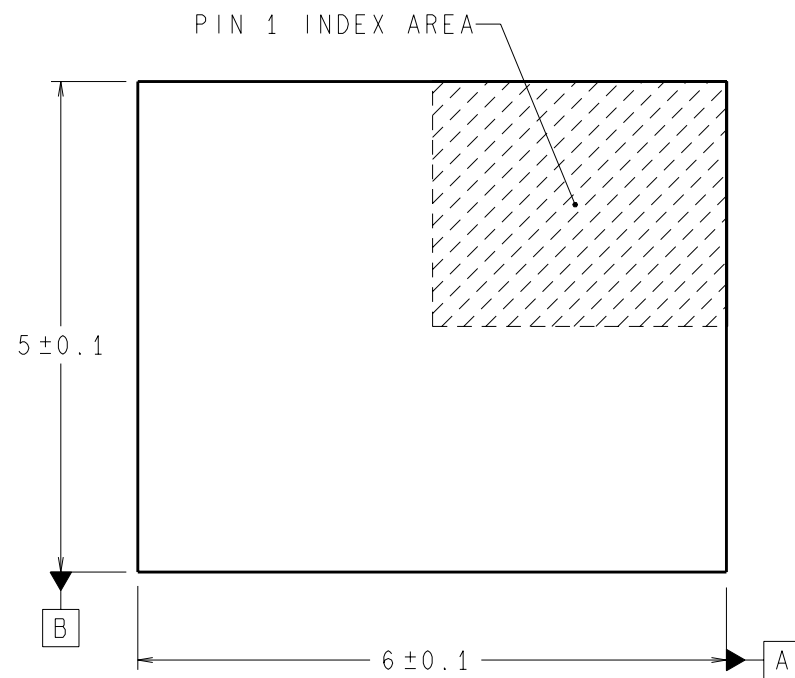


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12579	01/22/2001	AS/TL/SN



DIMENSIONS ARE IN MILLIMETERS

**RECOMMENDED LAND PATTERN**  
1:1 RATIO WITH PKG SOLDER PADS



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF JANUARY 2001.

APPROVALS	DATE	National Semiconductor			
DRAWN <b>AFZMAM S.</b>	01/22/2001	2900 Semiconductor Dr., Santa Clara, CA 95052-8090			
DFTG. CHK. <b>THANH LEQUANG</b>	01/22/2001	<b>LLP, PLASTIC, QUAD, 6 X 5 X 0.8mm BODY, 16 LD, 0.8mm PITCH</b>			
ENGR. CHK. <b>N. SANTHIRAN</b>	01/22/2001				
		SCALE <b>N/A</b>	SIZE <b>C</b>	DRAWING NUMBER <b>(SC)MKT-LQC16B</b>	REV <b>A</b>
		DO NOT SCALE DRAWING		SHEET 1 of 1	