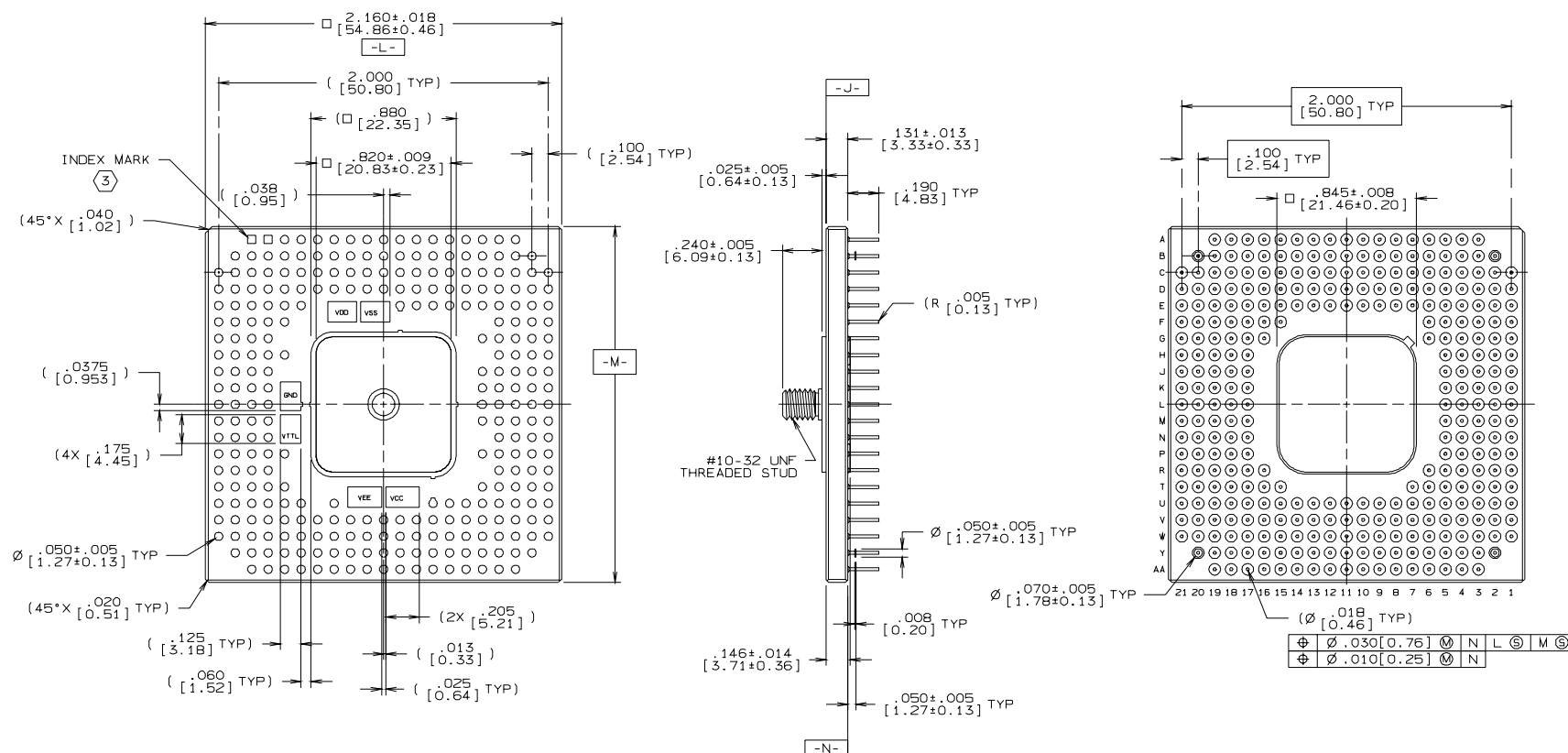


THIS DRAWING IS THE PROPERTY OF NATIONAL SEMICONDUCTOR CORPORATION. NO USE THEREOF SHALL BE MADE OTHER THAN AS A REFERENCE FOR PROPOSALS AS SUBMITTED TO NATIONAL SEMICONDUCTOR CORPORATION FOR JOBS TO BE EXECUTED IN CONFORMITY WITH SUCH PROPOSALS UNLESS THE CONSENT OF SAID NATIONAL SEMICONDUCTOR CORPORATION HAS PREVIOUSLY BEEN OBTAINED. NO PART OF THIS DRAWING SHALL BE COPIED OR DUPLICATED OR ITS CONTENTS DISCLOSED. THE INFORMATION CONTAINED ON THIS DRAWING IS CONFIDENTIAL AND PROPRIETARY.

R E V I S I O N S				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'R
A	RELEASE TO DOCUMENT CONTROL.	10063	08/30/94	DEG/

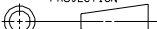


CONTROLLING DIMENSIONS: INCH

NOTES: UNLESS OTHERWISE SPECIFIED

1. PLATING: ALL EXPOSED METAL AND METALLIZED AREA TO BE GOLD PLATED 100 MICRO INCHES MINIMUM OVER 50-350 MICRO INCHES OF NICKEL.
2. THE ALLOWABLE TORQUE STRENGTH APPLIED TO THE STUD IS 15 In-lb MAXIMUM.
3. INDEX MARK MAY BE ANY SHAPE OTHER THAN CIRCLE.
4. NO JEDEC REGISTRATION AS OF 9/15/93.

02	WITHOUT STUD
01	WITH STUD
OPT	DESCRIPTION

APPROVALS		DATE	QTY		DESCRIPTION
DRAWN	D.E. Grady	08/30/94	 National Semiconductor 2900 Semiconductor dr, Santa Clara, CA 95052-8010		
DFTG. CHK.			PIN GRID ARRAY, CERAMIC, 319 PIN, CAVITY DOWN		
ENGR. CHK.					
APPROVAL					
PROJECTION 			SCALE	C	DRAWING NUMBER
			N/A	SIZE	MKT-UA319A
FORMERLY:			N/A	SHEET	1 OF 1