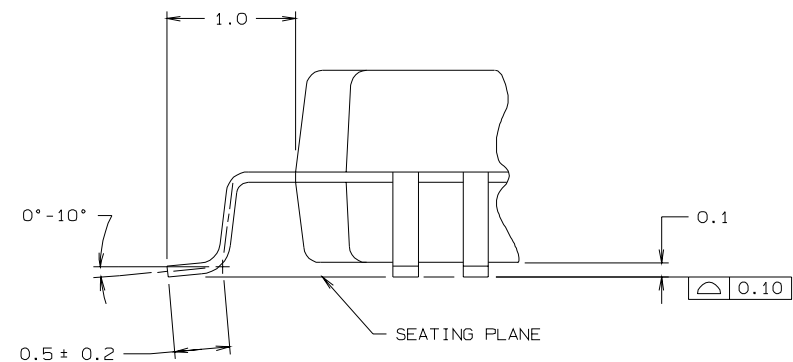
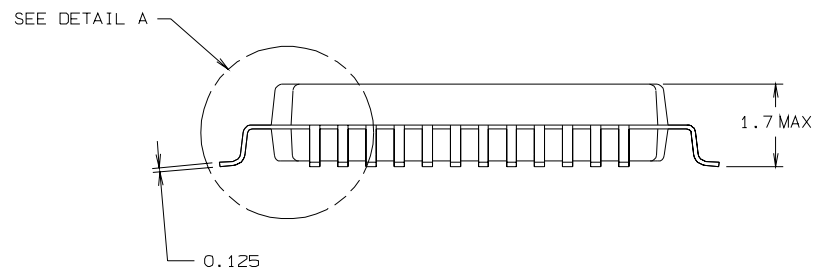
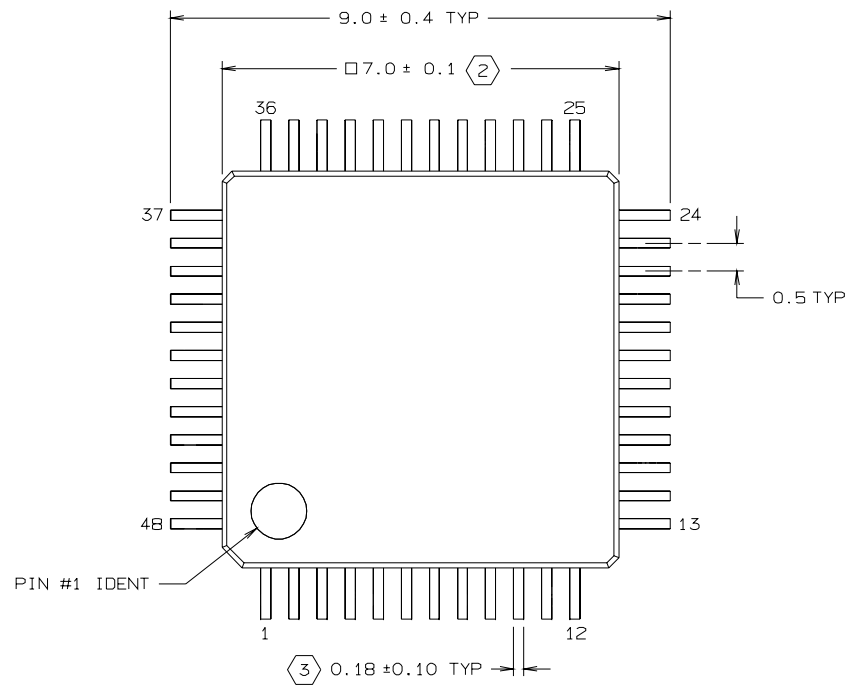


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	09076	04/20/92	DEG/



DETAIL A
TYPICAL

NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH:
5.08 MICROMETERS MINIMUM SOLDER PLATING (80/20)
THICKNESS ON ALLOY 42 / COPPER.
- (2) DIMENSION DOES NOT INCLUDE MOLD PROTRUSION.
MAXIMUM ALLOWABLE MOLD PROTRUSION 0.25 mm PER SIDE.
- (3) DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION.
ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.15.

DIMENSIONS ARE IN MILLIMETERS

APPROVALS	DATE	NATIONAL SEMICONDUCTOR CORPORATION		
DRAWN D.E. GRADY	04/20/92	2900 Semiconductor Drive, Santa Clara, CA 95052-8090		
DFTG. CHK.		PLASTIC QUAD FLATPACK, (S), 7 X 7 mm 48 LEAD		
ENGR. CHK.				
APPROVAL				
PROJECTION		SCALE	SIZE	DRAWING NUMBER
[INCH [MM]		N/A	C	MKT-VBG48A
		DO NOT SCALE	DRAWING	SHEET 1 OF 1