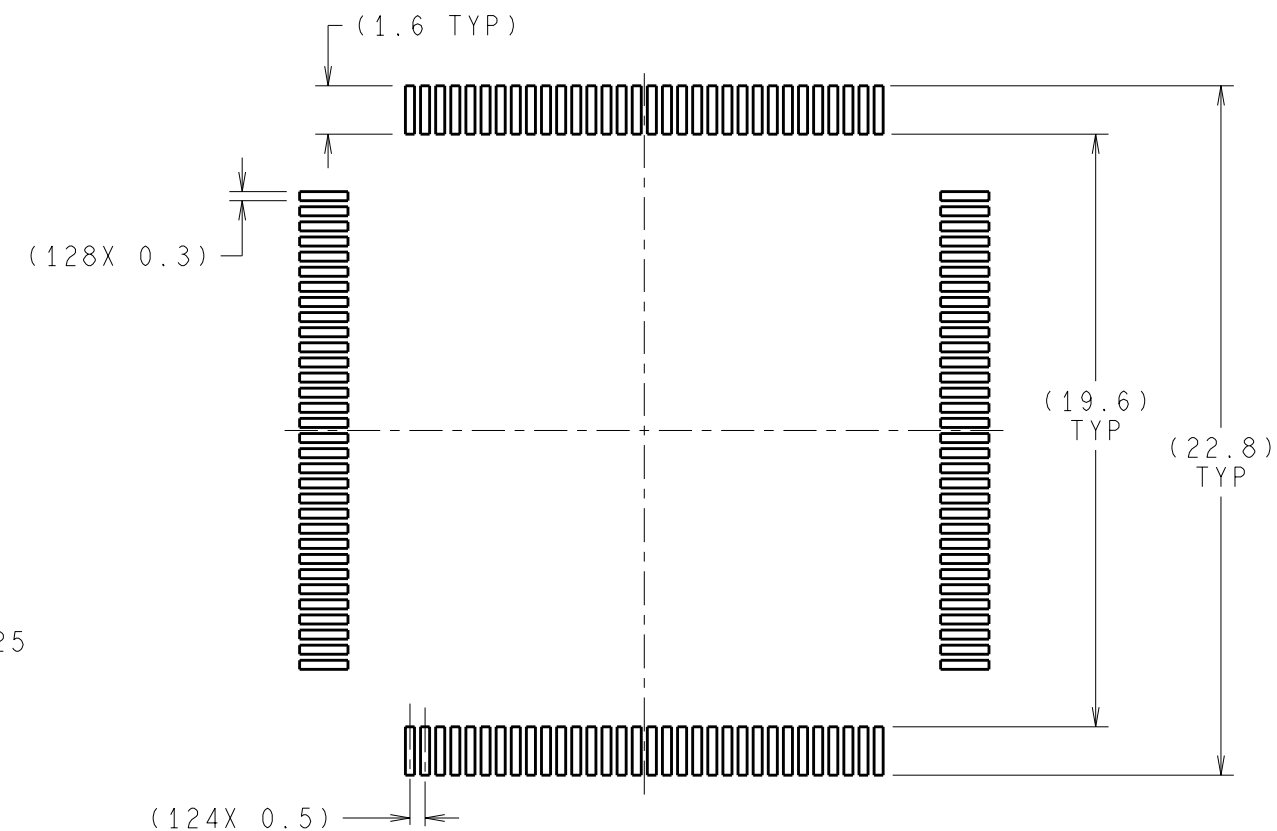
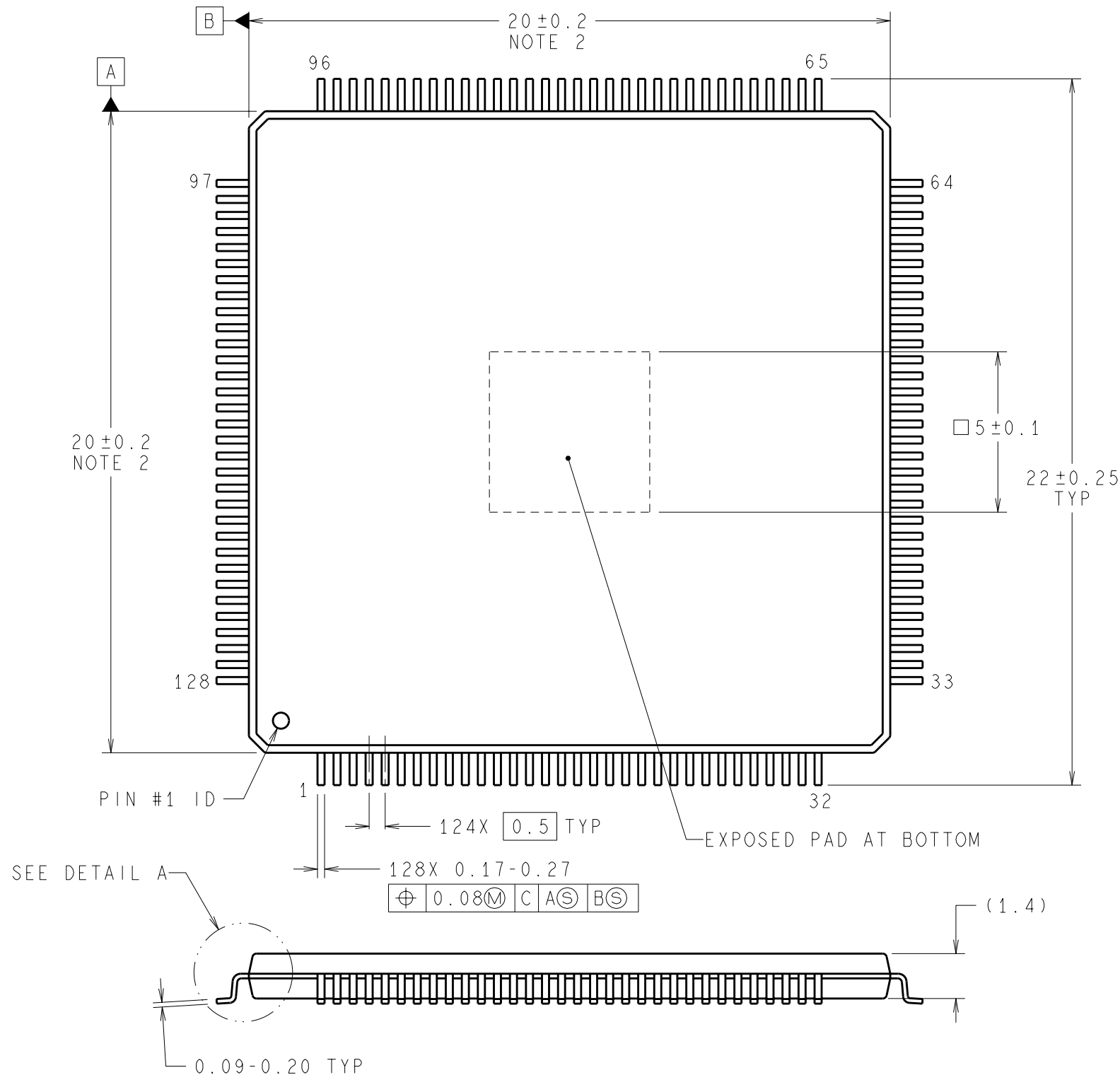
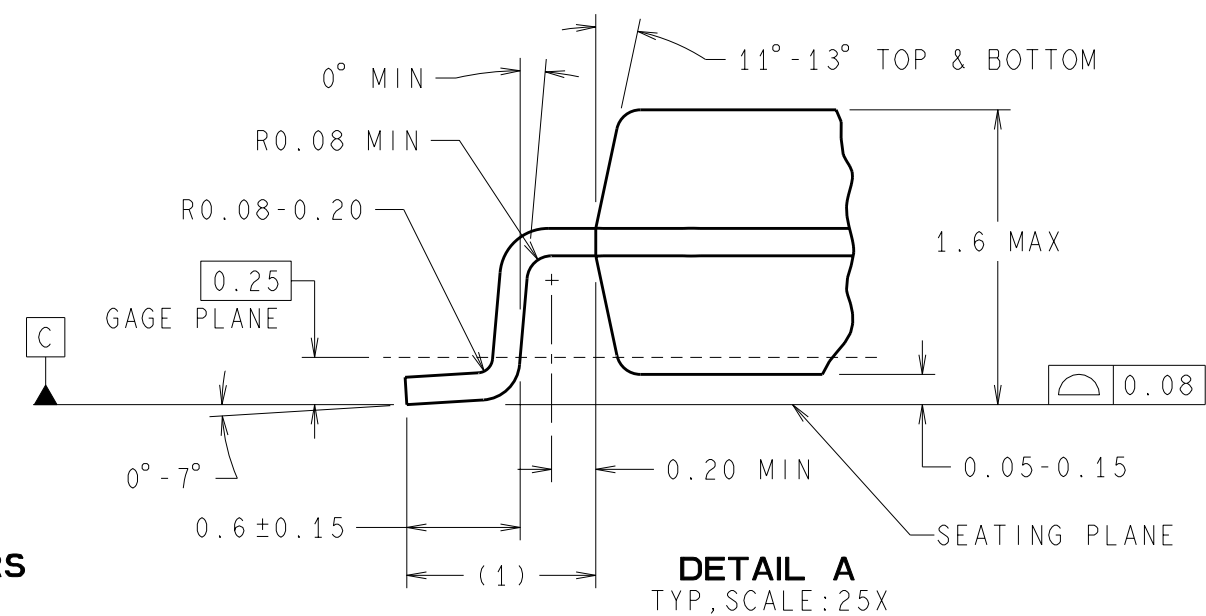


REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	924	03/14/2003	MS/RW



RECOMMENDED LAND PATTERN



DETAIL A
TYP, SCALE: 25X

NOTES: UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN MILLIMETERS

- FOR LEAD FINISH THICKNESS AND COMPOSITION, SEE "SOLDER INFORMATION" IN THE PACKAGING SECTION OF THE NATIONAL SEMICONDUCTOR WEB PAGE (www.national.com).
- DIMENSION DOES NOT INCLUDE MOLD PROTRUSION. MAXIMUM ALLOWABLE MOLD PROTRUSION 0.25mm PER SIDE.
- REFERENCE JEDEC REGISTRATION MS-026, VARIATION BFB.

APPROVALS		DATE	National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090	
DRAWN	MARTA SUCHY	03/14/2003		
DFTG. CHK.	MARTA SUCHY	03/14/2003	LQFP, 20x20x1.4mm BODY, 128 LD, 0.5mm PITCH, EXPOSED PAD	
ENGR. CHK.	RANDY WALBERG	03/14/2003		
PROJECTION		SCALE	SIZE	DRAWING NUMBER
MM		NTS	B	(SC)MKT-VNX128A
FORMERLY: N/A		SHEET 1 of 1		