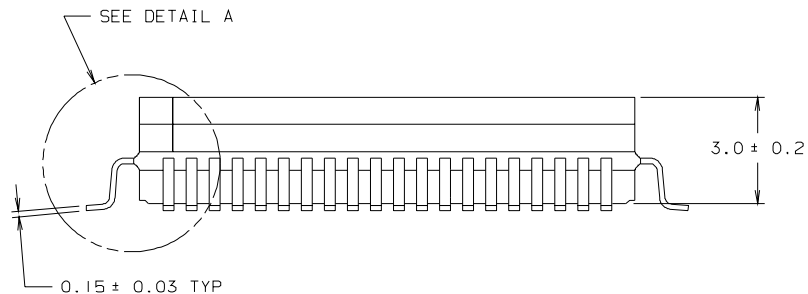
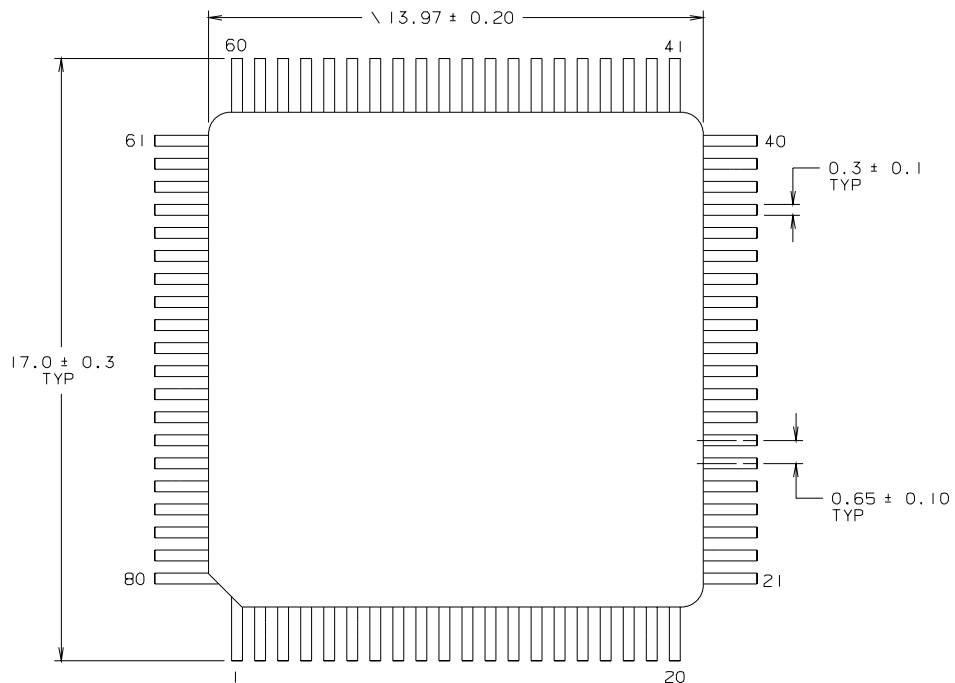


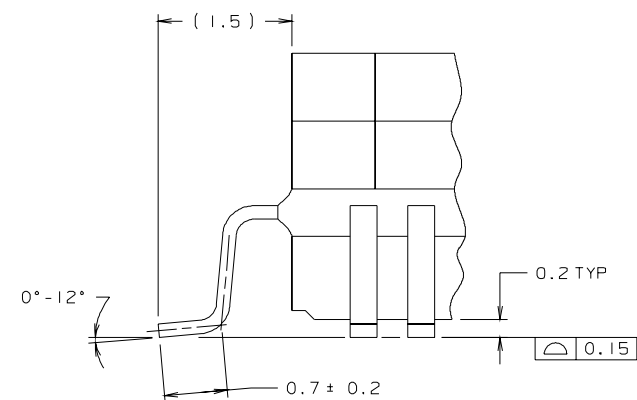
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	10117	12/10/93	TL/SL
B	ADD 3.0±0.2; 0.2 WAS 0.20; 0.3±0.1 WAS 0.30±0.10	10332	04/04/94	TL/



DIMENSIONS ARE IN MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

1. STANDARD LEAD FINISH:
1.52 MICROMETERS MINIMUM GOLD THICKNESS ON ALLOY 42.
2. FOR PROTOTYPE ENGINEERING USE ONLY.
3. NO JEDEC REGISTRATION AS OF 9/24/93.



DETAIL A
TYPICAL

APPROVALS	DATE	NATIONAL SEMICONDUCTOR CORPORATION		
DRAWN T. LEQUANG	12/10/93	2900 Semiconductor Drive, Santa Clara, CA 95052-8090		
DFTG. CHK.		CERQUAD, 14 X 14 X 3.0 mm, 80 LEAD		
ENGR. CHK.				
APPROVAL				
PROJECTION		SCALE	SIZE	DRAWING NUMBER
		N/A	C	MKT-WKV80A
DO NOT SCALE DRAWING		SHEET 1 OF 1		