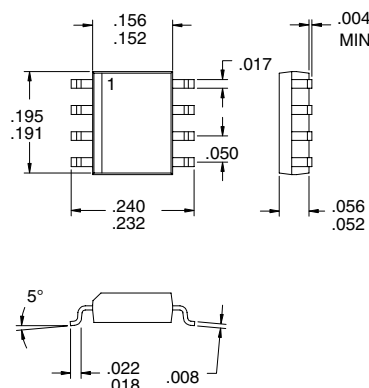


Typical Applications

- CATV Distribution Amplifiers
- Cable Modems
- Broadband Gain Blocks
- Laser Diode Driver
- Return Channel Amplifier
- Base Stations

Product Description

The RF2312 is a general purpose, low cost high linearity RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily cascadable 75Ω gain block. The gain flatness of better than 0.5dB from 5MHz to 1000MHz, and the high linearity, make this part ideal for cable TV applications. Other applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to 2500MHz. The device is self-contained with 75Ω input and output impedances, and requires only two external DC biasing elements to operate as specified.



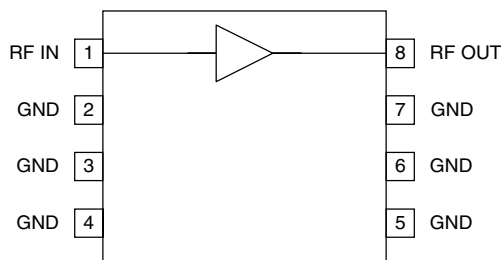
Optimum Technology Matching® Applied

- | | | |
|-------------------------------------|--|--------------------------------------|
| <input type="checkbox"/> Si BJT | <input checked="" type="checkbox"/> GaAs HBT | <input type="checkbox"/> GaAs MESFET |
| <input type="checkbox"/> Si Bi-CMOS | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si CMOS |

Package Style: SOP-8

Features

- DC to well over 2500MHz Operation
- Internally Matched Input and Output
- 15dB Small Signal Gain
- 3.8dB Noise Figure
- +20dBm Output Power
- Single 5V to 12V Positive Power Supply



Functional Block Diagram

Ordering Information

RF2312	Linear General Purpose Amplifier
RF2312 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc.
7625 Thorndike Road
Greensboro, NC 27409, USA

Tel (336) 664 1233
Fax (336) 664 0454
<http://www.rfmd.com>

Absolute Maximum Ratings

Parameter	Rating	Unit
Device Current	125	mA
Input RF Power	+18	dBm
Output Load VSWR	20:1	
Ambient Operating Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



Caution! ESD sensitive device.

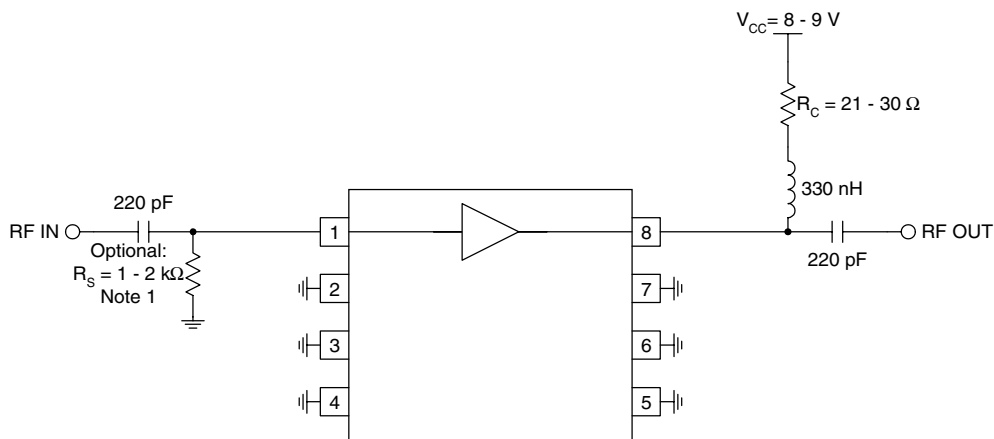
RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall (50Ω)					T=25 °C, V _{CC} =9V, Freq=900 MHz, R _C =30Ω, 50Ω System, P _{IN} =-4 dBm 3dB Bandwidth
Frequency Range		DC to 2500		MHz	
Gain	14.5	15.1		dB	
Noise Figure		3.8	4.3	dB	From 50MHz to 300MHz, -30 to +70 °C
		4.2	4.8	dB	From 300MHz to 1000MHz, -30 to +70 °C
Input VSWR			1.7:1		Appropriate values for the DC blocking capacitors and bias inductor are required to maintain this VSWR at the intended operating frequency range.
Output VSWR			1.4:1		Appropriate values for the DC blocking capacitors and bias inductor are required to maintain this VSWR at the intended operating frequency range.
Output IP ₃	+40	+42		dBm	At 100MHz
Output IP ₃	+33	+36		dBm	At 500MHz
Output IP ₃	+30	+33		dBm	At 900MHz
Output P _{1dB}	+21	+22		dBm	At 100MHz
Output P _{1dB}	+20	+21		dBm	At 500MHz
Output P _{1dB}	+17	+18.5		dBm	At 900MHz
Saturated Output Power		+23		dBm	At 100MHz
Saturated Output Power		+22.5		dBm	At 500MHz
Saturated Output Power		+20.5		dBm	At 900MHz
Reverse Isolation		20		dB	
Thermal					
Theta _{JC}		73		°C/W	P _{DISS} =0.6W, T _{AMB} =85°C
Maximum junction temperature		138		°C	
Mean Time Between Failures		1.1x10 ⁶		years	T _{AMB} =+85°C
Mean Time Between Failures		3.2x10 ³		years	T _{AMB} =+25°C
Power Supply					
Device Voltage (V _D)		6.0		V	On pin 8, I _{CC} =100mA
		5.0		V	On pin 8, I _{CC} =40mA
Operating Current Range	85	100	115	mA	V _{CC} =9.0V, R _C =30Ω

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall (75Ω)					T=25 °C, V _{CC} =9V, Freq = 900 MHz, R _C =30Ω, 75Ω System
Frequency Range		DC to 2500		MHz	3dB Bandwidth
Gain	15.5	16		dB	
Noise Figure		3.8	4.3	dB	From 50MHz to 300MHz, -30 to +70 °C
		4.2	4.8	dB	From 300MHz to 1000MHz, -30 to +70 °C
Input VSWR		1.3:1	1.4:1		From 50MHz to 900MHz, -30 to +70 °C. Appropriate values for the DC blocking capacitors and bias inductor are required to maintain this VSWR at the intended operating frequency range.
Output VSWR			1.25:1		From 50MHz to 300MHz, -30 to +70 °C. Appropriate values for the DC blocking capacitors and bias inductor are required to maintain this VSWR at the intended operating frequency range.
			1.4:1		From 300MHz to 500MHz, -30 to +70 °C.
		1.5:1	1.7:1		From 500MHz to 900MHz, -30 to +70 °C.
Output IP ₃	+36	+38		dBm	At 100MHz
Output IP ₃	+33	+36		dBm	At 500MHz
Output IP ₃	+28	+30		dBm	At 900MHz
Output P _{1dB}	+21	+22		dBm	At 100MHz
Output P _{1dB}	+20	+21		dBm	At 500MHz
Output P _{1dB}	+17	+18.5		dBm	At 900MHz
Saturated Output Power		+23		dBm	At 100MHz
Saturated Output Power		+22.5		dBm	At 500MHz
Saturated Output Power		+20.5		dBm	At 900MHz
Reverse Isolation		20		dB	
CATV					110 Channels, 10dBmV/channel at input
CSO		>86		dBc	61.25MHz
		>86		dBc	83.25MHz
		76		dBc	193.25MHz
		70		dBc	313.2625MHz
		64		dBc	547.25MHz
CTB		84		dBc	61.25MHz
		86		dBc	83.25MHz
		85		dBc	193.25MHz
		81		dBc	313.2625MHz
		80		dBc	547.25MHz
Cross Modulation		77		dBc	61.25MHz
		74		dBc	445.25MHz
CNR	65	66		dB	
					77 Channels to 550MHz at 10dBmV, 33 channels to 760MHz at 0dBmV flat at DUT input
CSO		>86		dBc	61.25MHz
		>86		dBc	83.25MHz
		76		dBc	193.25MHz
		72		dBc	313.2625MHz
		64		dBc	547.25MHz
CTB		>86		dBc	61.25MHz
		>86		dBc	83.25MHz
		86		dBc	193.25MHz
		84		dBc	313.2625MHz
		83		dBc	547.25MHz
CNR	65	66		dB	

Pin	Function	Description	Interface Schematic
1	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in all applications. The device has internal feedback, and not using a DC blocking capacitor will disable the temperature compensation. The bias of the device can be controlled by this pin. Adding an optional 1 k Ω resistor to ground on this pin reduces the bias level, which may be compensated for by a higher supply voltage to maintain the appropriate bias level. The net effect of this is an increased output power capability, as well as higher linearity for signals with high crest factors. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	
2	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance. Each ground pin should have a via to the ground plane.	
3	GND	Same as pin 2.	
4	GND	Same as pin 2.	
5	GND	Same as pin 2.	
6	GND	Same as pin 2.	
7	GND	Same as pin 2.	
8	RF OUT	RF output and bias pin. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. For biasing, an RF choke in series with a resistor is needed. The value for the resistor R_C is 30 Ω (0.5W) for $V_{CC}=9V$ and 21 Ω for $V_{CC}=8V$. The DC voltage on this pin is typically 6.0V with a current of 100mA. In lower power applications the value of R_C can be increased to lower the current and V_D on this pin.	

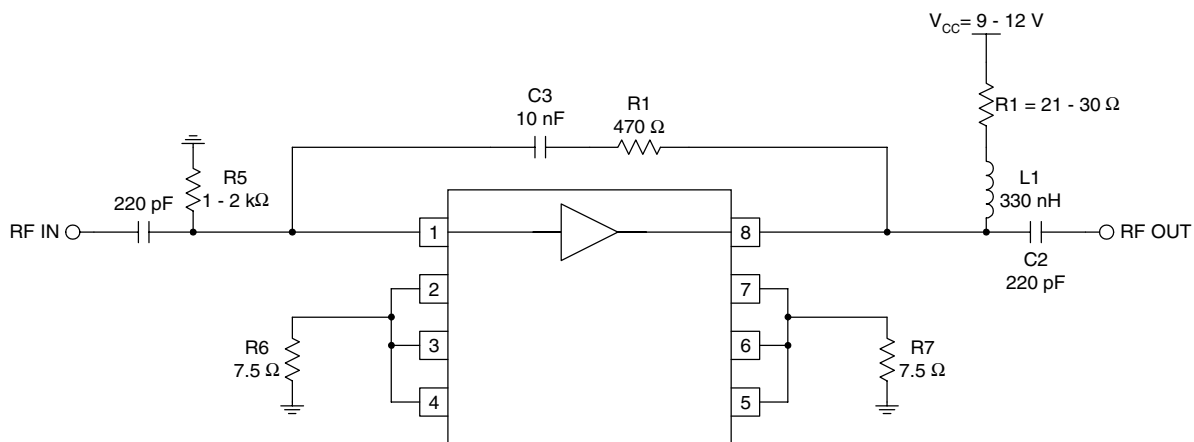
Application Schematic 5MHz to 50MHz Reverse Path



NOTE 1:

Optional resistor R_S can be used to maintain the correct bias level at higher supply voltages. This is useful to increase output capability or linearity for signals with high crest factors.

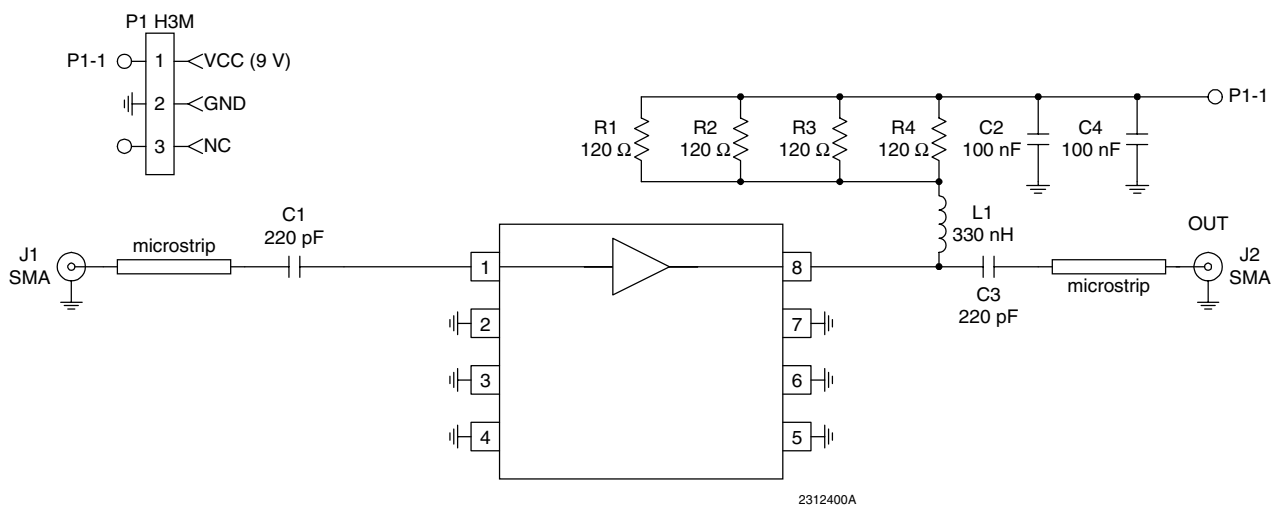
Application Schematic 10dB Gain



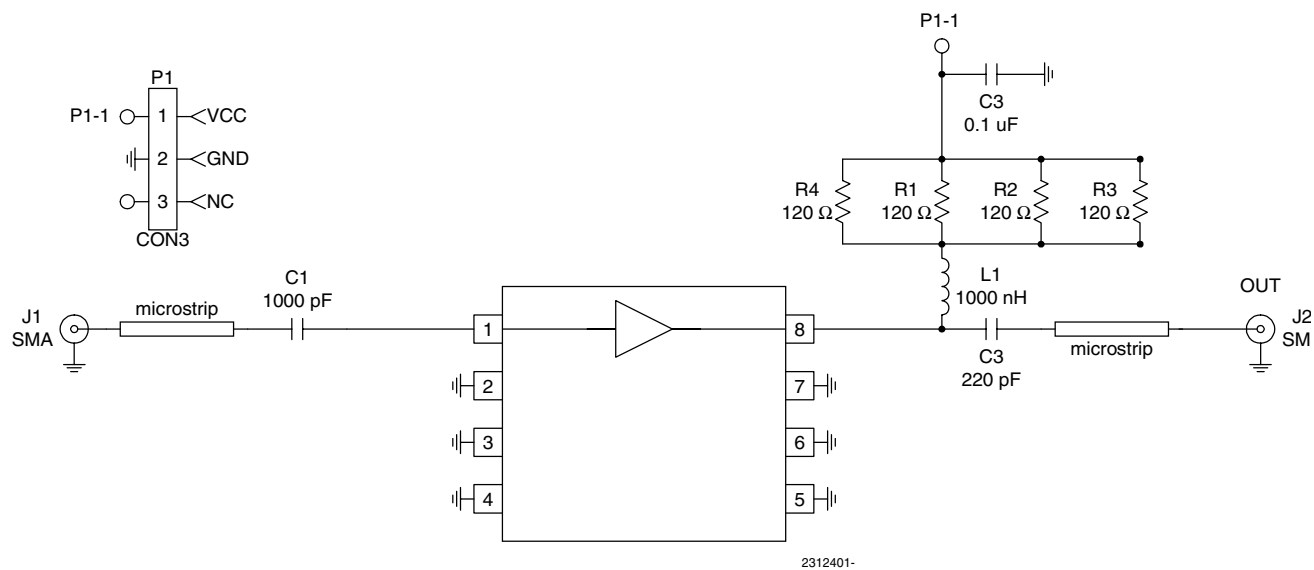
R_5 is used to maintain the correct bias level at higher supply voltages and is also required in this configuration. The RC feedback network of R_1 and C_3 should be kept physically as short as possible. R_1 can be adjusted as required to improve the input and output impedance matching. R_6 and R_7 reduce the typical gain by increasing the emitter resistance. L_1 should be at least 200Ω reactive at the lowest operating frequency. C_1 and C_2 should be less than 10Ω at the lowest operating frequency.

Evaluation Board Schematic - 50Ω

(Download [Bill of Materials](http://www.rfmd.com) from www.rfmd.com.)

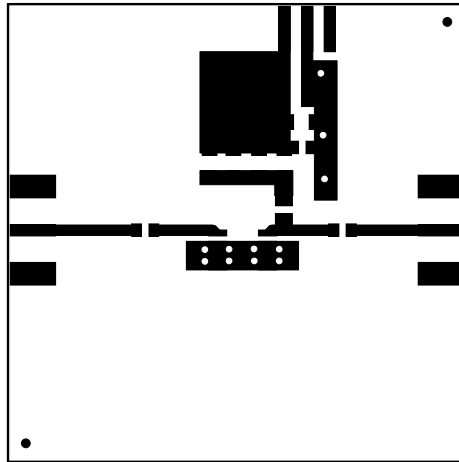
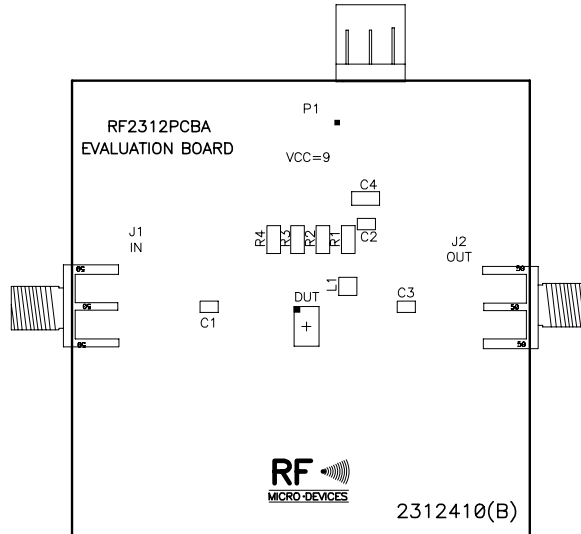


Evaluation Board Schematic - 75Ω



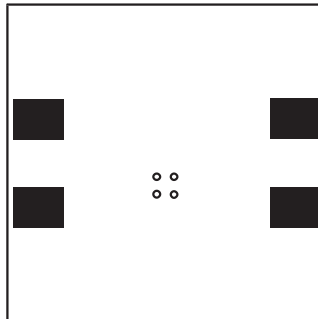
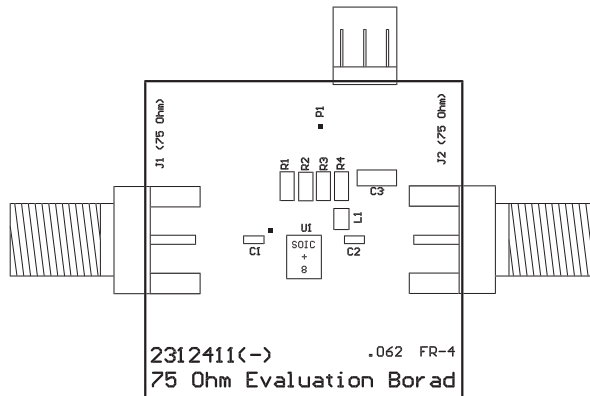
Evaluation Board Layout - 50Ω 2.02" x 2.02"

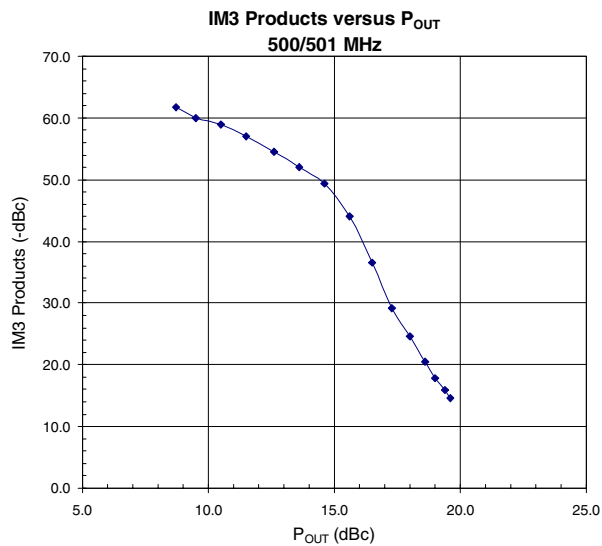
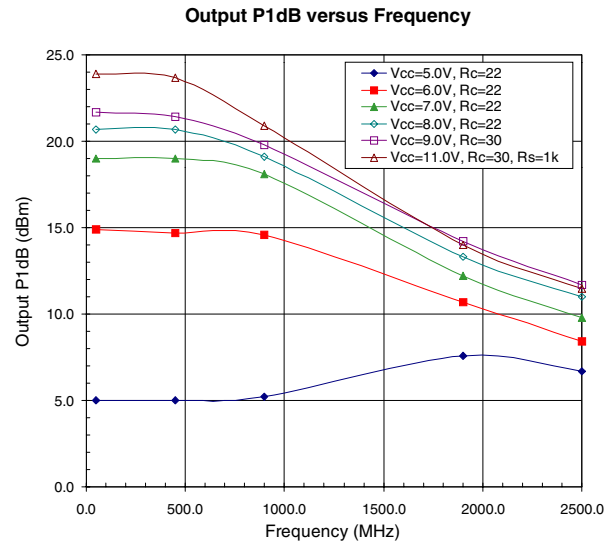
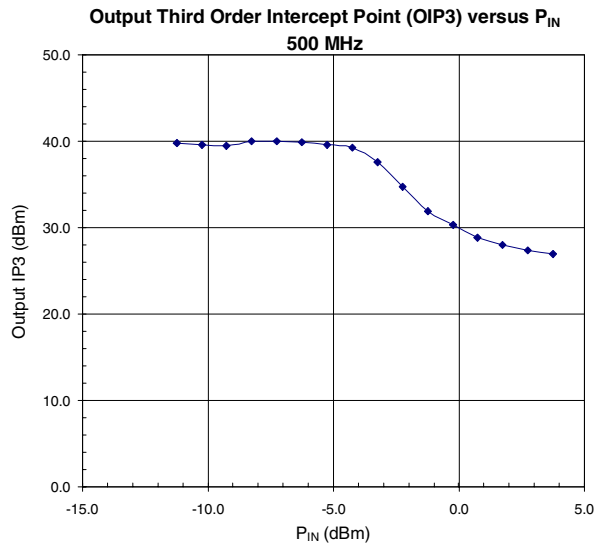
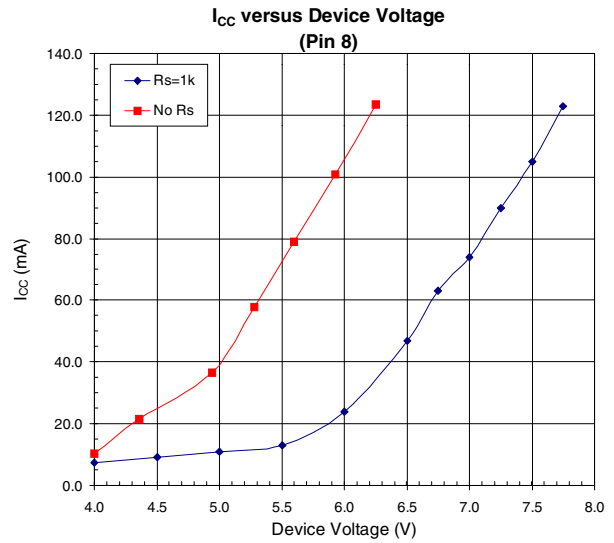
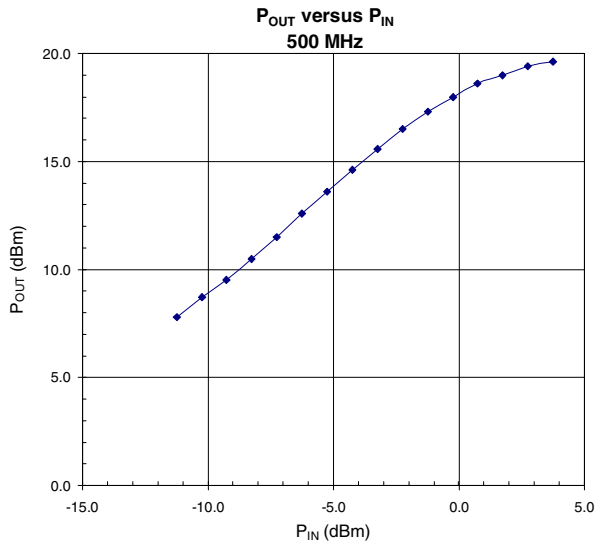
Board Thickness 0.031", Board Material FR-4

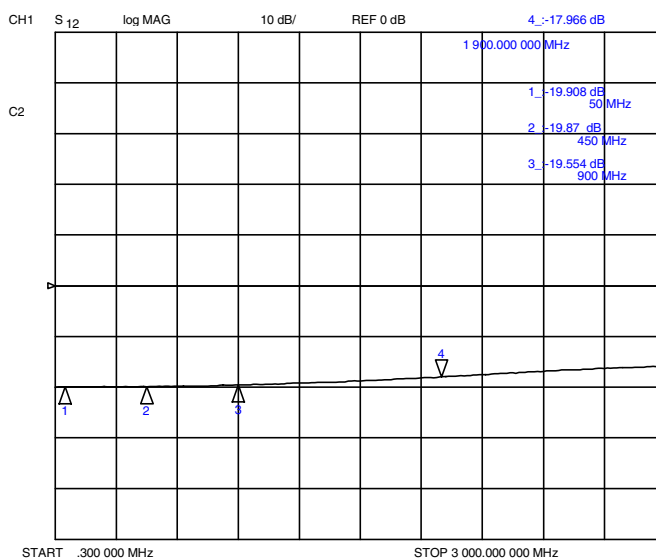
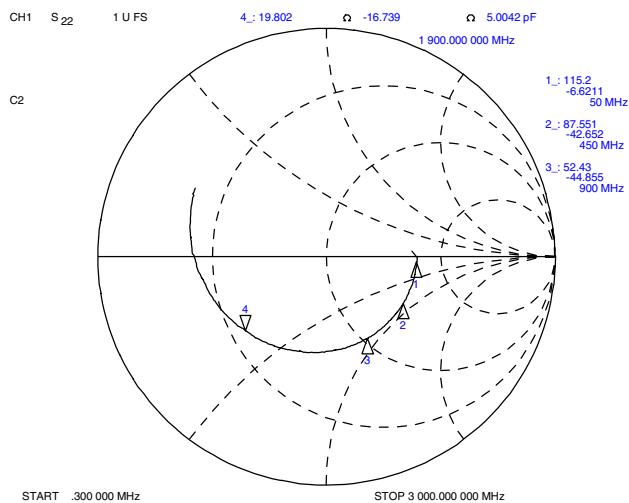
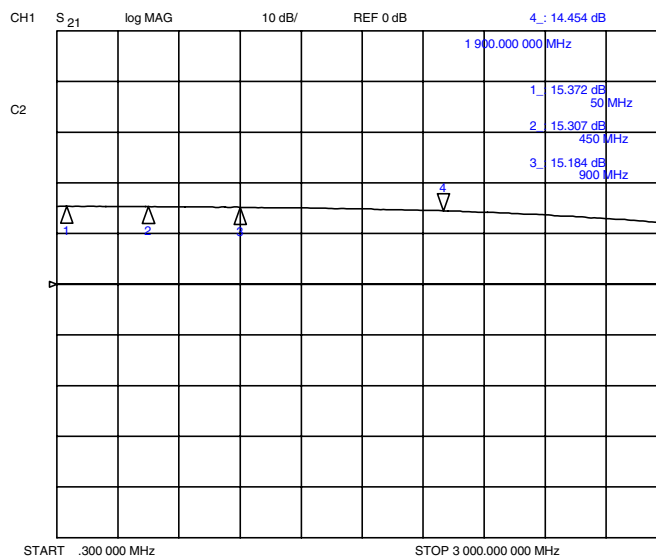
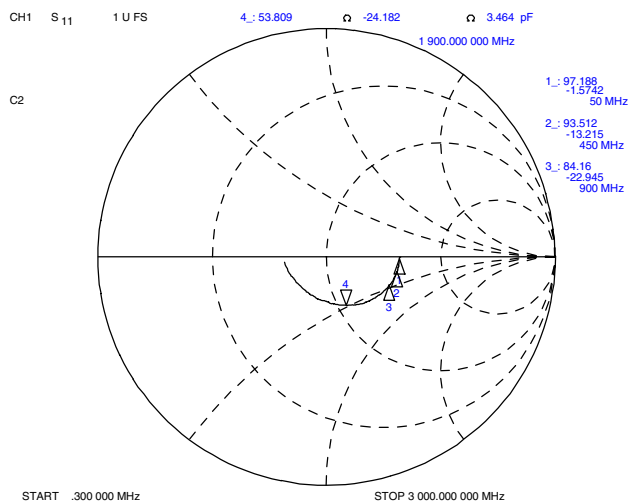


Evaluation Board Layout - 75Ω 2.02" x 2.02"

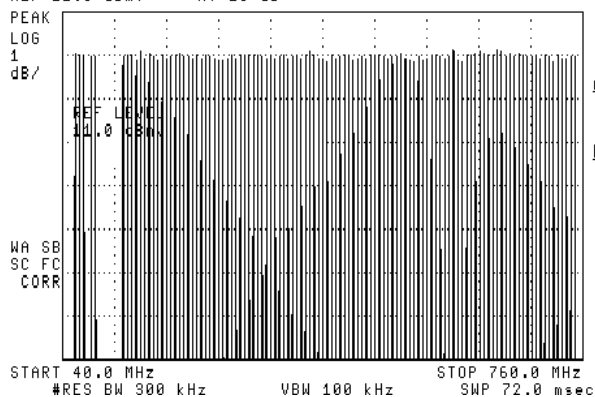
Board Thickness 0.062", Board Material FR-4



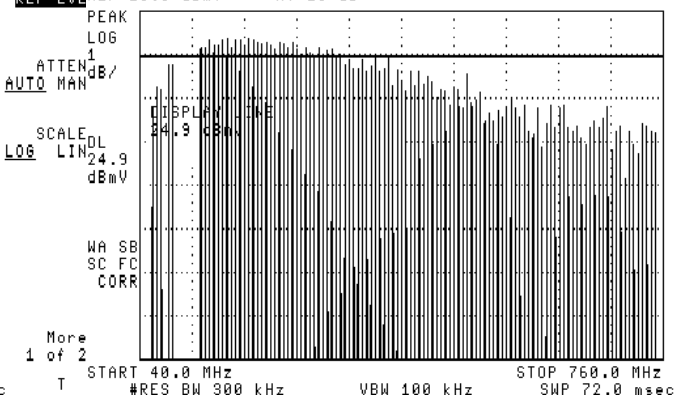




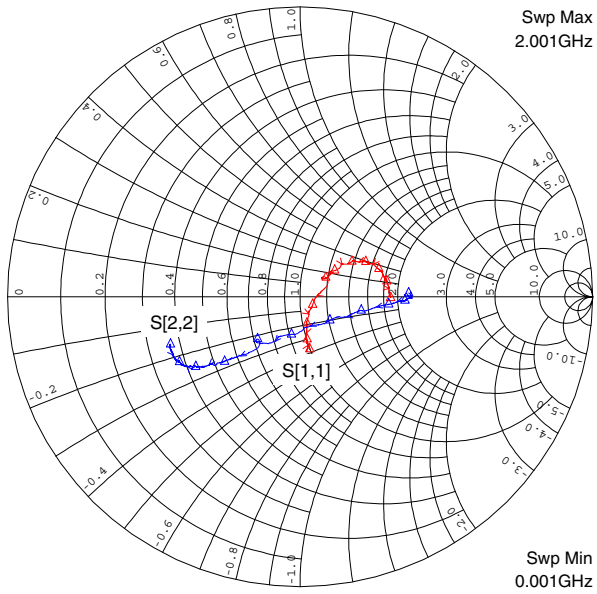
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Matrix Generator Output
REF 11.0 dBmV AT 10 dB



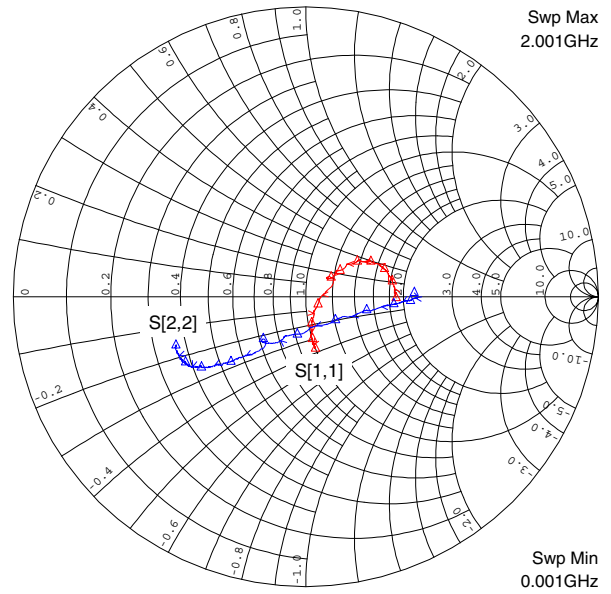
21:57:12 JUL 23, 1998
Amplifier Output at +25 dBmV
REF LEVEL 26.0 dBmV AT 10 dB



75 Ohms, ICC = 100 mA, Temp = 25°C



75 Ohms, ICC = 110 mA, Temp = 25°C



RF2312